Tutorial on Separation Logic

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Outline

► Part I : Fluency, Examples

► Part II : Model Theory

► Part III : Proof Theory



- ▶ 2000's: impressive practical advances in automatic program verification E.g.
 - SLAM: Protocol properties of procedure calls in device drivers, e.g. any call to ReleaseSpinLock is preceded by a call to AquireSpinLock
 - ASTRÉE: no run-time errors in Airbus code



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 - ASTRÉE assumes: no dynamic pointer allocation
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- ► In some (distant?) future: automatically crash-proof Apache, OpenSSL...



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- ► In some (distant?) future: automatically crash-proof Apache, OpenSSL...
- ▶ a possible motivation, not the motivation for separation logic



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Part 1

Fluency, Examples

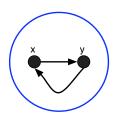
Sources

- O'Hearn-Reynolds-Yang, CSL'01: Local reasoning about programs that alter data structures
- Reynolds, LICS'02: Separation Logic: A logic for shared mutable data structure.
- ► Hoarefest'00 paper of Reynolds, POPL'01 paper of Ishtiaq-O'Hearn, BSL'99 paper of O'Hearn-Pym, MI'72 paper of Burstall



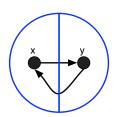
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xI->y * yI-> x



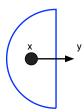


$$xI->y * yI-> x$$



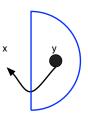






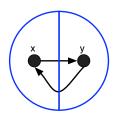






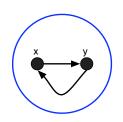


$$xI->y * yI-> x$$





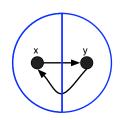
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$$xI->y * yI-> x$$

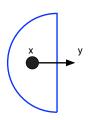








xl->y

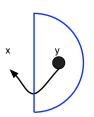








yl-> x



x=10

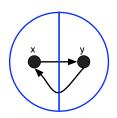
y=42

42

10



$$xI->y * yI-> x$$





Heaplets (heap portions) as possible worlds (i.e., a kind of modal logic)

- Add to Classical Logic:
 - ▶ emp : "the heaplet is empty"
 - $x \mapsto y$: "the heaplet has exactly one cell x, holding y"
 - ▶ A * B: "the heaplet can be divided so A is true of one partition and B of the other".



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- ► Add inductive definitions , and other more exotic things ("magic wand", "septraction") as well.
- Standard model: RAM model

heap: $N \rightarrow_f Z$

and lots of variations (records, permissions, ownership... more later).



A Substructural Logic

$$A \not\vdash A*A$$

$$10 \mapsto 3 \not\vdash 10 \mapsto 3 * 10 \mapsto 3$$

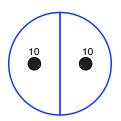
$$A*B \not\vdash A$$

$$10 \mapsto 3 * 42 \mapsto 5 \not\vdash 10 \mapsto 3$$



An inconsistency: trying to be two places at once

101->3 * 101->3





$$\{(x \mapsto -) * P\} [x] := 7 \{(x \mapsto 7) * P\}$$



```
\{(x \mapsto -) * P\} [x] := 7 \{(x \mapsto 7) * P\}
\{\text{true}\} [x] := 7 \{??\}
```



$$\{(x \mapsto -) * P\} [x] := 7 \{(x \mapsto 7) * P\}$$

 $\{\text{true}\} [x] := 7 \{??\}$
 $\{P * (x \mapsto -)\} \text{ dispose}(x) \{P\}$



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\{(x \mapsto -) * P\} \ [x] := 7 \ \{(x \mapsto 7) * P\}
\{\text{true}\} \ [x] := 7 \ \{??\}
\{P * (x \mapsto -)\} \ \text{dispose}(x) \ \{P\}
\{\text{true}\} \ \text{dispose}(x) \ \{??\}
```

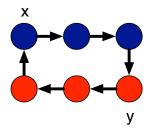


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\{(x \mapsto -) * P\} [x] := 7 \{(x \mapsto 7) * P\}
\{true\} [x] := 7 \{??\}
\{P*(x\mapsto -)\}\ dispose(x)\ \{P\}
\{true\}\ dispose(x)\ \{??\}
\{P\} x = cons(a, b) \{P * (x \mapsto a, b)\} \{x \notin free(P)\}
```



List segments (list(E) is shorthand for lseg(E, nil)) $|seg(E,F)| \iff \text{if } E=F \text{ then emp}$ $else \exists y.E \mapsto tl: y* |seg(y,F)|$

$$\mathsf{lseg}(x,y) * \mathsf{lseg}(y,x)$$

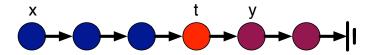




List segments (list(
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$$lseg(E,F) \iff if E = F then emp$$

$$else \exists y.E \mapsto tl: y * lseg(y,F)$$

$$lseg(x, t) * t \mapsto [tl: y] * list(y)$$





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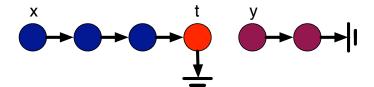
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Entailment
$$lseg(x, t) * t \mapsto [tl: y] * list(y) \vdash list(x)$$



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In-place reasoning and Inductive Definitions

Example Inductive Definition:

```
\mathsf{tree}(E) \iff \mathsf{if} \ E = \mathsf{nil} \ \mathsf{then} \ \mathsf{emp}
\mathsf{else} \ \exists x, y. \ (E \mapsto \mathsf{l} : x, r : y) \ * \ \mathsf{tree}(x) \ * \ \mathsf{tree}(y)
```

Example Proof:

```
\{ tree(p) \land p \neq nil \}
i := p \rightarrow l; \quad j := p \rightarrow r;
dispose(p);
\{ tree(i) * tree(j) \}
```



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Example Proof:

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\{ \operatorname{tree}(p) \land p \neq \operatorname{nil} \}
\{ (p \mapsto l : x', r : y') * \operatorname{tree}(x') * \operatorname{tree}(y') \}
i := p \mapsto l; \quad j := p \mapsto r;
\operatorname{dispose}(p);
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In-place reasoning and Inductive Definitions

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Example Proof:

```
 \begin{aligned} &\{\mathsf{tree}(p) \land p \neq \mathsf{nil}\} \\ &\{(p \mapsto l \colon x', r \colon y') * \mathsf{tree}(x') * \mathsf{tree}(y')\} \\ &i \coloneqq p \mapsto l; \ j \coloneqq p \mapsto r; \\ &\{(p \mapsto l \colon i, r \colon j) * \mathsf{tree}(i) * \mathsf{tree}(j)\} \\ &\mathsf{dispose}(p); \\ &\{\mathsf{tree}(i) * \mathsf{tree}(j)\} \end{aligned}
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\mathsf{else} \ \exists x, y. \ (E \mapsto \mathsf{l} : x, r : y) \ * \ \mathsf{tree}(x) \ * \ \mathsf{tree}(y)
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Example Proof:



```
▶ Spec
{tree(p)} DispTree(p) {emp}
```

```
{tree(i)*tree(j)}
DispTree(i);
{emp * tree(j)}
DispTree(j);
```

$$\frac{\{P\}C\{Q\}}{\{P*R\}C\{Q*R\}}$$
 Frame Rule



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```
▶ procedure DispTree(p)
local i, j;
if p \neq \text{nil then}
i = p \rightarrow l; j := p \rightarrow r;
DispTree(i);
DispTree(j);
dispose(p)
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An Unhappy Attempt to Specify

```
\{tree(p) \land reach(p, n)\}\
DispTree(p)
\{\neg allocated(n)\}
```



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```

► An Unfortunate Fix

```
\{\mathsf{tree}(p) \land \mathsf{reach}(p, n) \\ \land \neg \mathsf{reach}(p, m) \land \mathsf{allocated}(m) \land m.f = m' \land \neg \mathsf{allocated}(q) \} \\ \mathsf{DispTree}(p) \\ \{\neg \mathsf{allocated}(n) \\ \land \neg \mathsf{reach}(p, m) \land \mathsf{allocated}(m) \land m.f = m' \land \neg \mathsf{allocated}(q) \} \}
```

An unhappy proof

```
\{ def?(p.tl) \land
     \exists j. list([l_{j+1}, \ldots, l_n], p.tl, tl \oplus p \mapsto \Omega) \land
     \bigwedge_{k=1}^{j} \neg def?(l_k.(tl \oplus p \mapsto \Omega))
    q := p;
\{ def?(p.tl) \wedge def?(q.tl) \wedge \}
     \exists j. list([l_{i+1}, \ldots, l_n], p.tl, tl \oplus q \mapsto \Omega) \land
     \bigwedge_{k=1}^{j} \neg def?(l_k.(tl \oplus q \mapsto \Omega))
    p := p.t1;
\{ def?(q.tl) \land
     \exists j. list([l_{j+1}, \ldots, l_n], p, tl \oplus q \mapsto \Omega) \land
     \bigwedge_{k=1}^{j} \neg def?(l_k.(tl \oplus q \mapsto \Omega))
\{ def?(a.tl) \land
     (\exists j. list([l_{j+1}, \ldots, l_n], p, tl) \land
     \bigwedge_{k=1}^{j} \neg def?(l_k.tl))[\Omega/q.tl]
    dispose(q);
\{\exists j. list([l_{i+1},\ldots,l_n],p,tl) \land \bigwedge_{k=1}^{j} \neg def?(l_k.tl)\}
```



```
▶ Spec
{tree(p)} DispTree(p) {emp}
```



Main Points

- * lets you do in-place reasoning
- * interacts well with inductive definitions
- powerful way to avoid writing frame axioms



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- * lets you do in-place reasoning
- * interacts well with inductive definitions
- powerful way to avoid writing frame axioms
- Pre/post specs tied to footprint (describe "local surgeries")



$$\frac{\{P_1\}C_1\{Q_1\} - \{P_2\}C_2\{Q_2\}}{\{P_1 * P_2\}C_1 \parallel C_2\{Q_1 * Q_2\}}$$

```
Prog ::= x:= E \mid x:= [E] \mid [E]:= F

\mid x:= cons(E_1, ..., E_n) \mid dispose(E)

\mid skip \mid C; C \mid if B then C else C

\mid while B do C
```



$$\frac{\{P_1\}\,C_1\{\,Q_1\}\quad \{P_2\}\,C_2\{\,Q_2\}}{\{\,P_1*\,P_2\}\,C_1\parallel C_2\{\,Q_1*\,Q_2\}}$$

We can't prove racy programs like

$$\begin{cases}
10 \mapsto - \\
[10] := 42 \parallel [10] := 6 \\
\{?? \}
\end{cases}$$



$$\frac{\{P_1\}C_1\{Q_1\} \quad \{P_2\}C_2\{Q_2\}}{\{P_1*P_2\}C_1 \parallel C_2\{Q_1*Q_2\}}$$

We can't prove racy programs like

$$\begin{cases}
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[10] := 42 \parallel [10] := 6 \\
\{??\}
\end{cases}$$

We cannot send 10 to both processes in their preconditions, since

$$(10 \mapsto -) * (10 \mapsto -)$$

is false. But...



$$\frac{\{P_1\}C_1\{Q_1\} \quad \{P_2\}C_2\{Q_2\}}{\{P_1*P_2\}C_1 \parallel C_2\{Q_1*Q_2\}}$$

Preconditions can pick out race-free start-states, when they exist:



$$\frac{\{P_1\}C_1\{Q_1\} - \{P_2\}C_2\{Q_2\}}{\{P_1*P_2\}C_1 \parallel C_2\{Q_1*Q_2\}}$$

Preconditions can pick out race-free start-states, when they exist:

That 'proof figure" is an annotation form for

$$\frac{\{x \mapsto 3\} [x] := 4 \{x \mapsto 4\} \qquad \{y \mapsto 3\} [y] := 5 \{y \mapsto 5\}}{\{x \mapsto 3 * y \mapsto 3\} [x] := 4 \|_{27} [y] := 5 \{x \mapsto 4 * y \mapsto 5\}}$$



Racy programs and phantom blocks

▶ Brookes's theorem: proven programs are race free



Racy programs and phantom blocks

- ▶ Brookes's theorem: proven programs are race free
- ▶ To deal with racy programs, need to be explicit about granularity:

```
(with phantom do [10]:= 3) \parallel (with phantom do [10]:= 42)
```



```
\begin{aligned} & \{ \mathit{array}(a,i,j) \} \\ & \mathsf{procedure} \ \mathsf{ms}(a,i,j) \\ & \mathsf{newvar} \ m := (i+j)/2; \\ & \mathsf{if} \quad i < j \ \mathsf{then} \\ & \quad \left( \mathsf{ms}(a,i,m) \ \| \ \mathsf{ms}(a,m+1,j) \right); \\ & \quad \mathsf{merge}(a,i,m+1,j); \\ & \{ \mathit{sorted}(a,i,j) \} \end{aligned}
```



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```

Can't prove with disjoint concurrency rule

$$\frac{\{P\}C\{Q\} \quad \{P'\}C'\{Q'\}}{\{P \land P'\}C \parallel C'\{Q \land Q'\}}$$

where C does not modify any variables free in P', C', Q', and conversely. Because: Hoare logic treats an assignment to an array component as an assignment to the whole array.

```
 \begin{aligned} & \{ \mathit{array}(a,i,j) \} \\ & \mathsf{procedure} \ \mathsf{ms}(a,i,j) \\ & \mathsf{newvar} \ m := (i+j)/2; \\ & \mathsf{if} \quad i < j \ \mathsf{then} \\ & \quad \left( \mathsf{ms}(a,i,m) \ \| \ \mathsf{ms}(a,m+1,j) \right); \\ & \quad \mathsf{merge}(a,i,m+1,j); \\ & \{ \mathit{sorted}(a,i,j) \} \end{aligned}
```

- ► To prove with invariants+preservation, you track many irrelevant interleavings
 - and... state complex recursion hypothesis



```
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```

- ➤ To prove with rely/guarantee, you complicate the spec (not just the reasoning)
 - ▶ Rely: no one else touches my segment
 - Guarantee: I only touch my own segment (frame axiom)



In Separation Logic¹

We just use the given pre/post spec.

```
 \begin{cases} \textit{array}(\textit{a},\textit{i},\textit{m}) * \textit{array}(\textit{a},\textit{m}+1,\textit{j}) \} \\ \{\textit{array}(\textit{a},\textit{i},\textit{m})\} & \{\textit{array}(\textit{a},\textit{m}+1,\textit{j})\} \\ \texttt{ms}(\textit{a},\textit{i},\textit{m}) & \| \texttt{ms}(\textit{a},\textit{m}+1,\textit{j}) \} \\ \{\textit{sorted}(\textit{a},\textit{i},\textit{m}) * \textit{sorted}(\textit{a},\textit{m}+1,\textit{j})\} \end{cases}
```

Concurrency proof rule:

$$\frac{\{P_1\}C_1\{Q_1\} \quad \{P_2\}C_2\{Q_2\}}{\{P_1*P_2\}C_1 \parallel C_2\{Q_1*Q_2\}}$$



 $^{^{1}}a[i]$ is sugar for [a+i] in RAM model

Part II

Model Theory

Sources:

▶ Papers of Calcagno, O'Hearn, Pym, Yang



$$\circ: H \times H \rightarrow H$$
 , $e \in H$



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- Particularly. RAM model (lots of others possible)
 - $\vdash H = N \rightarrow_f Z$
 - union of functions with disjoint domain, undefined when overlapping domains
 - e = empty partial function



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 - $\blacktriangleright H = N \rightarrow_f Z$
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- ▶ An order $h_1 \sqsubseteq h_3$



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 - **▶ General**: $\exists h_2. h_1 \circ h_2 = h_3$



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 - e = empty partial function
- ▶ An order $h_1 \sqsubseteq h_3$
 - **▶ General**: $\exists h_2. h_1 \circ h_2 = h_3$
 - ▶ Particular: $h_1 \subseteq h_3$



▶ We can lift \circ : $H \times H \to H$ to *: $\mathcal{P}(H) \times \mathcal{P}(H) \to \mathcal{P}(H)$ $h \in A * B$ iff $\exists h_A, h_B. h = h_A \circ h_B$ and $h_A \in A$ and $h_B \in B$



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 - ▶ $(\mathcal{P}(H), *, emp)$ is a *total* commutative monoid



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 - $(\mathcal{P}(H), *, emp)$ is a *total* commutative monoid
- $\triangleright \mathcal{P}(H)$ is (in the subset order) both
 - A Boolean Algebra, and
 - A Residuated Monoid

$$A * B \subset C \Leftrightarrow A \subset B \twoheadrightarrow C$$



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 iff $\exists h_A, h_B, h = h_A \circ h_B$ and

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 - A Boolean Algebra, and
 - A Residuated Monoid

$$A * B \subset C \Leftrightarrow A \subset B \twoheadrightarrow C$$

cf. Boolean BI logic (O'Hearn, Pym)



Models of Programs

Program = while programs with

$$[e]:=e'$$
 $x:=[e]$ $x:=new(e_1,\ldots,e_n)$ dispose (x)

- We represent a program as a transition system
- ► Each program Prog determines a set of (finite, nonempty) traces

$$h_1 \cdot \cdot \cdot h_n$$

possibly terminated with a special state

$$h_1 \cdots h_n$$
Error



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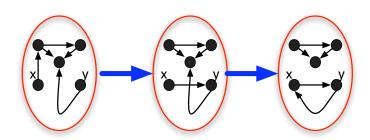
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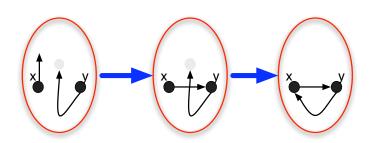
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► These transition systems/traces have special structure

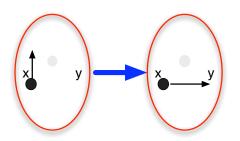




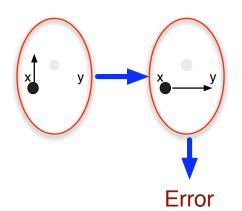














Footprint Theorem

- 1. Recall order on states $h \sqsubseteq h'$.
- 2. Extend pointwise to traces, $t \sqsubseteq t'$

```
h_1 \sqsubseteq h'_1
\vdots
h_n \sqsubseteq h'_n
```

- 3. Notes: requires traces of same length; $Error \sqsubseteq only itself$.
- 4. **Footprint Theorem** If t is a trace of program Prog, then there is a smallest $t_f \sqsubseteq t$ where t_f is a trace of Prog



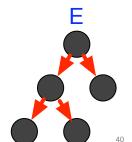
$$\mathsf{tree}(E) \iff if \ E=nil \ then \ \mathsf{emp}$$

$$else \ \exists x,y. \ (E\mapsto l: x,r: y) \ * \ \mathsf{tree}(x) \ * \ \mathsf{tree}(y)$$



$$tree(E) \iff if E=nil then emp$$
 $else \exists x, y. (E \mapsto l: x, r: y) * tree(x) * tree(y)$

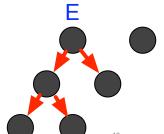
▶ tree(E) is true of





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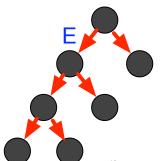
▶ tree(E) is false of





 $tree(E) \iff if E=nil then emp$ else $\exists x, y. (E \mapsto I: x, r: y) * tree(x) * tree(y)$

and even false of





Small Specs (only talk about footprint)

We saw

```
\{tree(p)\}\ DispTree(p)\ \{emp\}
```



Small Specs (only talk about footprint)

We saw

$$\{tree(p)\}\ DispTree(p)\ \{emp\}$$

and we could have given

$$\{E \mapsto -\} [E] = b \{E \mapsto b\}$$

$$\{emp\} x = new(y, z) \{x \mapsto y, z\}$$

$$\{E \mapsto -\} dispose(E) \{emp\}$$



▶ Frame Theorem: If t is a trace of program Prog and $t \sqsubseteq t'$ then t' is a trace of Prog



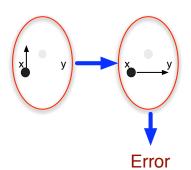
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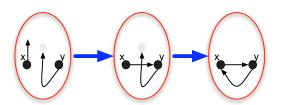
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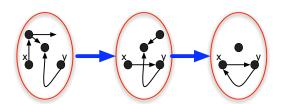


► Frame Theorem: If t is a successful (non-error) trace of program Prog and $t \sqsubseteq t'$ then t' is a trace of Prog



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Recall the Order

- 1. Order on states $h \sqsubseteq h'$.
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```
h_1 \sqsubseteq h'_1
\vdots
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```



▶ If $t = h_1 \cdots h_n$, define $t \circ h = (h_1 \circ h) \cdots (h_n \circ h)$

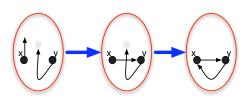


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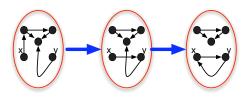
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$$[x]=y ; [y]=x$$





Recall the ???

```
\{(x \mapsto -) * P\} \ [x] := 7 \ \{(x \mapsto 7) * P\}
\{\text{true}\} \ [x] := 7 \ \{??\}
\{P * (x \mapsto -)\} \ \text{dispose}(x) \ \{P\}
\{\text{true}\} \ \text{dispose}(x) \ \{??\}
```



- ▶ $\{A\}Prog\{B\}$ holds iff $\forall h \in A$,
 - 1. no error: $\neg \exists t$. $htError \in Traces(Prog)$
 - 2. partial correctness: $\forall t, h'$. $hth' \in Traces(Prog) \Rightarrow h' \in B$
- ▶ If we run *Prog* in $h \circ h_{fr}$ where $h \in A$, then h_{fr} will not change.²

²One more technical property concerning safety and footprints is needed to imply this: any safe (doesn't lead to error) state has a smallest safe state below it, and start states of footprints are below (or equal) those.

³Error-avoiding used in Hoare-Wirth 1972, tightness observed in 2000

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- ► This "will not change" property is a **fact** of the semantics of programs and specs. It is independent of separation logic.
- ▶ It is true of many more models than the RAM
- ▶ We can just "exploit" this fact with the frame rule

$$\frac{\{P\}C\{Q\}}{\{P*R\}C\{Q*R\}}$$
 Frame Rule

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²One more technical property concerning safety and footprints is needed to imply this: any safe (doesn't lead to error) state has a smallest safe state below it, and start states of footprints are below (or equal) those.

Summary (Model Theoretic Properties)

- 1. **Footprint Theorem** If t is a trace of program Prog, then there is a smallest $t_f \sqsubseteq t$ where t_f is a trace of Prog
- 2. **Frame Theorem**: If t is a *successful* (non-error) trace of program Prog and $t \circ h$ is defined, then then $t \circ h$ is a trace of Prog



Part III

Proof Theory

▶ Papers of Berdine, Calcagno, Distefano, Yang, O'Hearn



A Special Format

A special form⁴

$$(B_1 \wedge \cdots \wedge B_n) \wedge (H_1 * \cdots * H_m)$$

where

$$\begin{array}{ll} H & ::= & E \mapsto \rho \mid \mathsf{tree}(E) \mid \mathsf{lseg}(E, E) \\ B & ::= & E = E \mid E \neq E \end{array}$$

$$E ::= x \mid \text{nil}$$

 $\rho ::= f_1 \colon E_1, \dots, f_n \colon E_n$
 $B ::= E = E \mid E \neq E$

and many other inductive predicates



⁴assertional if-then-else as well

▶ A proof theory oriented around Abstraction and Subtraction .



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- ► Sample Abstraction Rule

$$lseg(x, t) * list(t) \vdash list(x)$$



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Subtraction Rule

$$\frac{Q_1 \vdash Q_2}{Q_1 * S \vdash Q_2 * S}$$



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Subtraction Rule

$$\frac{Q_1 \vdash Q_2}{Q_1 * S \vdash Q_2 * S}$$

▶ Try to reduce an entailment to the axiom

$$\overline{B \wedge \mathsf{emp} \vdash \mathsf{true} \wedge \mathsf{emp}}$$



Works great!

$$lseg(x, t) * t \mapsto [tl: y] * list(y) \vdash list(x)$$

Abstract (Roll)



Works great!

$$|\operatorname{lseg}(x, t) * \operatorname{list}(t) \vdash \operatorname{list}(x) |\operatorname{lseg}(x, t) * t \mapsto [tl : y] * \operatorname{list}(y) \vdash \operatorname{list}(x)$$

Abstract (Inductive) Abstract (Roll)



```
\begin{aligned} & \mathsf{list}(x) \vdash \mathsf{list}(x) \\ & \mathsf{lseg}(x, t) * \mathsf{list}(t) \vdash \mathsf{list}(x) \\ & \mathsf{lseg}(x, t) * t \mapsto [tt : y] * \mathsf{list}(y) \vdash \mathsf{list}(x) \end{aligned}
```

Subtract Abstract (Inductive) Abstract (Roll)



```
emp \vdash emp

list(x) \vdash list(x)

lseg(x, t) * list(t) \vdash list(x)

lseg(x, t) * t \mapsto [tl : y] * \text{list}(y) \vdash \text{list}(x)
```

Axiom! Subtract Abstract (Inductive) Abstract (Roll)



```
emp \vdash emp
list(x) \vdash list(x)
lseg(x, t) * list(t) \vdash list(x)
lseg(x, t) * t \mapsto [tl:y] * list(y) \vdash list(x)
```

$$lseg(x, t) * t \mapsto nil * list(y) \vdash list(x)$$

Abstract (Inductive)



```
emp \vdash emp

list(x) \vdash list(x)

lseg(x, t) * list(t) \vdash list(x)

lseg(x, t) * t \mapsto [tl:y] * list(y) \vdash list(x)
```

Axiom! Subtract Abstract (Inductive) Abstract (Roll)

$$list(x) * list(y) \vdash list(x)
lseg(x, t) * t \mapsto nil * list(y) \vdash list(x)$$

Subtract (Inductive)



```
list(x) \vdash list(x)
lseg(x, t) * list(t) \vdash list(x)
lseg(x, t) * t \mapsto [tl : y] * list(y) \vdash list(x)
\vdots
list(y) \vdash emp
list(x) * list(y) \vdash list(x)
```

 $lseg(x, t) * t \mapsto nil * list(y) \vdash list(x)$

emp ⊢ emp

Axiom!
Subtract
Abstract (Inductive)
Abstract (Roll)

Junk: Not Axiom!
Subtract
Abstract (Inductive)



List of abstraction rules for Iseg

Rolling

$$\mathsf{emp} \quad \to \quad \mathsf{lseg}(E,E)$$

$$E_1 \neq E_3 \land E_1 \mapsto [tl : E_2, \rho] * \mathsf{lseg}(E_2, E_3) \quad \to \quad \mathsf{lseg}(E_1, E_3)$$

Induction Avoidance

 $lseg(E_1, E_2) * lseg(E_2, nil) \rightarrow lseg(E_1, nil)$

$$\begin{split} & |\mathsf{seg}(E_1,E_2)*E_2 {\mapsto} [t : \mathsf{nil}] \quad \rightarrow \quad \mathsf{lseg}(E_1,\mathsf{nil}) \\ & |\mathsf{lseg}(E_1,E_2)*|\mathsf{seg}(E_2,E_3)*E_3 {\mapsto} [\rho] \quad \rightarrow \quad \mathsf{lseg}(E_1,E_3)*E_3 {\mapsto} [\rho] \\ & |E_3 {\neq} E_4 \land \mathsf{lseg}(E_1,E_2)*|\mathsf{lseg}(E_2,E_3)*|\mathsf{lseg}(E_3,E_4) \\ & \quad \rightarrow \quad \mathsf{lseg}(E_1,E_3)*|\mathsf{lseg}(E_3,E_4) \end{split}$$

Proof Procedure for $Q_1 \vdash Q_2$, Normalization Phase

Substitute out all equalities

$$\frac{Q_1[E/x] \vdash Q_2[E/x]}{x = E \land Q_1 \vdash Q_2}$$

Generate disequalities. E.g., using

$$x \mapsto [\rho] * y \mapsto [\rho'] \rightarrow x \neq y$$

- ▶ Remove empty lists and trees: lseg(x,x), tree(nil)
- ► Check antecedent for inconsistency, if so, return "valid". Inconcistencies: $x \mapsto [\rho] * x \mapsto [\rho']$ $\text{nil} \mapsto -x \neq x$...
- Check pure consequences (easy inequational logic), if failed then "invalid"

Proof Procedure for $Q_1 \vdash Q_2$, Abstract/Subtract Phase

Trying to prove $B_1 \wedge H_1 \vdash H_2$

- ▶ For each spatial predicate in H_2 , try to apply abstraction rules to match it with things in H_1 .
- ▶ Then, apply subtraction rule.

$$\frac{Q_1 \vdash Q_2}{Q_1 * S \vdash Q_2 * S}$$

▶ If you are left with

$$B \wedge \mathsf{emp} \vdash \mathsf{true} \wedge \mathsf{emp}$$

report "valid", else "invalid"



- ▶ The BC procedure is cubic and complete on certain formulae
- ▶ In general it is incomplete, but BC have another (exponential) procedure that is complete.



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- ► For embeddings in proof assistants, similar strategies can be used in tactics (I think).. ConcCminor, ArmCam, L4.verified, TopsyTokyo...
- ► Abstract interpreters based on sep logic Space Invader, SLAyer, THOR, jStar, Xisa, VELOCITY - use special versions of the abstraction rules to ensure convergence. See Yang's and Magill's CAV talks on Saturday.

Earlier Slide... Let's think about automating

```
► Spec
{tree(p)} DispTree(p) {emp}
```

Rest of proof of evident recursive procedure



$$A \vdash B$$



$$A \vdash B * ?$$



$$tree(i) * tree(j) \vdash tree(i) * ?$$



$$tree(i) * tree(j) \vdash tree(i) * tree(j)$$



$$x \neq \text{nil} \land \text{list}(x) \vdash \exists x'. x \mapsto x' * ?$$



$$x \neq \text{nil} \land \text{list}(x) \vdash \exists x'. x \mapsto x' * \text{list}(x')$$



$$A \vdash B * ?$$



How to infer a frame

Convert a failed derivation

```
\begin{array}{ll} \mathsf{list}(y) \vdash \mathsf{emp} & \mathsf{Junk: Not Axiom!} \\ \mathsf{list}(x) * \mathsf{list}(y) \vdash \mathsf{list}(x) & \mathsf{Subtract} \\ \mathsf{lseg}(x,t) * t \mapsto \mathsf{nil} * \mathsf{list}(y) \vdash \mathsf{list}(x) & \mathsf{Abstract (Inductive)} \end{array}
```

into a successful one

How to infer a frame, more generally

- ▶ Problem: $A \vdash B*$?
- Apply abstraction and subtraction to shrink your goal: if you get to F ⊢ emp then F is your frame axiom.

$$F \vdash \text{emp}$$
 \uparrow \vdots \uparrow $A \vdash B$ \uparrow

► Sometimes you need to deal with multiple leaves at top (case analysis)



Extensions of the entailment question II:

$$A \vdash B$$



Extensions of the entailment question II:

$$A*? \vdash B$$



Extensions of the entailment question II: Abduction

$$A*? \vdash B$$



Extensions of the entailment question II:

$$A*? \vdash B$$

▶ We call the ? here an "anti-frame".⁵



```
1 void p(list-item *y) {
2    list-item *x;
3    x=malloc(sizeof(list-item));
4    x→tail = 0;
5    merge(x,y);
6    return(x); }
```

Abductive Inference:

Given Summary/spec:
$$\{list(x) * list(y)\} merge(x, y)\{list(x)\}$$



```
1 void p(list-item *y) {
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```
1 void p(list-item *y) { emp

2 list-item *x;

3 x=malloc(sizeof(list-item));

4 x\rightarrow tail = 0; x\mapsto 0

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Abductive Inference:

Given Summary/spec:
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```
1 void p(list-item *y) { emp list(y) } 2 list-item *x; 3 x=malloc(sizeof(list-item)); 4 x\rightarrowtail = 0; x\mapsto 0 5 merge(x,y); 6 return(x); } Abductive Inference: x\mapsto 0* list(y) \vdash list(x) * list(y) Given Summary/spec: {list(x) * list(y)} merge(x, y){list(x)}
```



```
1 void p(list-item *y) {
                                                                list(y)
                                               emp
      list-item *x;
2
3
      x=malloc(sizeof(list-item));
      x \rightarrow tail = 0:
                                              x\mapsto 0
5
      merge(x,y);
                                              list(x)
6
      return(x); }
Abductive Inference: x \mapsto 0 * \operatorname{list}(y) \vdash \operatorname{list}(x) * \operatorname{list}(y)
Given Summary/spec: \{list(x) * list(y)\} merge(x, y) \{list(x)\}
```



```
1 void p(list-item *y) {
                                                                list(y)
                                               emp
      list-item *x;
2
3
      x=malloc(sizeof(list-item));
4
      x \rightarrow tail = 0:
                                              x\mapsto 0
5
      merge(x,y);
                                              list(x)
6
      return(x); }
                                                                list(ret)
Abductive Inference: x \mapsto 0 * \operatorname{list}(y) \vdash \operatorname{list}(x) * \operatorname{list}(y)
Given Summary/spec: \{list(x) * list(y)\} merge(x, y) \{list(x)\}
```



```
1 void p(list-item *y) {
                                                              list(y) (Inferred Pre)
                                             emp
      list-item *x:
2
3
      x=malloc(sizeof(list-item));
4
     x \rightarrow tail = 0:
                                             x\mapsto 0
5
     merge(x,y);
                                             list(x)
6
      return(x); }
                                                              list(ret) (Inferred Post)
Abductive Inference: x \mapsto 0 * \operatorname{list}(y) \vdash \operatorname{list}(x) * \operatorname{list}(y)
Given Summary/spec: \{list(x) * list(y)\} merge(x, y) \{list(x)\}
```



Proof Theory Summary

▶ Despite undecidability results for even propositional logics, when used in the right way, substructural proof theory can be "quite" effective



Proof Theory Summary

- ▶ Despite undecidability results for even propositional logics, when used in the right way, substructural proof theory can be "quite" effective
- ▶ Interesting inference questions beyond entailment:
 - Frame inference

$$A \vdash B * ?$$

which lets you use small specs, and

Anti-frame inference (or, abduction),

$$A*? \vdash B$$

which can help in finding the small specs

