## Answer Question 1 and two further questions

1. Answer the following five parts
a. Describe the concept of transactions by discussing what atomicity, consistency, isolation and durability mean.

Transactions are sequences of operations that are atomic, consistency preserving, isolated and durable. Atomicity means that the sequence is performed completely or not at all. Consistency preservation means that the sequence leads from one consistent state to another and inconsistencies are confined to within the transaction. Isolation means that the transaction is executed in isolation to any concurrent transactions and durability means that once the transaction is completed its changes will persist.
b. What is two-phase locking and how does it achieve which of the above transaction properties?


Protocol for serializing transactions. Achieves isolation. Lock acquisition phase and lock release phase. Only resources for which lock has been acquired are accessed. Locks are only granted if request is compatible with previously granted lock requests.
c. Explain the circumstances in which you would use hierarchical locking and the lock modes that are required for this concurrency control scheme. For which situations are each of these lock modes intended?

Hierarchical locking is used when there are containers that contain resources that may be locked individually. It defines the following four locking modes: A Read lock (R) indicates that a process may read a resource or any of the resource that is contained therein. A Write lock ( W ) indicates that a process may modify a resource or any of the resources that is contained therein. An Intention Read lock (IR) indicates that a process may have read locks on any of the resources that are contained in the IR locked resource. Finally, an Intention Write lock (IW) indicates that a process may have a write lock on the resources that are included in the container.
d. Assume you have to build the concurrency control manager of a database management system. The database consists of many database tables, each of which is composed of a set of records. Each record of the table has several attributes. You choose to use hierarchical locking. Explain what the manager needs to do for:
i. changing an attribute value; request IW lock on database, table, record and W lock on attribute.
ii. reading an attribute value; request IR lock on database, table and record and R lock on attribute.
iii. modifying a record; request IW lock on database, table and W lock on record.
iv. reading all attributes of a record;
request IR lock on database, table and R lock on record.
v. updating a complete column;
request IW lock on database, W lock on table
vi. reading a complete column; request IR lock on database, R lock on table
vii. inserting a table;

request $W$ lock on database
viii. reading the complete database;
request R lock on database
e. Two-phase locking may lead to deadlocks. This causes many complications in the design of database management systems. Absence of deadlocks can be established using reachability analysis on LTSs. Why can this method not be used to implement deadlock-free database concurrency control?

Queries and transactions are defined in an ad-hoc manner and it is more computationally expensive to compute a labelled transition system and analyse it for absence of deadlocks than it is to detect and recover from deadlocks.

2. Answer the following three parts.
a. Draw an equivalent LTS for the following FSP process definitions:
i. $A=\left(a->b->\left(\begin{array}{ll}\mathrm{c} & \mathrm{a} \\ \mathrm{C} & \mathrm{A} \mid \mathrm{d}->A\end{array}\right)\right.$ ).


ii. $A=(a->B), B=(b->B \mid a->A)$.

iii. $A=(a->b->A) . C=(c->b->C) . \quad| | A C=(A| | C)$
[3 marks]

iv. $A=(a->b->A) . C=(c->d->C) .| | A C=(A| | C) /\{b / c\}$.
[3 marks]
A

b. You have been asked to assist in the re-engineering of the control software for the Northern Line of London Underground. There is a particular difficulty because of the intersection of the Bank/Charing Cross and the Edgware/High Barnet branches between Camden and Euston. In particular, there are trains coming from both Edgware and High Barnet that need to be dispatched onto the Bank and Charing Cross branches. For southbound trains, there is thus a piece of track that needs to be protected by signalling equipment in such a way that trains can be dispatched without crashing into each other.

Complete the following FSP model to describe the signalling equipment, taking into account that trains can concurrently arrive from the Edgware and from the High Barnet branches and must not crash into each other.

```
Signal = (green -> red -> Signal).
```

Train $=$ (request $->$... -> Train).
||Branch = ...
Controller = ...
||Camden = (edgware:Branch || highbarnet:Branch || Controller).
[13 marks]
Signal $=$ (green $->$ red $->$ Signal).
Train $=$ (request $->$ green $->$ cross $->$ leave $\rightarrow$ red $->$ Train).
||Branch = (Signal || Train)
Controller = (edgeware.green -> edgeware.red ->
highbarnet.green->highbarnet.red -> Controller).
||Camden $=$ (edgware:Branch || highbarnet:Branch || Controller).
c. For the model of Question 2.b specify a safety property so that you can use a model checker to prove that there is never more than one train crossing the southbound track intersection between Camden and Euston.
3. Answer the following three parts
a. Consider the following Labelled Transition Systems and give equivalent FSP process algebras

i.

$$
\begin{aligned}
A= & (a->b->B), \\
B= & (b->B \\
& \mid a->A) .
\end{aligned}
$$

ii.

$A=(a->b->A)$.
$|\mid A A=(c: A| | d: A)$.

iii.


iv.

$$
\begin{aligned}
& A=(a->a a->A) . \\
& B=(C->B) . \\
& |\mid A B=(A| | B) \backslash\{a a\} .
\end{aligned}
$$


[Subtotal 11 marks]
b. Consider the McDonald's Restaurant on the corner of Warren St and Tottenham Court Rd. At lunchtime, the restaurant has four waiters (lucy, bob, alice and jill), who take orders (mostly from UCL students). As we are in England, a student who arrives at McDonald's enters an arrival queue. She waits until one of the waiters becomes available. The waiter then takes her order, serves her and finally the student pays her bill. Model how students queue and how their orders are processed concurrently in FSP.
[13 marks]

```
const MaxIF=4
range IF=0..MaxIF
set Waiters={lucy, bob, alice, jill}
ARRIVE=ARRIVE[0],
ARRIVE [num:IF] = (enterarrq[num] ->ARRIVE[(num+1)%MaxIF]).
ARRIVALQ=(enterarrq[num:IF]->ARRIVALQ[num]),
ARRIVALQ[n0:IF]=(enterarrq[num:IF] ->ARRIVALQ[n0][num]
    |Waiters.order[n0] ->ARRIVALQ),
ARRIVALQ [n0:IF][n1:IF]=(
        enterarrq[num:IF]->ARRIVALQ[n0][n1][num]
    |Waiters.order[n0] ->ARRIVALQ[n1]),
ARRIVALQ[n0:IF][n1:IF][n2:IF]=(
    enterarrq[num:IF] ->ARRIVALQ[n0][n1][n2][num]
    |Waiters.order[n0]->ARRIVALQ[n1][n2]),
ARRIVALQ[n0:IF][n1:IF][n2:IF][n3:IF]=(
    Waiters.order[n0]->ARRIVALQ[n1][n2][n3]).
Waiter = (order[c:IF] -> serve [c] -> pay[c] ->Waiter).
    |McDonalds=(Waiters:Waiter| ARRIVE || ARRIVALQ)}.
```

c. Specify the liveness property that "hungry students eventually get their lunch".
progress LunchServed=\{Waiters.serve[IF]\}
[Total 33 marks]
4. Answer the following three parts.
a. For each of the following FSP processes, define the alphabet of:
i. Server = (receive -> process -> send-> Server ).
\{receive, process, send\}
ii. Client $=$ (send -> wait $->$ receive -> Client) $\backslash\{$ wait \}. \{receive, send\}
iii. $\{\mathrm{a}, \mathrm{b}\}:$ : Server

[2 marks]
[3 marks]
\{a.process, a.invoke, a.return b.process, b.invoke, b.return\}
iv. ||CS=(a:Client||b:Client||\{a, b\}::Server\}/\{a.send/a.invoke, b. send/b.invoke,
a.receive/a return,
b.receive/b.return\}.

b. Consider the following FSP Process of a cross country railroad that has only a single track, which connects villages N and S via M .
const MaxTrains=4
range Trains=0..MaxTrains
NM=NM[0][0],
NM [nb:Trains][mb:Trains] =
(when (nb==0 \&\& mb<MaxTrains) go_nm->NM[nb][mb+1]
|when (mb==0 \&\& nb<MaxTrains) go_n->NM[nb+1][mb]
|when (mb>0) arr_at_m_from_n->NM[nb][mb-1]
|when ( $\mathrm{nb}>0$ ) arr_at_n->NM[nb-1][mb]
|when ( $\mathrm{nb}>0 \& \& \mathrm{mb}>0$ ) unsafe->ERROR)
SM=SM[0][0],
SM[mb:Trains][sb:Trains]=
(when (mb==0 \&\& sb<MaxTrains) go_s $\rightarrow$ SM[mb] [sb+1]
|when (sb==0 \&\& mb<MaxTrains) go_sm $\rightarrow$ SM[mb+1][sb]
|when (sb>0) arr_at_s->SM[mb][sb-1] |when (mb>0) arr_at_m_from_s->SM[mb-1][sb] |when (mb>0\&\&sb>0) unsafe->ERROR)
$\mathrm{N}=\left(\mathrm{go} \_\mathrm{nm}->\mathrm{N}\right.$ |arr_at_n $\left.\rightarrow \mathrm{N}\right)$.
$S=($ go_sm->S |arr_at_s->S).
$\mathrm{M}=\mathrm{M}[0][0]$,
M[nb:Trains][sb:Trains] $=$
(when (sb<MaxTrains) arr_at_m_from_n $\rightarrow$ M[nb][sb+1]
|when (nb<MaxTrains) arr_at_m_from_s $\rightarrow$ M[nb+1][sb]
|when (sb>0) go_s $\rightarrow$ M[nb] [sb-1]
|when ( $n b>0$ ) go_n $\rightarrow M[n b-1][s b])$.

## $|\mid$ COUNTRYRR $=(N M| | S M| ||N||S| \mid M)$.

Design the signalling control software in a UML class diagram, detailing associations, operations and attributes. Use stereotype <<Monitor>> to identify those classes in your design that are monitors.
[13 marks]

c. Show the implementation of the operations of the class that controls access to station
M.

```
class StationM {
    private
        final int MaxTrains=10;
        int nb;
        int sb;
    public synchronized void arr_at_m_from_n() {
        while(sb>=MaxTrains) wait();
        sb++;
        notifyAll();
    }
    public synchronized void arr_at_m_from_s() {
        while(nb>=MaxTrains) wait();
        nb++;
        notifyAll();
    }
    public synchronized void go_s() {
        while (sb<=0) wait();
        sb--;
        notifyAll();
    }
    public synchronized void go_n() {
        while (nb<=0) wait();
        nb--;
        notifyAll();
    }
}
```

                                    [9 marks]
    [Total 33 marks]
5. Answer the following three parts.
a. In no more than 50 words each define the following concepts:
i. Semaphore
[2 marks]
ADT with encapsulated counter and waiting list. Exports two operations signal and wait. wait checks whether counter is greater than 0 . If yes decrements counter, otherwise appends calling process to waiting list. If there are waiting processes, signal wakes up first of these, otherwise it increments the counter.
ii. Monitor

A programming language concept that guarantees mutually exclusive access to a data structure encapsulated by the monitor.

## iii. Deadlock

[2 marks]
A set of processes mutually waiting for each other.

## iv. Livelock

[2 marks]
A process spinning while waiting for a condition that will never become true.
v. Condition Synchronization

Condition synchronization is implemetned inside a monitor, forces calling process to wait (outside the monitor) if conditions are not yetmet. Only let them proceed when conditions are met and notifies other processes if changes to data structures have occurred.
vi. Safety Property

Ascertain that nothing bad will ever happen
vii. Liveness Property

Ascertain that something desirable will eventually happen

b. An ftp server provides two principal operations, put and get. In order to use these operations, clients have to open a connection first and they close the connection when they no longer want to use the server. The ftp server can have several concurrent sessions with different clients. To protect ftp servers from becoming too overloaded, the number of concurrent sessions in practice has an upper boundary, which we can assume to be 4 for this exercise. Use FSP to model the behaviour of the ftp server to show how it serves a number of concurrent clients.
[11 marks]

```
const MaxSessions = 4
range Session = 0..MaxSessions
Client = (open-> Connected),
Connected = (put -> Connected
    |get -> Connected
    |close -> Client).
Server = Sessions[0],
Sessions[i:Session] = (
    when (i<MaxSessions) open -> Sessions[i+1]
    | when (i>0) close -> Sessions[i-1]
).
set Clients = {c1,c2,c3,c4,c5,c6}
||FTP = (Clients:Client || Clients::Server)
```

c. When trying to access popular ftp servers you might have experienced that it is not possible to get through. Specify the liveness property that every client will eventually be able to connect to the ftp server of Question 5.b in FSP.



