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# Enhancing Separation Logic for Object-Orientation

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presented by Stephanus Johannes van Staden Master of Science, International University in Germany

> *born* February 22nd, 1983

> > citizen of South Africa

accepted on the recommendation of

Prof. Dr. Bertrand Meyer, examiner Prof. Dr. Peter O'Hearn, co-examiner Dr. Matthew Parkinson, co-examiner Dr. Cristiano Calcagno, co-examiner

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# Contents

1	1 Introduction				
	1.1	History and motivation			
	1.2	Overview of contributions			
<b>2</b>	Background material				
	2.1	Language syntax			
	2.2	Logic syntax and semantics			
	2.3	Specification refinement			
	2.4	The specification environment			
	2.5	Statement verification			
	2.6	Method verification			
	2.7	Class and program verification			
	2.8	Useful lemmas			
3	Rea	oning about related abstractions 19			
	3.1	Examples $\ldots \ldots 23$			
		3.1.1 Intertwining ancestor abstractions			
		3.1.2 Access control and call protocols			
		3.1.3 Diamond inheritance $30$			
	3.2	MultiStar			
		3.2.1 Front-end			
		3.2.2 Back-end			
	3.3	Case study $\ldots \ldots 38$			
	3.4	Formalisation $\ldots \ldots 41$			
		3.4.1 Language syntax			
		3.4.2 Operational semantics			
		3.4.3 Logic syntax and semantics			
		3.4.4 Specification refinement			
		3.4.5 The specification environment			
		3.4.6 Export information verification			

В	Ant	itone Galois connections	127
А	Sen	nantics of the proof system	123
6	Con	nclusion	121
	5.4	Related Work	. 118
		5.3.2 Development calculus	. 114
	0.0	5.3.1 Language and proof rules	. 111
	5.3	Constructing correct OO programs	. 111
		5.2.2 Hoate Logic	. 102
		5.2.1 Lambda Calculus	. 99 109
	5.2	Applications	. 99
	FO	5.1.5 Discussion	. 95
		5.1.4 The Refinement of Refinement Systems	. 88
		5.1.3 System $V_2$ and Refinement	. 87
		5.1.2 The Extended Language and Formal System	. 81
		5.1.1 The Inputs	. 79
0	5.1	Freefinement	. 79
5	Cor	rectness by construction	77
	4.8	Conclusions and related work	. 75
	4.7	Implementation	. 74
		4.6.2 Predicate extraction	. 73
		4.6.1 Relative purity $\ldots$ $\ldots$ $\ldots$ $\ldots$ $\ldots$ $\ldots$	. 71
	4.6	Relative purity and predicate extraction	. 71
	4.5	Model-based specifications	. 67
	4.4	Class invariant verification	.62
	4.2 4 3	Postcondition verification	. 55 58
	19	4.1.2 I ne concrete setting	. 54 55
		4.1.1 The abstract setting: triples and footprints	. 54
	4.1	Background	. 53
4	Ver	ifying executable contracts	51
	3.5	Conclusions and related work	. 47
		3.4.10 Class and program verification	. 46
		3.4.9 Method verification	. 44
		3.4.8 Statement verification	. 44
		3.4.7 Axiom verification	. 44

## ABSTRACT

Reasoning about programs can be tricky and error-prone. Formal verification facilitates the formulation and demonstration of rigorous arguments about program correctness. To be usable and useful, a program logic, i.e. a proof system for program verification, should accommodate the principal mechanisms of the programming language and crystallise the informal reasoning of programmers.

Recently, separation logic emerged as a promising tool for reasoning about shared mutable state, which pervades mainstream programming languages such as C, Java and Eiffel. Parkinson and others proposed a proof system for Object-Oriented (OO) programs that combines separation logic with mechanisms to reason about inheritance and dynamic dispatch. This system can verify a wide range of OO programs and design patterns in a concise way. The flexibility and simplicity of the system made it an attractive target for further improvement and broader application.

The contributions of this thesis address the following problems:

- 1. Specifying, verifying and using relationships between state abstractions. This is especially important when reasoning about OO programs that use multiple inheritance.
- 2. Reasoning about executable contracts. Executable contracts are often weak and may perform side-effects. Yet they capture useful design information, are programmer-friendly and assist in debugging.
- 3. Refinement and correctness by construction. Instead of first writing the code and then proving it correct, these techniques make it possible to write a correct program in the first place. The program and its correctness proof grow and evolve together.

State abstraction mechanisms, such as abstract predicates, are useful for reasoning about programs with modules that encapsulate state and hide information. Parkinson adapted abstract predicates to the OO setting, and they play a central role in his proof system. Since OO code frequently relies on relationships between state abstractions of a class or a class hierarchy, this thesis enhances Parkinson's system with mechanisms for specifying, verifying and exploiting such relationships. The extension also makes it possible to establish the logical consistency of a class hierarchy without considering implementation details, and it facilitates reasoning about multiple inheritance.

Existing OO code often contains contracts in the form of executable preconditions, postconditions and class invariants. These specifications are typically weaker than separation logic assertions, but they are more lightweight and perhaps more likely to be written by programmers. Contracts also record valuable information about program design and are useful for testing and debugging. This thesis contributes a new technique for using the separation logic assertions to verify that executable contracts will always hold at runtime and that they will not perform unwanted side-effects. As a result, verified contracts need not be monitored at runtime, and they add confidence in the correctness of the code and the separation logic specification.

Correctness by construction is an important feature of a mature engineering discipline. In the context of software engineering, it is realised by a calculus for top-down program development that features refinement as a central technique. A refinement calculus helps to construct correct code from a given specification in a series of steps. This thesis proposes freefinement – an algorithm for obtaining a sound refinement calculus from a modular program logic. The resulting refinement calculus can interoperate closely with the program logic, and it is even possible to reuse and translate proofs between them.

Many aspects of the work also apply to other settings. None of the contributions rely on a specific flavour of separation logic. The work on multiple inheritance subsumes interface inheritance, and the reasoning techniques for executable contracts generalise to non-OO languages that use explicit memory management. Finally, freefinement applies to a great variety of formal systems, including program logics for other languages and type systems.

## ZUSAMMENFASSUNG

Beweisführung über Programme kann knifflig und fehleranfällig sein. Formale Verifikation unterstützt das Formulieren und Beweisen von präzisen Behauptungen über die Korrektheit von Programmen. Um brauchbar und nützlich zu sein, sollte eine Programmlogik, d.h. ein Beweissystem für Programmverifikation, die Hauptmechanismen der Programmiersprache unterstützen und die informelle Beweisführung der Programmierer präzisieren.

In letzter Zeit hat sich Separation Logic als nützliches Werkzeug erwiesen, um Beweise über einen gemeinsam genutzten, veränderlichen Zustandsraum zu führen, der in populären Programmiersprachen wie z.B. C, Java und Eiffel allgegenwärtig ist. Parkinson et al. schlugen ein Beweissystem für objektorientierte (OO) Programme vor, das Separation Logic mit Mechanismen für die Beweisführung über Vererbung und dynamischer Bindung kombiniert. Dieses System kann ein breites Spektrum von OO-Programmen und Entwurfsmustern knapp und präzise verifizieren. Die Flexibilität und Einfachheit des Systems haben es zu einem attraktiven Zielobjekt für weitere Verbesserungen und Einsatzmöglichkeiten gemacht.

Die Beiträge dieser Dissertation befassen sich mit den folgenden Problemen:

- Spezifikation, Verifikation und Verwendung von Beziehungen zwischen Zustandsabstraktionen. Dies ist speziell dann wichtig, wenn Beweise über OO-Programme geführt werden, die Mehrfachvererbung verwenden.
- 2. Beweisführung über ausführbare Spezifikationen. Ausführbare Spezifikationen sind oft schwach und können mit Nebeneffekten behaftet sein. Dennoch erfassen sie nützliche Designinformationen, sind programmiererfreundlich und helfen bei der Fehlersuche.
- 3. Verfeinerung und konstruktionsbedingte Korrektheit. Anstatt zuerst den Programmtext zu schreiben und dann zu verifizieren, erlauben diese Techniken es, das Programm von Anfang an korrekt zu schreiben. Das

Programm und der Beweis seiner Korrektheit wachsen und entwickeln sich gemeinsam.

Mechanismen für die Zustandsabstraktion, wie z.B. abstrakte Prädikate, sind nützlich für die Beweisführung über Programme mit Modulen, die Zustand und Informationen kapseln. Parkinson adaptierte abstrakte Prädikate an das OO-Umfeld und sie spielen eine zentrale Rolle in seinem Beweissystem. Da OO-Programme sich oft auf Beziehungen zwischen Zustandsabstraktionen einer Klasse oder Klassenhierarchie stützen, erweitert diese Dissertation Parkinsons System mit Mechanismen für die Spezifikation, Verifikation und Instrumentalisierung dieser Beziehungen. Die Erweiterung ermöglicht es auch, die logischen Konsistenz einer Klassenhierarchie zu begründen ohne Implementationsdetails zu berücksichtigen, und sie unterstützt die Beweisführung über Mehrfachvererbung.

Bereits existierende OO-Programme enthalten oft Spezifikationen in der Form von ausführbaren Vor-, Nachbedingungen und Klasseninvarianten. Diese Spezifikationen sind typischerweise schwächer als Zusicherungen in Separation Logic, aber sie sind auch schlanker und werden vielleicht eher von Programmierern geschrieben. Auch enthalten sie wertvolle Informationen über den Programmaufbau und sind nützlich für Tests und die Fehlerbehebung. Ein Beitrag dieser Dissertation ist eine neue Technik für die Verwendung von Zusicherungen in Separation Logic, die sicherstellt, dass ausführbare Spezifikationen zur Laufzeit immer eingehalten werden und keine ungewollten Nebeneffekte haben. Dadurch müssen ausführbare Spezifikationen zur Laufzeit nicht mehr überprüft werden und sie erhöhen das Vertrauen in die Korrektheit des Programms und der Spezifikationen in Separation Logic.

Konstruktionsbedingte Korrektheit (correctness-by-construction) ist eine wichtige Eigenschaft ausgereifter Ingenieursdisziplinen. Im Kontext von Software Engineering wird es durch einen Kalkül für Top-Down-Programmentwicklung realisiert, der Verfeinerung als zentrale Technik aufweist. Ein Verfeinerungskalkül hilft bei der Konstruktion korrekter Programme in einer Serie von Schritten, ausgehend von einer gegebenen Spezifikation. Diese Dissertation schlägt Freefinement vor – ein Algorithmus um einen korrekten Verfeinerungskalkül aus einer modularen Programmlogik zu erhalten. Der resultierende Verfeinerungskalkül ist in der Lage, mit der Programmlogik eng zu interagieren, und es ist sogar möglich, Beweise zwischen diesen beiden wiederzuverwenden und zu übersetzen.

Viele Aspekte dieser Arbeit sind auch auf andere Umgebungen anwendbar, keiner der Beiträge hängt von spezifischen Ausprägungen von Separation Logic ab. Die Arbeit an Mehrfachvererbungen umfasst auch Schnittstellenvererbung, und die Beweisführungstechnik für ausführbare Spezifikationen kann auf Nicht-OO-Sprachen mit explizitem Speichermanagement verallgemeinert werden. Überdies betrifft Freefinement eine grosse Vielfalt von formalen Systemen, inklusive Programmlogiken für andere Sprachen und Typsysteme.

# CHAPTER 1

## INTRODUCTION

### 1.1 History and motivation

Object-oriented (OO) programming languages are widely used in software engineering. They follow the imperative paradigm, i.e. programs are commands that transform state. Perhaps the most distinguishing feature of OO languages is that they promote a data-centered approach to programming: objects encapsulate data and operations associated with that data. Since humans often think about a system or a problem domain in terms of its constituent objects, the object abstraction is a natural fit for modelling them and hence reflecting this undertanding in software.

Mainstream OO languages have many features, such as classes, inheritance, dynamic binding, dynamic allocation of objects and sharing of references to objects. Although most OO programs use these features, their effects can be subtle and hard to understand. Especially in more complex programs where several features interact, it often difficult to see that the code is correct, i.e. that it will do what it is supposed to do. What is needed is a formalism that facilitates simple reasoning about the correctness of OO programs.

A system for verifying the correctness of imperative programs that manipulate simple variables has long been known: Hoare logic [32]. The basic judgement of Hoare logic is the triple  $\{P\}c\{Q\}$ , where P is the precondition, c the command and Q the postcondition. Informally, it means: if c is executed in an initial state satisfying P, and it terminates, then the final state will satisfy Q. An example of a valid triple is  $\{x = 7\}x := x + 1\{x = 8\}$ .

Hoare logic is compositional, or modular, which is important for larger verification efforts. Both programs and specifications should compose nicely. For example, the rule for reasoning about a sequential composition involves only the specifications of its two operands:

$$\frac{\{P\}c\{Q\} \quad \{Q\}c'\{R\}}{\{P\}c;c'\{R\}}$$

The rule of constancy is a good example of how specifications compose. Provided c does not modify any of the free variables in R, we have:

$$\frac{\{P\}c\{Q\}}{\{P \land R\}c\{Q \land R\}}$$

The rule states that a command preserves all properties of variables it did not modify. Continuing the earlier example, we would be able to conclude  $\{x = 7 \land y = 6\}x := x + 1\{x = 8 \land y = 6\}$ . A simple syntactic check suffices for determining the variables that a command might modify. Everything else must stay the same.

The situation is rather straightforward for imperative languages that manipulate simple variables: the state is just a mapping from variable names to values, and the only way to manipulate a variable is to mention it explicitly in the program text. OO languages are more complicated. In addition to simple variables (in the stack), the state also has shared objects (in the heap) that are accessed indirectly through references. Because of reference aliasing, there is no simple syntactic check for determining whether an object remains unmodified by a piece of code. OO programs regularly use many objects and much sharing, so it is important to reason about them in a convenient way.

It turns out that there are several ways to extend Hoare logic to programs that manipulate shared mutable state such as objects. One family of approaches keep the rule of constancy. Doing so requires more complicated techniques to determine 1) the set of objects or parts of objects that a command may modify, and 2) whether the predicate R describes any of them. Because 1 is not a simple syntactic check, and 2 tends to dissect R and not limit the inspection to its free variables, we shall not consider such approaches in this thesis.

Separation logic [54, 60] is another way of generalising Hoare logic to programs with shared mutable state. It adapts the rule of constancy by replacing the  $\wedge$  with \*, a new logical connective called separating conjunction. A state satisfies the predicate P \* Q when 1) its simple variables satisfy the constraints imposed on them by P and Q, and 2) it is possible to partition the objects into two disjoint sets of storage locations (i.e. heap locations or object-field pairs) such that the one set satisfies the constraints of P and the other those of Q. The reasoning about simple variables is exactly the same as in Hoare logic. Separation logic recognises that the concept of separation is important for reasoning about the new kind of storage: mutable locations that may be aliased by references.

The adapted rule is known as the *frame rule*:

$${P}c{Q} {P*R}c{Q*R}$$

It is applicable whenever c does not modify any of the free variables in R. This simple syntactic check needs no information about the definitions of predicates that appear in R, which is good for abstraction. The frame rule says that the separate objects described by R will not be modified. In order for this to work, it must be impossible for c to change the storage locations that R describes. The separation logic triple  $\{P\}c\{Q\}$  therefore has a generalised meaning<sup>1</sup>: if c is executed in an initial state satisfying P, then it will not access storage locations except those described in P or allocated by c, and if it terminates, then the final state will satisfy Q. The frame rule is very useful for knowing how a command behaves in a larger context, i.e. when more objects are present. It is heavily used in many proofs of correctness, and it tends to make them simple and short.

Separation logic simplifies reasoning about shared mutable state, but OO programs also use other features that are historically difficult to reason about. Inheritance and the associated dynamic dispatch (or dynamic binding) of routine calls enable a single piece of code to have an incredible variety of runtime behaviours. Static reasoning therefore needs to impose some discipline on inheritance and the behaviour of (overridden) routines. Moreover, the OO paradigm encourages programmers to think about objects as black boxes with hidden implementation details. Since the purpose of formalism is to make intuitive arguments rigorous, it is desirable to express and exploit such abstraction patterns in formal reasoning.

In his PhD thesis [57] and work with others [56, 58], Parkinson proposed an elegant proof system that addresses these issues. The main ingredients of the system are as follows (Chapter 2 contains the details):

- It uses separation logic to reason about the fields of objects.
- Abstract predicate families (apfs) provide encapsulated data abstractions in the logic. An apf is a predicate with a designated 'target' argument. Each class C can define the apf predicate for the case when the target argument has dynamic type C. It is only possible to use this

<sup>&</sup>lt;sup>1</sup>That c does not access (i.e. read or modify) these locations is a stronger requirement than what we actually need here, but it is widely used and useful for reasoning about concurrency.

definition within class C when the dynamic type of the target argument is C – the predicate must be treated abstractly in all other cases. Apfs can thus express the idea that objects (or parts of objects or object aggregates) are black boxes for outsiders, and that subclasses may change the representation. A specification of a routine typically says how the code transforms one or more of these black boxes into others.

• Each routine has a static and a dynamic specification, each consisting of a precondition and a postcondition. The static specification states in detail what the routine body does. It is used to reason about calls that use static dispatch (e.g. *precursor* or *super* calls). The dynamic specification is typically more abstract – it describes the idea behind the routine that must be respected in all subclasses. It imposes discipline on inheritance, and is used to reason about dynamically dispatched calls.

The organisation of specifications is modular: a routine body is verified only against the static specification, which must in turn be consistent with the dynamic specification. Static specifications make it unnecessary to re-verify code in subclasses, so each routine body is verified exactly once.

These ingredients combine to yield a compact system that can reason about many uses and abuses of inheritance, including behaviour extension, restriction and modification, and change of representation in subclasses.

Because of all its desirable features, this system is an ideal starting point for further enhancements. This thesis considers the improvement of the system and its broader application in reasoning. It tackles the following problems:

- 1. The basic apf mechanism is not always flexible enough. A class might want to share some properties of its definitions with others. Also, some properties of definitions, or relationships between them, may be invariant in a whole class hierarchy, and client code might rely on this. For example, it could be a design decision that all subclasses must use the same definition and hence the same implementation of a logical abstraction. Or it might be intended that an implication between two different apf predicates will always hold. We need ways to express, verify and use such information.
- 2. OO languages like Eiffel use multiple inheritance and deferred (abstract) classes and routines. Reasoning about these constructs requires extensions to the original system.

- 3. Much existing OO code has been specified with executable contracts, such as preconditions, postconditions and class invariants. They capture expectations and intentions in a programmer-friendly way. There has been several attempts to use them as a basis for verification, but it tends to be complicated because they 1) are often too weak, 2) do not have a separating conjunction, and hence struggle with framing and aliasing, and 3) often contain side-effects. The separation logic system ignores these assertions, but it would be better to reason about them and exploit them in verification.
- 4. Instead of hacking away at code and verifying its correctness afterwards, it is often easier to develop code that is correct by construction. However, the separation logic system is geared for bottom-up verification instead of top-down development. It does not directly support techniques for stepwise transformation such as refinement. A major goal of this thesis is therefore to support refinement and correctness by construction in a way that complements the separation logic system.

#### **1.2** Overview of contributions

Chapter 3 addresses the first problem by adding features to Parkinson's system. It shows with examples how a single class may employ multiple apfs to achieve its purpose, and that these are often related and combined in disciplined ways. Since the correctness of OO code frequently depends on the relationships between these abstractions, we need mechanisms to express, verify and use such information in a rigorous fashion. In particular, the work introduces two general specification mechanisms: *export clauses* for relating abstractions in individual classes, and *axiom clauses* for relating abstractions in a class and all its descendants, i.e. for an entire (sub-)hierarchy of classes. The information expressed in export and axiom clauses is first verified before it is assumed for the verification of code. Proof rules that avoid logical inconsistencies formalise the manipulation.

Chapter 3 also extends Parkinson's system to support multiple inheritance and deferred (abstract) classes and routines. This subsumes reasoning about interface inheritance, a form of multiple inheritance used in languages like Java. Multiple inheritance usually intertwines several logical abstractions from parent classes, and it turns out that export and axiom clauses are useful for specifying such information. Knowing more about ancestor classes through specifications means that fewer of their implementation details have to be considered, and verification is consequently more modular.

Chapter 4 attacks the third problem: reasoning about executable contracts and exploiting them in verification. The work shows how separation logic can be used to verify that executable contracts are satisfied, i.e. that they will always hold at runtime and not perform undesirable side-effects. Both the program and its executable assertions are verified with respect to separation logic specifications. A novel notion called *relative purity* embraces historically problematic side-effects in executable specifications, and verification boils down to proving *connecting implications*. Even model-based specifications, i.e. expressive executable contracts that use mathematical abstractions, can be verified. A failed verification attempt may indicate a discrepancy between the two kinds of specification. For example, the separation logic precondition of a routine may be too liberal for the particular application, and the executable precondition can help to uncover this problem. Or a failed verification may point to situations in which a runtime violation of an executable postcondition would occur. In any case, the separation logic and executable specifications complement each other in verification. Several examples illustrate the use and utility of the approach.

The above mechanisms are well-suited to automation. We implemented them in MultiStar, a fully automatic verification tool for verifying Eiffel programs. MultiStar uses separation logic specifications that are embedded in the source program. The verification proceeds in two steps. Firstly, the specifications and code are translated into an intermediate representation that is simpler to process. Secondly, a reasoning engine based on jStar [23] performs the actual verification. Section 3.2 describes the architecture of MultiStar and its treatment of export/axiom clauses and multiple inheritance. Section 4.7 explains how it supports reasoning about executable contracts. MultiStar successfully verified the examples in Chapters 3 and 4. Moreover, Section 3.3 describes a case study in which MultiStar was used to verify the core iterator hierarchy of a popular data structure library. MultiStar is freely available as part of the EVE (Eiffel Verification Environment) download [28].

Chapter 5 tackles refinement and correctness by construction. The approach is to obtain a refinement calculus for OO commands, and then to use it as part of a larger calculus that handles other OO features. This reflects the fact that developing a routine body involves different reasoning than, say, adding an attribute to a class or a dynamic specification to a routine.

It is moreover a cost-effective way to structure a solution, because the refinement calculus comes for free! Section 5.1 describes *freefinement*, an algorithm that constructs a sound refinement calculus from a verification system under certain conditions. In this work, a verification system is any formal system for establishing whether an inductively defined term, such as a command, satisfies a specification. Examples of verification systems include

Hoare logics and type systems. Freefinement first extends the term language to include specification terms, and builds a verification system for the extended language that is a sound and conservative extension of the original system. The extended system is then transformed into a sound refinement calculus. The resulting refinement calculus can interoperate closely with the verification system – it is even possible to reuse and translate proofs between them. Freefinement gives a semantics to refinement at an abstract level: it associates each term of the extended language with a set of terms from the original language, and refinement simply reduces this set. For illustration purposes, Section 5.2 applies freefinement to a simple type system for the lambda calculus and also to a Hoare logic.

It is straightforward to apply freefinement to the part of the separation logic proof system that reasons about OO commands. The resulting refinement calculus is plugged into a larger system that deals with other OO constructs, and this larger calculus is compatible with (and complements) the full separation logic system. In particular, all its transformation steps preserve correctness with respect to the system, and every verifiable OO program can be constructed from scratch in a stepwise manner.

Many of the results in this thesis are quite general and also apply to different settings. None of the contributions rely on a particular flavour of separation logic. The work on executable contracts can be used for non-OO languages with explicit memory management such as C. Finally, freefinement can give refinement calculi for a great variety of formal systems, including modular type systems such as System F.

# Chapter 2

### BACKGROUND MATERIAL

This chapter describes the OO programming language and its separation logic proof system that are used throughout the thesis. It summarises the published work of Parkinson [57] and his joint work with Bierman [58] with minor changes in the presentation.

### 2.1 Language syntax

The grammar of our kernel OO language with specifications is shown in Figure 2.1. It is deliberately minimal to keep the presentation simple, but it contains the essential language constructs of popular OO languages. The concrete notation is based on Eiffel [26].

A class is divided into top-level sections. The **inherit** section lists its parent class (or 'ANY'), the **define** section contains predicate definitions, and methods and fields are written in the **feature** section. Empty sections and '**inherit** ANY' will simply be omitted in examples.

A sequence of c's is denoted by  $\overline{c}$ . The letters G and H are used for class names, m for method names, and f for field names. Variables are denoted by u, x, y and z. **Void** corresponds to 'null' in other languages. Two reserved program variables **Current** and **Result** denote the current object ('this') and the result of a function call respectively. **Current** is never **Void**. The term *feature* describes methods and fields, sometimes called the 'members' of a class. Feature overloading, including method overloading, is not allowed.

Separate namespaces exist for class names, p, m and f. We assume the absence of clashes when names are introduced. This precludes method overloading and field shadowing. Later, when we extend the language with multiple inheritance, it will guarantee that methods or fields with the same name in parent classes stem from common ancestors. L ::=class G inherit H define  $\overline{D}$  feature  $\overline{M} \overline{F}$  end D ::=  $x.p_G(\overline{t:y})$  as P Define clause M ::= introduce m(Args) Rt Sd Ss BMethod declaration | override m(Args) Rt Sd Ss B | inherit m(Args) Rt Sd Ss F ::= f: Type Field declaration Sd ::= dynamic SpecDynamic specification Ss ::= static SpecStatic specification Spec ::=  $\{P\}_{Q} | \{P\}_{Q}$ also Spec Specification B ::= do s endMethod body s ::= skipStatement  $|\mathbf{x} := \mathbf{e}$ Assignment Sequential composition |s;s|if e then s else s end Conditional while e do s end Loop x: Type. s Local variable block x := new G**Object** allocation  $\mathbf{x} := \mathbf{y}.\mathbf{f}$ Field lookup x.f := eField update  $| \mathbf{x} := \mathbf{y}.\mathbf{m}(\overline{\mathbf{e}}) | \mathbf{y}.\mathbf{m}(\overline{\mathbf{e}})$ Dynamically dispatched call  $| \mathbf{x} := \mathbf{y}.\mathbf{G}::\mathbf{m}(\overline{\mathbf{e}}) | \mathbf{y}.\mathbf{G}::\mathbf{m}(\overline{\mathbf{e}})$ Direct method call  $e ::= x | e + e | e = e | Void | 0 | 1 | 2 \dots$ Expression Type ::=  $INT \mid BOOL \mid G$ Args ::=  $\overline{\mathbf{x}: \mathrm{Type}}$ Formal arguments Rt ::=  $\epsilon$  | : Type Return type

Figure 2.1: The kernel language grammar.

A constructor is simply an introduced method m where m is a class name. Except for the restriction that subclasses cannot inherit or override constructors, no special treatment is needed otherwise. In the examples, we will omit the target of a method call or field assignment if it is **Current**.

Methods have both **static** and **dynamic** specifications. A specification is written in pre-post form  $\{P\}_{-}\{Q\}$ , or  $\{P_1\}_{-}\{Q_1\}$  **also**  $\{P_2\}_{-}\{Q_2\}$  to indicate that both are satisfied (Section 2.3 shows how the **also** form abbreviates a single pre-post specification). A method's dynamic specification must be satisfied by all subclasses, and is used to verify dynamically-dispatched calls. A static specification describes properties about the particular method body, and is used to verify statically-dispatched calls, including **Precursor** ('super' or 'base') calls and direct calls x.C::m( $\bar{e}$ ) in C++ style.

To provide subclasses with the opportunity to respecify a method and to simplify the proof rules that follow later, we require a subclass to inherit or override explicitly all non-constructor methods present in its parent (in the examples of later chapters and the MultiStar tool, specification shorthands are employed to achieve this).

We assume the formal argument names of methods stay the same in subclasses. This simplifies the proof rules that follow, which would otherwise need additional substitutions.

Finally, to improve the readability of examples in later chapters, we frequently write statements in a list and omit the interspersed sequential composition operators.

### 2.2 Logic syntax and semantics

The predicates used in specifications and proofs have the following grammar.

P, Q, S, T, 
$$\Delta ::= \forall x \cdot P \mid P \Rightarrow Q \mid false \mid e = e' \mid x : G \mid x <: G \mid x.f \hookrightarrow e \mid P * Q \mid x.p(\overline{t:e}) \qquad Apf \ predicate \mid x.p_G(\overline{t:e}) \qquad Apf \ entry$$

The predicate x : G means x references an object whose dynamic type is exactly G, and x <: G means x references an object whose dynamic type is a subtype of G. In both cases  $x \neq Void$ , and  $x : G \Rightarrow x <: G$  holds. Within a context, if x is declared of type G then x <: G whenever  $x \neq Void$ .

Abstract predicate families [56, 58] provide logical abstractions of state for the OO setting. An abstract predicate family (abbreviated apf) provides an abstract predicate p for which each class C can define an entry  $p_C$ . The first argument of an apf predicate or entry is called the *root*. The root followed by a dot is written before the apf predicate or entry name. The whole prefix is omitted when the root is **Current**. An apf predicate's root can never be **Void**. Since the meaning of an apf predicate depends on the dynamic type of the root object, it can be seen as mirroring dynamic dispatch of object-orientation in the logic. The other arguments of an apf predicate or entry are a set (i.e. order-independent collection) of tagged arguments, where tag names t provide useful hints about the purpose of tagged values. Apfs offer high levels of abstraction in specifications and proofs: apf predicates and entries are treated abstractly by the clients of a class.

Other predicates have the usual intuitionistic separation logic semantics. Informally, the predicate  $x.f \hookrightarrow e$  means that the f field of object x has value e (f  $\hookrightarrow$  e abbreviates **Current**.f  $\hookrightarrow$  e), and P \* Q means that P and Q hold for disjoint portions of the heap. Readers are referred to [60, 54, 57] for a formal treatment of separation logic. Symbols such as  $\Leftrightarrow$ ,  $\neg$ , true,  $\lor$ ,  $\land$  and  $\exists$  are encoded in the standard way. Every occurrence of \_ in a predicate denotes a fresh existentially quantified variable, where the quantifier is placed in the innermost position. FV(P) denotes the free variables of P; every method precondition P must satisfy **Result**  $\notin FV(P)$ .

In the rest of the formalisation, the symbols P, Q, S and T are used for assertions and predicates, and  $\Delta$  for assumptions.

#### 2.3 Specification refinement

A specification  $\{P_1\}_{Q_1}$  is refined by  $\{P_2\}_{Q_2}$  when any statement s that satisfies  $\{P_1\}_{Q_1}$  also satisfies  $\{P_2\}_{Q_2}$ . Under the assumptions  $\Delta$ , the specification  $\{P_1\}_{Q_1}$  is refined by  $\{P_2\}_{Q_2}$  if we can prove  $\Delta \vdash \{P_1\}_{Q_1} \Longrightarrow \{P_2\}_{Q_2}$ , i.e. provide a proof tree with leaves  $\Delta \vdash$  $\{P_1\}_{Q_1}$  and root  $\Delta \vdash \{P_2\}_{Q_2}$  built with the structural rules of separation logic (Consequence, Frame, Auxiliary Variable Elimination, Disjunction, and others). In the context of method specification refinement,  $\Delta$  contains the apf assumptions of the class, and the Consequence and Frame rules are given by:

$$\frac{\Delta \Rightarrow (\mathbf{P}' \Rightarrow \mathbf{P}) \quad \Delta \vdash \{\mathbf{P}\}_{-}\{\mathbf{Q}\} \quad \Delta \Rightarrow (\mathbf{Q} \Rightarrow \mathbf{Q}')}{\Delta \vdash \{\mathbf{P}'\}_{-}\{\mathbf{Q}'\}} \quad \text{Consequence}$$

$$\frac{\Delta \vdash \{\mathbf{P}\}_{-}\{\mathbf{Q}\}}{\Delta \vdash \{\mathbf{P} \ast \mathbf{T}\}_{-}\{\mathbf{Q} \ast \mathbf{T}\}} \quad \text{Frame}$$

The Frame rule is applicable whenever **Result**  $\notin FV(T)$ , and expresses that disjoint portions of the heap stay unchanged.

Method specifications can be combined with **also** (Definition 1 in [58]):

$$\begin{array}{l} \{P_1\}_{-}\{Q_1\} \text{ also } \{P_2\}_{-}\{Q_2\} \stackrel{def}{=} \\ \{(P_1 \wedge x = 1) \lor (P_2 \wedge x \neq 1)\}_{-}\{(Q_1 \wedge x = 1) \lor (Q_2 \wedge x \neq 1)\} \end{array}$$

where x denotes a fresh auxiliary variable. The specifications  $\{P_1\}_{Q_1}$  and  $\{P_2\}_{Q_2}$  are equivalent w.r.t.  $\Delta$  iff both  $\Delta \vdash \{P_1\}_{Q_1} \implies \{P_2\}_{Q_2}$  and  $\Delta \vdash \{P_2\}_{Q_2} \implies \{P_1\}_{Q_1}$ . Two specifications are equivalent iff they are equivalent w.r.t. all  $\Delta$ . It can be shown that **also** is commutative, associative and idempotent modulo equivalence with identity  $\{false\}_{true}$ . The notation  $also_{i\in I} \{P_i\}_{Q_i}$  denotes the specification  $\{P_{e_1}\}_{Q_{e_1}}$  also  $\dots$  also  $\{P_{e_m}\}_{Q_{e_m}}$ , where  $e_1 \dots e_m$  are the elements of the finite index set I. Furthermore, when I is the empty set:

$$also_{i \in \emptyset} \{P_i\}_{-}\{Q_i\} \stackrel{der}{=} \{false\}_{-}\{true\}$$

It always holds that  $\Delta \vdash \{P\}_{-}\{Q\} \implies \{false\}_{-}\{true\}$ . Other useful lemmas involving **also** are given in Section 2.8. Finally, we use the abbreviation

$$\Delta \vdash \{P_1\}_{-}\{Q_1\} \stackrel{\operatorname{\mathbf{Current}}:G}{\Longrightarrow} \{P_2\}_{-}\{Q_2\}$$
$$\stackrel{\text{def}}{=} \Delta \vdash \{P_1\}_{-}\{Q_1\} \Longrightarrow \{P_2 * \operatorname{\mathbf{Current}}:G\}_{-}\{Q_2\}$$

in the formalisation of the Dynamic dispatch proof obligation for methods in Section 2.6.

#### 2.4 The specification environment

Most of the proof rules that follow use an environment  $\Gamma$ , which maps method names to their specifications for all classes in a program:

 $\begin{array}{ll} \Gamma ::= \operatorname{G.m} \mapsto (\overline{x}, \{P\}_{-}\{Q\}) & \qquad \text{Method dynamic specification} \\ \mid \operatorname{G::m} \mapsto (\overline{x}, \{S\}_{-}\{T\}) & \qquad \text{Method static specification} \\ \mid \overline{\Gamma} & \qquad \end{array}$ 

The  $\overline{x}$  in a specification of m denote its formal argument names.  $\Gamma$  is guaranteed to be a partial function for well-formed programs, and we write  $\Gamma(G.m) = (\overline{x}, \{P\}_{-}\{Q\})$  for  $G.m \mapsto (\overline{x}, \{P\}_{-}\{Q\}) \in \Gamma$ , and  $\Gamma(G::m) = (\overline{x}, \{S\}_{-}\{T\})$  for  $G::m \mapsto (\overline{x}, \{S\}_{-}\{T\}) \in \Gamma$ .

#### 2.5 Statement verification

The assumptions  $\Delta$  used to verify statements contain apf information about the enclosing class. The rules for most statements are standard:

$$\begin{array}{l} \Delta;\Gamma\vdash_{s}\{\mathrm{P}\}\mathbf{skip}\{\mathrm{P}\}\\\\\hline\hline\Delta;\Gamma\vdash_{s}\{\mathrm{P}[\mathrm{e}/\mathrm{x}]\}\mathrm{x}:=\mathrm{e}\{\mathrm{P}\}\\\\\hline\underline{\Delta};\Gamma\vdash_{s}\{\mathrm{P}\}\mathrm{s}\{\mathrm{Q}\}\quad\Delta;\Gamma\vdash_{s}\{\mathrm{Q}\}\mathrm{s}'\{\mathrm{R}\}\\\\\hline\Delta;\Gamma\vdash_{s}\{\mathrm{P}\}\mathrm{s};\mathrm{s}'\{\mathrm{R}\}\\\\\hline\underline{\Delta};\Gamma\vdash_{s}\{\mathrm{P}*\mathrm{e}\}\mathrm{s}\{\mathrm{Q}\}\quad\Delta;\Gamma\vdash_{s}\{\mathrm{P}*\neg\mathrm{e}\}\mathrm{s}'\{\mathrm{Q}\}\\\\\hline\Delta;\Gamma\vdash_{s}\{\mathrm{P}\}\mathrm{if}\ \mathrm{e}\ \mathrm{then}\ \mathrm{s}\ \mathrm{else}\ \mathrm{s}'\ \mathrm{end}\{\mathrm{Q}\}\\\\\hline\underline{\Delta};\Gamma\vdash_{s}\{\mathrm{P}\}\mathrm{while}\ \mathrm{e}\ \mathrm{do}\ \mathrm{s}\ \mathrm{end}\{\mathrm{P}*\neg\mathrm{e}\}\\\\\hline\Delta;\Gamma\vdash_{s}\{\mathrm{P}\}\mathrm{while}\ \mathrm{e}\ \mathrm{do}\ \mathrm{s}\ \mathrm{end}\{\mathrm{P}*\neg\mathrm{e}\}\end{array}$$

The rule for local variables has the proviso that 
$$x \notin FV(P) \cup FV(Q)$$
:

$$\frac{\Delta; \Gamma \vdash_{s} \{P\}s\{Q\}}{\Delta; \Gamma \vdash_{s} \{P\}x: \text{Type. }s\{Q\}}$$

In the rule for object allocation, allfields(G) denotes the set of field names listed in G and all its ancestors:

$$allfields(G) = \{f_1, f_2, \dots, f_n\}$$
$$\Delta; \Gamma \vdash_s \{true\}$$
$$x := \mathbf{new} G$$
$$\{x.f_1 \hookrightarrow \cdot * x.f_2 \hookrightarrow \cdot * \dots * x.f_n \hookrightarrow \cdot * x:G\}$$

For field lookup, when x is not free in e and not the same as y, we have:

$$\Delta; \Gamma \vdash_s \{ y.f \hookrightarrow e \} x := y.f \{ y.f \hookrightarrow e * x = e \}$$

Field update is simple:

$$\Delta; \Gamma \vdash_s \{ \mathbf{x}.\mathbf{f} \hookrightarrow_{-} \} \mathbf{x}.\mathbf{f} := \mathbf{e} \{ \mathbf{x}.\mathbf{f} \hookrightarrow \mathbf{e} \}$$

Dynamically dispatched calls use the dynamic specs of methods in  $\Gamma$ , while direct calls use the static ones. Provided x is not y and x is not free in  $\overline{e}$ , the rules for result-returning calls are:

$$\begin{split} \Gamma(\mathbf{G}.\mathbf{m}) &= (\overline{\mathbf{u}}, \{\mathbf{P}\}_{-}\{\mathbf{Q}\})\\ \Delta; \Gamma \vdash_{s} \{\mathbf{P}[\mathbf{y}, \overline{\mathbf{e}} / \mathbf{Current}, \overline{\mathbf{u}}] * \mathbf{y} <: \mathbf{G}\}\\ \mathbf{x} &:= \mathbf{y}.\mathbf{m}(\overline{\mathbf{e}})\\ \{\mathbf{Q}[\mathbf{y}, \overline{\mathbf{e}}, \mathbf{x} / \mathbf{Current}, \overline{\mathbf{u}}, \mathbf{Result}]\} \end{split}$$

$$\Gamma(G::m) = (\overline{u}, \{S\}_{-}\{T\})$$
  
$$\Delta; \Gamma \vdash_{s} \{S[y, \overline{e}/Current, \overline{u}] * y \neq Void\}$$
  
$$x := y.G::m(\overline{e})$$
  
$$\{T[y, \overline{e}, x/Current, \overline{u}, Result]\}$$

The important structural rules for this thesis are Frame, Consequence, Auxiliary Variable Elimination, and Disjunction. The Frame rule is the key to local reasoning. Provided s modifies no variable in FV(T):

$$\frac{\Delta; \Gamma \vdash_{s} \{P\}s\{Q\}}{\Delta; \Gamma \vdash_{s} \{P * T\}s\{Q * T\}}$$
 Frame

The rule of Consequence allows the use of assumptions  $\Delta$ :

$$\frac{\Delta \Rightarrow (\mathbf{P}' \Rightarrow \mathbf{P}) \qquad \Delta; \Gamma \vdash_s \{\mathbf{P}\} \mathbf{s}\{\mathbf{Q}\} \qquad \Delta \Rightarrow (\mathbf{Q} \Rightarrow \mathbf{Q}')}{\Delta; \Gamma \vdash_s \{\mathbf{P}'\} \mathbf{s}\{\mathbf{Q}'\}} \quad \text{Consequence}$$

In Auxiliary Variable Elimination, the variable x to be eliminated may not appear free in s.

$$\frac{\Delta; \Gamma \vdash_{s} \{P\}s\{Q\}}{\Delta; \Gamma \vdash_{s} \{\exists x.P\}s\{\exists x.Q\}}$$
AuxVarElim

Finally, the Disjunction rule is standard:

\_

$$\frac{\Delta; \Gamma \vdash_s \{P\}s\{Q\} \quad \Delta; \Gamma \vdash_s \{P'\}s\{Q'\}}{\Delta; \Gamma \vdash_s \{P \lor P'\}s\{Q \lor Q'\}} \text{ Disjunction}$$

### 2.6 Method verification

As for statement verification, the assumptions  $\Delta$  used to verify method definitions contain apf information about the method's enclosing class.

A newly introduced method's static and dynamic specifications must be consistent<sup>1</sup>, and its body must satisfy the static specification. These two requirements are captured by the Dynamic dispatch [D.d.] and Body verification [B.v.] proof obligations respectively.

<sup>&</sup>lt;sup>1</sup>We establish that the dynamic specification follows from the static one when the dynamic type of **Current** is G. If the dynamic type of x is G, then the body of m in G will be executed if x.m is called. The static and dynamic specifications must be consistent with each other in this case, since the dynamic specification is used for reasoning about the call statement, whereas the body was verified only w.r.t. the static specification.

$$\begin{split} \mathbf{B} &= \mathbf{do \ s \ end} \\ \mathrm{Sd} &= \mathbf{dynamic} \ \{\mathbf{P}_{\mathrm{G}}\}_{-} \{\mathbf{Q}_{\mathrm{G}}\} \\ \mathrm{Ss} &= \mathbf{static} \ \{\mathbf{S}_{\mathrm{G}}\}_{-} \{\mathbf{T}_{\mathrm{G}}\} \\ \Delta &\vdash \{\mathbf{S}_{\mathrm{G}}\}_{-} \{\mathbf{T}_{\mathrm{G}}\} \xrightarrow{\mathbf{Current}: \mathbf{G}} \{\mathbf{P}_{\mathrm{G}}\}_{-} \{\mathbf{Q}_{\mathrm{G}}\} \qquad [\mathrm{D.d.}] \\ \Delta; \boldsymbol{\Gamma} &\vdash_{s} \ \{\mathbf{S}_{\mathrm{G}}\}_{s} \{\mathbf{T}_{\mathrm{G}}\} \qquad [\mathrm{B.v.}] \end{split}$$

$$\Delta; \Gamma \vdash_m$$
**introduce** m(Args) Rt Sd Ss B in G parent H

The next rule is used whenever a method body gets redefined. Consistency must be proven between the new dynamic specification and those in the parent class; this is embodied in the Behavioural subtyping [B.s.] proof obligation. The other proof obligations are identical to those for method introduction above.

$$\begin{split} \Gamma(\mathrm{H.m}) &= (\overline{\mathbf{x}}, \{\mathrm{P}_{\mathrm{H}}\}_{-}\{\mathrm{Q}_{\mathrm{H}}\})\\ \mathrm{B} &= \mathbf{do} \ \mathbf{s} \ \mathbf{end}\\ \mathrm{Sd} &= \mathbf{dynamic} \ \{\mathrm{P}_{\mathrm{G}}\}_{-}\{\mathrm{Q}_{\mathrm{G}}\}\\ \mathrm{Ss} &= \mathbf{static} \ \{\mathrm{S}_{\mathrm{G}}\}_{-}\{\mathrm{T}_{\mathrm{G}}\}\\ \Delta &\vdash \{\mathrm{P}_{\mathrm{G}}\}_{-}\{\mathrm{Q}_{\mathrm{G}}\} \Longrightarrow \ \{\mathrm{P}_{\mathrm{H}}\}_{-}\{\mathrm{Q}_{\mathrm{H}}\} \qquad [\mathrm{B.s.}]\\ \Delta &\vdash \{\mathrm{S}_{\mathrm{G}}\}_{-}\{\mathrm{T}_{\mathrm{G}}\} \xrightarrow{\mathbf{Current}: \ \mathrm{G}} \ \{\mathrm{P}_{\mathrm{G}}\}_{-}\{\mathrm{Q}_{\mathrm{G}}\} \qquad [\mathrm{D.d.}]\\ \Delta; \Gamma &\vdash_{s} \ \{\mathrm{S}_{\mathrm{G}}\}_{\mathrm{S}}\{\mathrm{T}_{\mathrm{G}}\} \qquad [\mathrm{B.v.}] \end{split}$$

 $\Delta; \Gamma \vdash_m$  override m(Args) Rt Sd Ss B in G parent H

When a method is inherited, its static specification must follow from the one in the parent class. The Inheritance [Inh.] obligation ensures that this will be the case. The Behavioural subtyping and Dynamic dispatch obligations serve the same purposes as before.

$$\begin{split} &\Gamma(\mathrm{H.m}) = (\overline{x}, \{\mathrm{P}_{\mathrm{H}}\}_{-} \{\mathrm{Q}_{\mathrm{H}}\}) \\ &\Gamma(\mathrm{H::m}) = (\overline{x}, \{\mathrm{S}_{\mathrm{H}}\}_{-} \{\mathrm{T}_{\mathrm{H}}\}) \\ &\mathrm{Sd} = \mathbf{dynamic} \ \{\mathrm{P}_{\mathrm{G}}\}_{-} \{\mathrm{Q}_{\mathrm{G}}\} \\ &\mathrm{Ss} = \mathbf{static} \ \{\mathrm{S}_{\mathrm{G}}\}_{-} \{\mathrm{T}_{\mathrm{G}}\} \\ &\Delta \vdash \{\mathrm{P}_{\mathrm{G}}\}_{-} \{\mathrm{Q}_{\mathrm{G}}\} \Longrightarrow \ \{\mathrm{P}_{\mathrm{H}}\}_{-} \{\mathrm{Q}_{\mathrm{H}}\} \\ &\Delta \vdash \{\mathrm{S}_{\mathrm{H}}\}_{-} \{\mathrm{T}_{\mathrm{H}}\} \Longrightarrow \{\mathrm{S}_{\mathrm{G}}\}_{-} \{\mathrm{T}_{\mathrm{G}}\} \\ &\Delta \vdash \{\mathrm{S}_{\mathrm{G}}\}_{-} \{\mathrm{T}_{\mathrm{G}}\} \qquad [\mathrm{Inh.}] \\ &\Delta \vdash \{\mathrm{S}_{\mathrm{G}}\}_{-} \{\mathrm{T}_{\mathrm{G}}\} \stackrel{\mathbf{Current}:\,\mathrm{G}}{\Longrightarrow} \ \{\mathrm{P}_{\mathrm{G}}\}_{-} \{\mathrm{Q}_{\mathrm{G}}\} \qquad [\mathrm{D.d.}] \end{split}$$

 $\Delta; \Gamma \vdash_m$  inherit m(Args) Rt Sd Ss in G parent H

### 2.7 Class and program verification

A class is verified by verifying all of its methods. Once again, the formula  $\Delta$  contains apf information that is specific to the class.

 $\forall M_i \in \overline{M} \cdot \Delta; \Gamma \vdash_m M_i \text{ in } G \text{ parent } H$ 

 $\Delta; \Gamma \vdash_c$ class G inherit H define  $\overline{D}$  feature  $\overline{M} \overline{F}$  end

Finally, here is the rule for program verification:

 $\forall i \in 1..n \cdot L_{i} = class G_{i} \dots end$  $\Gamma = specs(L_{1} \dots L_{n})$  $\forall i \in 1..n \cdot apf(L_{i}); \Gamma \vdash_{c} L_{i}$  $true; \Gamma \vdash_{s} \{true\}s\{true\}$  $\vdash_{p} L_{1} \dots L_{n} s$ 

The function apf translates the abstract predicate family definitions of a class into a formula – its apf assumptions. It is adapted from [58] for tagged arguments:

 $\begin{array}{l} apf(\mathbf{class} \ \mathbf{G} \ \dots \mathbf{define} \ \mathbf{D}_1 \ \mathbf{D}_2 \ \dots \mathbf{D}_n \ \mathbf{feature} \ \dots \mathbf{end}) \ \stackrel{\text{def}}{=} \\ apf_{\mathbf{G}}(\mathbf{D}_1) \land \dots \land \ apf_{\mathbf{G}}(\mathbf{D}_n) \\ apf_{\mathbf{G}}(\mathbf{x}.\mathbf{p}_{\boldsymbol{G}}(\mathbf{Y}) \ \mathbf{as} \ \mathbf{P}) \ \stackrel{\text{def}}{=} \\ FtoE(\mathbf{p},\mathbf{G},\mathbf{Y}) \land EtoD(\mathbf{x}.\mathbf{p}_{\boldsymbol{G}}(\mathbf{Y}) \ \mathbf{as} \ \mathbf{P}) \land (\forall \mathbf{x} <: \mathbf{G} \ \cdot \ TR(\mathbf{p},\mathbf{x},\mathbf{Y})) \\ FtoE(\mathbf{p},\mathbf{G},\overline{\mathbf{t}}:\mathbf{y}) \ \stackrel{\text{def}}{=} \\ \forall \mathbf{x},\overline{\mathbf{y}} \cdot \mathbf{x} : \ \mathbf{G} \Rightarrow [\mathbf{x}.\mathbf{p}(\overline{\mathbf{t}}:\mathbf{y}) \Leftrightarrow \mathbf{x}.\mathbf{p}_{\boldsymbol{G}}(\overline{\mathbf{t}}:\mathbf{y})] \\ EtoD(\mathbf{x}.\mathbf{p}_{\boldsymbol{G}}(\overline{\mathbf{t}}:\mathbf{y}) \ \mathbf{as} \ \mathbf{P}) \ \stackrel{\text{def}}{=} \\ \forall \mathbf{x},\overline{\mathbf{y}} \cdot \mathbf{x}.\mathbf{p}_{\boldsymbol{G}}(\overline{\mathbf{t}}:\mathbf{y}) \Rightarrow \mathbf{P} \\ TR(\mathbf{p},\mathbf{x},\overline{\mathbf{t}}:\mathbf{y}) \ \stackrel{\text{def}}{=} \\ \land_{\overline{\mathbf{t}'}:\mathbf{y'}} + \overline{\mathbf{t}'':\mathbf{y''}} \equiv \overline{\mathbf{t}}:\mathbf{y}} \ \forall \overline{\mathbf{y'}} \cdot \mathbf{x}.\mathbf{p}(\overline{\mathbf{t}':\mathbf{y'}}) \Leftrightarrow \mathbf{x}.\mathbf{p}(\overline{\mathbf{t}':\mathbf{y'}} + \ \overline{\mathbf{t}'':\mathbf{y}}) \\ \end{array}$ 

#### 2.8 Useful lemmas

Lemmas 2.1 and 2.2 are frequently used in proofs of Behavioural Subtyping and Inheritance:

Lemma 2.1.  $\Delta \vdash (also_{i \in I} \{P_i\} \{Q_i\}) \Longrightarrow \{P_k\} \{Q_k\}$  for all  $k \in I$ .

**Lemma 2.2.** If  $\Delta \vdash \{P\}_{-}\{Q\} \Longrightarrow \{S_i\}_{-}\{T_i\}$  for all  $i \in I$ , then  $\Delta \vdash \{P\}_{-}\{Q\} \Longrightarrow (also_{i \in I} \{S_i\}_{-}\{T_i\}).$ 

For Body Verification:

**Lemma 2.3.** If  $\Delta; \Gamma \vdash_s \{S_i\} s\{T_i\}$  for all  $i \in I$ , then under assumptions  $\Delta$  and  $\Gamma$ , statement s satisfies (also<sub>i \in I</sub>  $\{S_i\}_{-}\{T_i\}$ ).

## CHAPTER 3

# REASONING ABOUT RELATED ABSTRACTIONS

The use of data abstractions is a hallmark of OO programming. A class is a typical example of such an abstraction. In interface or general multiple inheritance hierarchies, a class can combine and maintain several abstractions offered by its parents. Although most examples of this chapter involve abstractions in connection with inheritance, not all data abstractions are directly coupled with language constructs. Classes use them for various purposes: to simplify how clients manipulate a class, to separate various concepts that are combined in a class, or to encourage or enforce particular call protocols. For example, a complex object with a long initialisation phase can use the abstractions 'initialising' and 'ready', with methods applicable to an 'initialising' object, others to a 'ready' object, and some to both. Or algorithms can manipulate a mutable data structure under an 'immutable' abstraction, even if there is no interface making this explicit.

Relationships between data abstractions are important when reasoning about OO code. This chapter explores the problem of relating abstractions in an information hiding setting, where implementation details of abstractions are hidden from clients. Suppliers must therefore express and fulfill relationships between abstractions. If a class offers 'student' and 'person' abstractions (by using inheritance, or other means), for example, it might allow clients to convert a 'student' abstraction into a 'person' one. Clients can then manipulate the 'person' abstraction by calling e.g. library routines. After the manipulation, they might be allowed to convert back and assume that the number of exams the 'student' has taken is still the same. Specification mechanisms are needed to express and enforce the programmer's intentions about such relationships. This is especially important in multiple inheritance hierarchies where classes combine multiple abstractions in complicated ways.

Abstract predicate families are a flexible mechanism for data abstraction in OO specifications. An apf P provides a predicate name for an abstraction; each class C can define an *entry* predicate  $P_C$ . The definition of  $P_C$  describes how class C implements the apf P, and is hidden from other classes. For example, apfs S and P can be used to provide an abstraction of students and persons in a program respectively. Class STUDENT can define the entries  $S_{STUDENT}$  and  $P_{STUDENT}$ , while other classes can define their apf entries differently. The predicate x.S(age:a, exm:e) describes object x under the 'student' abstraction: its age is 'a' and the number of exams taken is 'e'. If the dynamic type of x is STUDENT, then class STUDENT can use the fact that

 $x.S(age: a, exm: e) \Leftrightarrow x.S_{STUDENT}(age: a, exm: e)$ 

In other words, the dynamic type of the first argument of an apf predicate (in this case the dynamic type of x) determines which apf entry applies. Apf predicates can therefore be seen to mirror dynamic dispatch of OO programs in the logic. The apf mechanism is modular and exercises information hiding: only the class defining an apf entry knows the definition and can relate its entry to the apf predicate.

The relationship described before, namely that a 'student' abstraction can be converted into a 'person' one, can be expressed as follows:

$$x.S(age:a, exm:e) \Rightarrow x.P(age:a)$$

Allowing the back conversion without affecting the number of exams requires a stronger property that uses separation logic's \*-connective:

$$x.S(age:a, exm:e) \Leftrightarrow [x.P(age:a) * x.RestStoP(exm:e)]$$
(A)

where RestStoP abstracts the parts of a 'student' abstraction that are independent from and not included in a 'person' one. With this property, a client can now reason as the following proof outline shows:

 $\begin{aligned} &\{x.S(age:a, exm:e)\} \\ &\{x.P(age:a) * x.RestStoP(exm:e)\} \\ &\{x.P(age:a)\} \\ & // \text{ Manipulation of 'person' abstraction by library routines.} \\ &\{x.P(age:a+1)\} \\ &\{x.P(age:a+1) * x.RestStoP(exm:e)\} \\ &\{x.S(age:a+1, exm:e)\} \end{aligned}$ 

The Frame rule of separation logic guarantees that the disjoint x.RestStoP(exm:e) remains unchanged. In essence, the client uses property (A) in combination with the Frame rule to infer an S-based specification for the library manipulation. It is not necessary to re-specify or re-verify the library – knowledge of the relationship saves specification overhead and keeps reasoning modular.

To which objects the property (A) applies is a design choice: a programmer might express that *selected* classes in a heterogeneous hierarchy fulfill the relationship, or that *all* classes in a homogeneous hierarchy fulfill it. We introduce two general specification mechanisms for the two cases: *export clauses* to express properties that hold for individual classes, and *axiom clauses* to describe properties of entire hierarchies. If class STUDENT specifies

#### export

 $\forall x, a, e \cdot x : STUDENT \Rightarrow [x.S(age: a, exm: e) \Leftrightarrow \\ [x.P(age: a) * x.RestStoP(exm: e)]]$ 

#### where {}

then a client must know that an object's dynamic type is exactly STUDENT before using the information in reasoning. On the other hand, if class STU-DENT specifies

#### axiom

 $S_P: \forall a, e \in S(age: a, exm: e) \Leftrightarrow [P(age: a) * RestStoP(exm: e)]$ 

then clients can use the stronger implication

 $\forall x, a, e \cdot x <: STUDENT \Rightarrow \\ [x.S(age: a, exm: e) \Leftrightarrow [x.P(age: a) * x.RestStoP(exm: e)]]$ 

Knowledge that the object's dynamic type is a subtype of STUDENT (including STUDENT) suffices to use the relationship. This is much more convenient for clients: a sound OO type system will guarantee that if a variable has static type STUDENT and references an object, then the object's dynamic type will always be a subtype of STUDENT.

Axiom clauses offer a general facility to constrain the implementation of abstractions in subclasses. For example, class STUDENT can express that the number of exams a 'student' has taken is always non-negative, and that all subclasses should use its implementation of the 'student' abstraction:

#### axiom

 $\begin{array}{l} \operatorname{exm\_non\_neg:} \forall a, e \cdot \ S(age: a, exm: e) \Rightarrow 0 \leq e \\ \operatorname{S\_constraint:} \forall a, e \cdot \ S(age: a, exm: e) \Leftrightarrow \\ \end{array} \\ \begin{array}{l} S_{TUDENT}(age: a, exm: e) \end{array}$ 

Axiom clause 'exm\_non\_neg' guarantees clients that  $0 \le e$  whenever they know x.S(age: a, exm: e) and x <: STUDENT. Subclass representation constraints such as the one expressed in the 'S\_constraint' clause are useful for

ensuring safe interaction between statically and dynamically dispatched calls on the same object – a pervasive pattern in OO programs (see Section 5.5of [58]).

The claims made in export and axiom specifications must be checked to obtain sound reasoning. Whether or not a class fulfills axiom clauses often depends on properties of particular other classes, such as its parents. For this reason our proof system has a layered assumption structure: axiom verification can use export information of all classes in a program, and method verification can additionally use axiom information. Several examples will show how export and axiom clauses are verified and applied in verification.

This chapter extends the proof system of Parkinson and Bierman (see chapter 2) with export/axiom clauses, abstract classes, abstract methods and shared multiple inheritance<sup>1</sup> where fields and methods of common ancestor classes are not replicated in the descendant [26]. Apart from the use of apfs to support abstraction and information hiding, Parkinson and Bierman's system has the attractive property that it can verify a wide range of inheritance uses and abuses. Flexible handling of inheritance is vital in a proof system for multiple inheritance, since classes often interrelate methods and data from parents in complicated ways: methods can intertwine ancestor abstractions as the example in Section 3.1.1 shows, or abstractions can share parts in the case of diamond inheritance. Export and axiom clauses provide flexible ways to relate abstractions, thereby giving clients an integrated view of how methods affect abstractions from different inheritance paths. Very few proof systems exist for multiple inheritance, and no proof system we know of can facilitate reasoning about multiple related abstractions at the same level of abstraction as ours.

We implemented our proof system in MultiStar – a fully automatic verification tool. MultiStar has a two-tier architecture: a GUI front-end that translates Eiffel code and specifications into a simpler form for verification, and a language-independent back-end based on jStar [23] for reasoning. The front-end uses specifications written inside classes. Future front-ends for e.g. Java and C# can reuse the MultiStar back-end: with its support for interface inheritance, export/axiom clauses, abstract classes and abstract methods, a wide range of programs can be verified. As we shall see, export and axiom clauses can also be useful in single-inheritance situations.

All the examples presented in this chapter have been verified with Multi-Star. To demonstrate the flexibility of our approach, we also used MultiStar to verify the iterator hierarchy of the Gobo data structure library [29]. The complete code and specifications of the examples and Gobo case study are

<sup>&</sup>lt;sup>1</sup>Inheritance with *virtual base classes* in C++ terminology [27].

available online [28].

**Chapter outline** Several examples illustrating the new specification mechanisms and proof system follow in Section 3.1. Section 3.2 presents the MultiStar tool, and Section 3.3 reports on the case study with Gobo iterator classes. A formal exposition of our proof system appears in Section 3.4. Section 3.5 mentions related work and concludes. Appendix A contains an overview of the formal semantics of our proof system and a proof of soundness.

### 3.1 Examples

A class may inherit from several parent classes. This is indicated by listing the names of all parents in its **inherit** section. The new **export** and **axiom** sections respectively contain the export and axiom clauses of a class. As usual, empty sections will simply be omitted.

We use the method specification shorthands of [58]: if only a static specification is listed, the dynamic specification is assumed to be exactly the same, and if only a dynamic specification is listed, then a static specification is derived by replacing each apf predicate whose first argument is **Current** with the entry predicate of the class. In other words, if the shorthand is used in class C, then  $p(\overline{t:e})$  is replaced with  $p_C(\overline{t:e})$ . Specifications of nonconstructor methods are furthermore propagated down the hierarchy: if a class does not explicitly list an inherited method, then it is assumed to have the same static and dynamic specifications as determined for the parent class. To avoid ambiguity, we require that if the method is available in multiple parents, then they must all have identical specifications for it.

The examples do not discuss details that are uninteresting from the perspective of this thesis, such as proofs of correctness of simple ancestor classes. The paper of Parkinson and Bierman [58] contains several examples that illustrate the basics.

#### 3.1.1 Intertwining ancestor abstractions

Classes CELL and COUNTER are shown in Figure 3.1. CELL models mutable integer-valued cells and uses apf Cell, while COUNTER uses apf Cn. The apfs provide logical abstractions of mutable cells and counters respectively. Class CCELL in Figure 3.2, the focus of this example, inherits from CELL and COUNTER. It intertwines the functionality of its parents by overriding *set\_value* to store the value and increment the count. It uses apf Cc to

```
class CELL
define x.\operatorname{Cell}_{CELL}(\operatorname{val:v}) as x.value \hookrightarrow v
feature
    introduce CELL(v: INT)
    dynamic {value \hookrightarrow _}_{Cell(val: v)}
    do value := v end
    introduce value(): INT
    \mathbf{dynamic} \{ \mathbf{Cell}(val:v) \}_{-} \{ \mathbf{Cell}(val:v) * \mathbf{Result} = v \}
    do Result := value end
    introduce set_value(v: INT)
    dynamic {Cell(val: _)}_{Cell(val: v)}
    do value := v end
    value: INT
end
class COUNTER
define x.Cn_{COUNTER}(cnt:c) as x.count \hookrightarrow c
feature
    introduce COUNTER()
    dynamic {count \hookrightarrow _}_{Cn(cnt:0)}
    do count := 0 end
    introduce count(): INT
    dynamic \{Cn(cnt:c)\} \{Cn(cnt:c) * Result = c\}
    do Result := count end
    introduce increment()
    dynamic \{Cn(cnt:c)\} \{Cn(cnt:c+1)\}
    do tmp: INT. tmp := count; count := tmp + 1 end
    count: INT
end
```

Figure 3.1: The CELL and COUNTER classes.
class CCELL inherit CELL COUNTER define x.Cell<sub>CCELL</sub>(val: v, cnt: c) as x.Cc<sub>CCELL</sub>(val: v, cnt: c) x.Cn<sub>CCELL</sub>(cnt:c) as x.Cn<sub>COUNTER</sub>(cnt:c)  $x.Cc_{CCELL}(val:v, cnt:c)$  as  $x.Cell_{CELL}(val:v) * x.Cn_{COUNTER}(cnt:c)$ export  $\forall x \cdot x : CCELL \Rightarrow [\forall c, v \cdot x.Cc(val:v, cnt:c) \Leftrightarrow x.Cell(val:v, cnt:c) \Leftrightarrow$ (x.Cn(cnt:c) \* Rest(x,v))] where {  $Rest(x,v) = x.Cell_{CELL}(val:v)$  } feature introduce CCELL(v: INT) dynamic {value  $\hookrightarrow$  \_ \* count  $\hookrightarrow$  \_}\_{Cc(val: v, cnt: 0)} do Precursor{CELL}(v); Precursor{COUNTER}() end **inherit** value(): INT dynamic {Cc(val: v, cnt: c)}-{Cc(val: v, cnt: c) \* Result = v} also {Cell(val: v, cnt: c)}\_{Cell(val: v, cnt: c) \* Result = v} override set\_value(v: INT) dynamic {Cc(val: ., cnt: c)} {Cc(val: v, cnt: c + 1)} **also** {Cell(val: -, cnt: c)}\_{Cell(val: v, cnt: c + 1)} **do** CCELL::increment(); CELL::set\_value(v) **end inherit** count(): INT dynamic {Cc(val:v, cnt:c)}-{Cc(val:v, cnt:c) \* Result = c} also  $\{Cn(cnt:c)\}$   $\{Cn(cnt:c) * Result = c\}$ **inherit** increment() dynamic {Cc(val: v, cnt: c)} {Cc(val: v, cnt: c + 1)} also  $\{Cn(cnt:c)\}$   $\{Cn(cnt:c+1)\}$ end // In an arbitrary class or library:

use\_counter(c: COUNTER) **dynamic**  $\{c.Cn(cnt:v)\}_{c.Cn}(cnt:v+10)\}$ 

```
use_cell(c: CELL, v: INT)
dynamic {c.Cell(val: _)}_{c.Cell(val: v)}
```

Figure 3.2: The CCELL class and two library methods.

provide an abstraction of such objects in the logic, and 'grows'  $\text{Cell}_{CCELL}$  to accommodate method *set\_value*, as we shall see.

The single export clause of CCELL relates the Cc, Cell and Cn abstractions. Only the predicate in front of **where** is exported for reasoning. Predicate definitions following **where** are used only to verify the clause and allow a class to hide implementation/representation details in its interface without introducing new predicate families. For example, if we modify the representation of class CCELL, then the definition of Rest(x,v) can be changed without invalidating correctness proofs of client code.

To verify an export clause, we must prove that the exported predicate follows from the standard apf assumptions of the class and the predicate definitions after the **where** keyword. The proof for CCELL's export clause is trivial (Section 2.7 describes the standard apf assumptions of a class). Note that export clauses are not verified for a particular dynamic type, since the standard apf assumptions of a class do not assume a particular one. It is therefore sound to use exported information to verify axiom clauses and methods of other classes, as we will do in later examples. However, nothing prevents the user from writing exported predicates as implications where the antecedent is of the form x : Type (see the export clause of CCELL). In this case information in the consequent can be applied only to objects satisfying this type constraint.

For the constructor we have to prove that its body satisfies the static specification (note that a **Precursor** call is syntactic sugar for a direct call):

 $\{ value \hookrightarrow _ * count \hookrightarrow _ \} \\ CELL::CELL(v) \\ \{ Cell_{CELL}(val:v) * count \hookrightarrow _ \} \\ Precursor \{ COUNTER \} () \\ \{ Cell_{CELL}(val:v) * Cn_{COUNTER}(cnt:0) \} \\ \{ Cc_{CCELL}(val:v, cnt:0) \}$ 

The constructor body simply passes the needed fields to parent constructors and treats their internal representations abstractly thereafter.<sup>2</sup>

Method *value* is respecified in CCELL with Cell and Cc specificiations. Since it inherits the body from CELL, we must prove that the new static specification is satisfied assuming the body's static specification of CELL. This method proof obligation is called Inheritance in the formalisation. By applying the Frame rule (with  $Cn_{COUNTER}(cnt:c)$ ) and then the rule of Con-

 $<sup>^{2}</sup>$ To simplify the formal presentation of proofs and make them more transparent, we mention fields explicitly in constructor preconditions. MultiStar injects them automatically – see Section 3.2.1 for more discussion.

sequence, we can derive each **also**-ed static specification, which is sufficient to conclude the proof by Lemma 2.2. Another proof obligation for *value* is Behavioural subtyping, where we must show that the dynamic specification listed in CELL follows from the new one, i.e. that CCELL maintains the old specification. For the proof, we first 'choose' the Cell dynamic spec with Lemma 2.1, and then remove the cnt tag by applying the Auxiliary Variable Elimination and Consequence rules. The application of Auxiliary Variable Elimination quantifies the variable c existentially in the pre- and postcondition, and the application of Consequence uses tag reduction information which is part of CCELL's standard apf assumptions.

Behavioural subtyping of  $set_value$  is similar. For its Body verification obligation, we must prove that both  $\operatorname{Cell}_{CCELL}$  and  $\operatorname{Cc}_{CCELL}$  static specifications are satisfied. The proof proceeds as follows:

 $\begin{cases} Cell_{CCELL}(val: \_, cnt: c) \\ CCELL::increment() \\ \\ \{Cell_{CCELL}(val: \_, cnt: c + 1) \} \\ \\ Cell_{CELL}(val: \_) * Cn_{COUNTER}(cnt: c + 1) \} \\ \\ CELL::set\_value(v) \\ \\ \\ \{Cell_{CELL}(val: v) * Cn_{COUNTER}(cnt: c + 1) \} \\ \\ \\ \{Cell_{CCELL}(val: v, cnt: c + 1) \} \end{cases}$ 

An application of Consequence proves the other **also**-ed static spec and Lemma 2.3 completes the proof. As the body operates on state described by  $\text{Cell}_{CELL}$  and  $\text{Cn}_{COUNTER}$ , the proof obligations and separation logic's faulting semantics demand that we 'grow'  $\text{Cell}_{CCELL}$  to include both state parcels.

Now consider the two library routines at the bottom of Figure 3.2. The export clause contains the necessary information to prove the two triples:

```
{true} cc := new CCELL(5); use_counter(cc) {cc.Cc(val: 5, cnt: 10)}
{true} cc := new CCELL(5); use_cell(cc,20) {cc.Cc(val: 20, cnt: _)}
```

The proof of the second triple reduces and expands tags according to standard apf rules:

```
 \{ true \} \\ cc := new CCELL(5) \\ \{ cc : CCELL * cc.Cc(val:5, cnt:0) \} \\ \{ cc : CCELL * cc.Cell(val:5, cnt:0) \} \\ \{ cc : CCELL * cc.Cell(val:5, cnt:_) \} \\ \{ cc : CCELL * cc.Cell(val:5, cnt:_) \} \\ \{ cc : CCELL * cc.Cell(val:5) \} \\ use\_cell(cc,20) \\ \{ cc : CCELL * cc.Cell(val:20) \}
```

 $\{ cc : CCELL * cc.Cell(val: 20, cnt: _) \}$  $\{ cc.Cc(val: 20, cnt: _) \}$ 

Information about cnt is lost in the postcondition, which is unavoidable because  $use\_cell$  could call  $set\_value$  more than once. In a version of CCELL where  $Cn_{CCELL}$  is defined to include the  $Cell_{CELL}$  state and the equivalence of Cc, Cn and Cell is exported, information about val will likewise be lost in the first triple. Also note that dynamic type information is required to use the exported relationships, since the subclasses of CCELL are not obliged to implement them.

#### 3.1.2 Access control and call protocols

Our proof system can enforce interesting access control patterns in verified programs. Consider class CCEL2 in Figure 3.3 which has the same executable code as CCELL but different specifications. Its export clause relates the Cell and Cn abstractions in a one-directional way. The constructor produces a Cell apf predicate with which methods *value*, *set\_value* and *count* can be called. Verified clients cannot call *increment* with the Cell predicate. They must use exported information to get a Cn predicate, yet they lack information to change back after the call: no export or axiom clause is available to do this, and every method producing a Cell predicate requires one. The following proof attempt where cc2 : CCEL2 shows the problem:

While the client has a Cell predicate, the argument tagged by cnt and returned by *count* reflects precisely how many times the value has been set. If the client tries to manipulate the count by calling *increment*, then it can never regain the needed capability to call *value* and *set\_value*, and must forever treat the object as a simple counter in the code. The combination of abstract predicate relationships and method specifications enforces this protocol in verified code.

```
class CCEL2 inherit CELL COUNTER
define
x.Cell_{CCEL2}(val:v, cnt:c) as x.Cell_{CELL}(val:v) * x.Cn_{COUNTER}(cnt:c)
x.Cn<sub>CCEL2</sub>(cnt:c) as x.Cn<sub>COUNTER</sub>(cnt:c)
export
    \forall x \cdot x : CCEL2 \Rightarrow [\forall v, c \cdot x. Cell(val; v, cnt; c) \Rightarrow x. Cn(cnt; c)] \text{ where } \{\}
feature
    introduce CCEL2(v: INT)
    dynamic {value \hookrightarrow _ * count \hookrightarrow _}-{Cell(val: v, cnt: 0)}
    do Precursor{CELL}(v); Precursor{COUNTER}() end
    inherit value(): INT
    dynamic {Cell(val: v, cnt: c)}_{Cell(val: v, cnt: c)} * Result = v}
    override set_value(v: INT)
    dynamic {Cell(val: _, cnt: c)}_{Cell(val: v, cnt: c + 1)}
    do CCEL2::increment(); CELL::set_value(v) end
    inherit count(): INT
    dynamic {Cell(val: v, cnt: c)}_{Cell(val: v, cnt: c)} * Result = c}
          also \{Cn(cnt:c)\} \{Cn(cnt:c) * Result = c\}
```

end



```
class PERSON
define x.P<sub>PERSON</sub>(age: a) as x.age \hookrightarrow a
export \forall x, a \cdot x. P_{PERSON}(age: a) \Leftrightarrow x.age \hookrightarrow a where \{\}
feature
    introduce PERSON(a: INT)
    dynamic {age \hookrightarrow _}_{P(age: a)}
    do age := a end
    introduce age(): INT
    dynamic \{P(age:a)\}_{P(age:a)} * Result = a\}
    do Result := age end
    introduce set_age(a: INT)
    dynamic \{P(age: \_)\}_{} \{P(age: a)\}
    do age := a end
    introduce celebrate_birthday()
    static \{P(age:a)\} \{P(age:a+1)\}
    do tmp: INT. tmp := age(); tmp := tmp+1; set_age(tmp) end
    age: INT
```

end

Figure 3.4: The PERSON class.

#### 3.1.3 Diamond inheritance

Verification of multiple inheritance requires proper handling of data from several parent classes. Diamond inheritance complicates matters because common ancestor fields are shared. This is unproblematic for our proof system, although the abstraction of the shared data is typically lost. Diamond inheritance can moreover require relationships between several abstractions, which this example achieves with axiom clauses.

An axiom clause consists of a name and a predicate. The name identifies the clause and allows subclasses to refine the predicate. We propagate axiom clauses down the hierarchy to save specification overhead: if a class does not list an axiom clause with the same name as one in a parent, then it is assumed to list an identical clause. To avoid ambiguity in the presence of multiple inheritance, we require that if clauses with the same name are present in multiple parents, then they must all be identical. An axiom clause copied in class STUDENT inherit PERSON define x.P<sub>STUDENT</sub>(age: a) as x.P<sub>PERSON</sub>(age: a)  $x.S_{STUDENT}(age: a, exm: e) as x.P_{STUDENT}(age: a) * x.exams \hookrightarrow e$  $x.RestStoP_{STUDENT}(exm:e)$  as  $x.exams \hookrightarrow e$ export  $\forall x, a, e \in [x.P_{PERSON}(age:a) * x.RestStoP_{STUDENT}(exm:e)] \Leftrightarrow$ x.S<sub>STUDENT</sub> (age: a, exm: e) where {} **axiom** S\_P:  $\forall a, e \in S(age: a, exm: e) \Leftrightarrow [P(age: a) * RestStoP(exm: e)]$ feature introduce STUDENT(a: INT, e: INT) dynamic {age  $\hookrightarrow$  \_ \* exams  $\hookrightarrow$  \_}\_{S(age: a, exm: e)} do Precursor{PERSON}(a); exams := e end introduce exams(): INT dynamic {S(age: a, exm: e)}\_{S(age: a, exm: e)} \* Result = e} do Result := exams end introduce take\_exam() dynamic {S(age: a, exm: e)}\_{S(age: a, exm: e + 1)} do tmp: INT. tmp := exams; exams := tmp + 1 end

exams: INT

#### $\mathbf{end}$

Figure 3.5: The STUDENT class.

# class SMUSICIAN inherit STUDENT MUSICIAN define

 $x.P_{SMUSICIAN}(age:a)$  as  $x.P_{PERSON}(age:a)$ 

 $x.S_{SMUSICIAN}(age:a, exm:e)$  as  $x.S_{STUDENT}(age:a, exm:e)$ 

 $x.M_{SMUSICIAN}(age:a, pfm:p)$  as  $x.M_{MUSICIAN}(age:a, pfm:p)$ 

```
x.SM_{SMUSICIAN}(age:a, exm:e, pfm:p) as x.P_{PERSON}(age:a) *
```

 $SM\_S: \forall a, e, p \cdot SM(age: a, exm: e, pfm: p) \Leftrightarrow \\ [S(age: a, exm: e) * RestSMtoS(pfm: p)] \\ SM\_M: \forall a, e, p \cdot SM(age: a, exm: e, pfm: p) \Leftrightarrow \\ [M(age: a, pfm: e) * RestSMtoM(exm: e)]$ 

feature

```
introduce SMUSICIAN(a: INT, e: INT, p: INT)

dynamic {age \hookrightarrow _ * exams \hookrightarrow _ * performances \hookrightarrow _}_

{SM(age: a, exm: e, pfm: p)}

do Precursor{STUDENT}(a,e); Precursor{MUSICIAN}(a,p) end
```

```
\begin{array}{l} \textbf{introduce } do\_exam\_performance() \\ \textbf{static } \{ SM(age:a,exm:e,pfm:p) \}_{} \{ SM(age:a,exm:e+1,pfm:p+1) \} \\ \textbf{do } take\_exam(); \ perform() \ \textbf{end} \\ \textbf{od} \end{array}
```

end

Figure 3.6: The SMUSICIAN class.

this way is not refined in the subclass and automatically consistent with its parent versions. In the general case where a subclass refines an axiom clause, Parent consistency must be proven as indicated in the later formalisation.

The focus of this example is class SMUSICIAN, shown in Figure 3.6. It inherits from STUDENT and MUSICIAN, both which inherit from PER-SON. The STUDENT and PERSON classes are shown in Figures 3.4 and 3.5; MUSICIAN is similar to STUDENT and not shown. A diamond is formed with PERSON at the top, and an instance of SMUSICIAN has one 'age' field, one *set\_age* method, etc. under shared multiple inheritance semantics. The classes use axiom clauses to specify relationships between abstractions P, S, M and SM.

Since SMUSICIAN is non-abstract, we must prove that axiom SM\_S holds for its direct instances. This proof obligation for axiom clauses is called Implication in the formalisation that will follow. It holds indeed, since under the standard apf assumptions of SMUSICIAN, exported information of all classes, and the assumption **Current** : SMUSICIAN, we have:

SM(age: a, exm: e, pfm: p)// Standard apf assumptions, **Current** : SMUSICIAN  $\Leftrightarrow$ SM<sub>SMUSICIAN</sub> (age: a, exm: e, pfm: p) // Standard apf assumptions.  $\Leftrightarrow$  $P_{PERSON}(age:a) * RestStoP_{STUDENT}(exm:e) *$ RestMtoP<sub>MUSICIAN</sub>(pfm:p) // Exported information from STUDENT.  $\Leftrightarrow$  $S_{STUDENT}(age:a, exm:e) * RestMtoP_{MUSICIAN}(pfm:p)$ // Standard apf assumptions.  $S_{SMUSICIAN}(age:a, exm:e) * RestSMtoS_{SMUSICIAN}(pfm:p)$ // Standard apf assumptions, Current : SMUSICIAN  $\Leftrightarrow$ S(age:a, exm:e) \* RestSMtoS(pfm:p)

The export clause in STUDENT is not closely connected to multiple inheritance. In fact, any class C inheriting from STUDENT which defines  $P_C$  to be  $P_{PERSON}$  and  $S_C$  to be  $S_{STUDENT}$  will need the export clause to prove Implication of S\_P, which we omit here for SMUSICIAN. Axiom verification frequently requires exported information of this kind. What is vital about the export clause in the shared multiple inheritance setting is that it isolates the shared ancestor state of SMUSICIAN, namely  $P_{PERSON}$ . This allows SMUSICIAN to relate ancestor abstractions in a fairly abstract way. Only the constructor's Body verification proof needs the export clause in PERSON<sup>3</sup>:

<sup>&</sup>lt;sup>3</sup>Unless a class manipulates fields of its ancestors directly, this export clause would not be needed in languages where constructors of common ancestor classes cannot be called more than once.

 $\{ age \hookrightarrow \_ * exams \hookrightarrow \_ * performances \hookrightarrow \_ \}$   $\mathbf{Precursor} \{ STUDENT \} (a,e)$   $\{ \mathbf{S}_{STUDENT} (age: a, exm: e) * performances \hookrightarrow \_ \}$   $\{ \mathbf{P}_{PERSON} (age: a) * \mathbf{RestStoP}_{STUDENT} (exm: e) * performances \hookrightarrow \_ \}$   $\{ age \hookrightarrow a * \mathbf{RestStoP}_{STUDENT} (exm: e) * performances \hookrightarrow \_ \}$   $\mathbf{Precursor} \{ MUSICIAN \} (a,p)$   $\{ \mathbf{M}_{MUSICIAN} (age: a, pfm: p) * \mathbf{RestStoP}_{STUDENT} (exm: e) \}$   $\{ \mathbf{P}_{PERSON} (age: a) * \mathbf{RestMtoP}_{MUSICIAN} (pfm: p) *$   $\mathbf{RestStoP}_{STUDENT} (exm: e) \}$   $\{ \mathbf{SM}_{SMUSICIAN} (age: a, exm: e, pfm: p) \}$ 

Note that class SMUSICIAN would not have needed exported information if it ignored the parent constructors and simply overrode everything. The same is true for proof systems with less abstraction where method bodies are reverified in subclasses.

Since **Current** in SMUSICIAN will always reference an object whose dynamic type is a subtype of SMUSICIAN, the Body verification proof of *do\_exam\_performance* can use axiom information to infer SM-specs for *take\_exam* and *perform*:

 $\{ SM(age:a, exm:e, pfm:p) \}$   $\{ S(age:a, exm:e) * RestSMtoS(pfm:p) \}$   $take\_exam()$   $\{ S(age:a, exm:e+1) * RestSMtoS(pfm:p) \}$   $\{ SM(age:a, exm:e+1, pfm:p) \}$   $\{ M(age:a, pfm:p) * RestSMtoM(exm:e+1) \}$  perform()  $\{ M(age:a, pfm:p+1) * RestSMtoM(exm:e+1) \}$   $\{ SM(age:a, exm:e+1, pfm:p+1) \}$ 

The specification overhead incurred by axiom clauses is offset by specification inference gains: SM, S and M specifications can be inferred for *age*, *set\_age* and *celebrate\_birthday*, while SM specifications can be inferred for *exams*, *take\_exam*, *performances* and *get\_performance* – a total of 13 specifications for methods of SMUSICIAN. These inferred specifications are guaranteed to be implemented by all subclasses, and no dynamic type information is needed to use them<sup>4</sup>. Yet the system is still flexible – a subclass can always choose

<sup>&</sup>lt;sup>4</sup>The technique used by Chin et al. [14] of inheriting static method specifications and deriving dynamic specifications from them implements "internal specification inference", i.e. a class infers and publishes dynamic specifications for its methods which external clients can use. In contrast to this, the technique of equipping classes with export/axiom clauses implements "external specification inference", i.e. clients infer specifications for methods based on published export/axiom information. A benefit of the

to satisfy such constraints vacuously by defining selected apf entries as false. Class DCell in [58] provides an example of this.

# 3.2 MultiStar

This section sketches notable aspects of the MultiStar implementation. MultiStar has a two-tier architecture: a front-end that translates Eiffel programs and specifications into a simpler form for verification, and a languageindependent back-end based on jStar [23] which implements our proof system.

#### 3.2.1 Front-end

The front-end provides a graphical user interface within the EVE integrated development environment, and is part of the standard EVE download [28]. It translates Eiffel code and specifications into the back-end's input format, and provides access to verification results. Verification is triggered by picking and dropping an annotated class on the MultiStar tool. Class annotations consist of apf entry definitions, export/axiom clauses and method specifications.

To simplify the proofs and formalisation in this chapter, constructor preconditions explicitly mention fields and break information hiding. The frontend translation of MultiStar injects them automatically. For example, a user would write the specification of CCELL's constructor as

dynamic {true}\_{Cc(val: v, cnt: 0)}

instead of

dynamic {value  $\hookrightarrow$  \_ \* count  $\hookrightarrow$  \_}-{Cc(val: v, cnt: 0)}

In detail, the front-end facilitates this by:

- 1. Using jStar's **new** statement, whose specification is given by the triple  $\{true\}x := new C\{x : C\}$ . This allows us to omit the fields in the dynamic precondition<sup>5</sup>.
- 2. Adding all fields (including ancestor ones) to the static precondition when checking Body verification, and consuming all fields of a parent

external approach is that clients can infer valid specifications for library code without re-verifying it. For example, knowing only the inferred specifications for methods of class SMUSICIAN is not enough for proving the triple  $\{x.SM(age:a, exm:e, pfm:p)\}$ use\_student(x) $\{x.SM(age:a + 1, exm:e + 4, pfm:p)\}$  without looking at the implementation of the library routine use\_student(st: STUDENT) whose dynamic specification is  $\{st.S(age:a, exm:e)\}_{st.S}(age:a + 1, exm:e + 4)\}$ .

<sup>&</sup>lt;sup>5</sup>The dynamic specification of the constructor is used for object initialisation. x := **new** C( $\overline{e}$ ) abbreviates x := **new** C;  $x.C(\overline{e})$  if x is not free in  $\overline{e}$ .

class and its ancestors right before the parent constructor is called. This is communicated to the back-end by emitting special instructions, and allows us to omit the fields in the static precondition.

The Dynamic dispatch proof obligation, which checks that the static and dynamic specifications are consistent with each other, is unaffected because fields are omitted in both static and dynamic preconditions. In languages where no fields are shared by ancestors, or constructors of common ancestors are called only once, the manipulation does not have to add ancestor fields to the static precondition and consume fields when a parent constructor is called. This is the approach jStar uses for Java verification. A front-end for C++ can use a similar technique because every ancestor constructor is called exactly once when virtual base classes are used [27].

#### 3.2.2 Back-end

The MultiStar back-end extends jStar with support for export and axiom clauses, abstract classes and multiple inheritance. The latter two demand generalised proof obligation checking for methods.

#### Export and axiom clauses

The background theory used by the jStar theorem prover is encoded as a list of sequent rules. A sequent is of the form  $P \mid Q \vdash R$ , meaning  $(P * Q) \Rightarrow (P * R)$ . Each sequent rule has the form

 $A \mid B \vdash C$ <br/>if<br/> $D \mid E \vdash F$ 

If the prover is trying to prove a sequent that matches the rule's conclusion  $A \mid B \vdash C$ , it suffices to prove the premise sequent  $D \mid E \vdash F$  where the appropriate substitutions due to matching have been made. A new proof goal is thus obtained, and the proof is complete when the goal is of the form  $G \mid H \vdash$ . For details the reader is referred to [23].

Exported information is written as sets of implications. Before verifying an export clause, the background theory is temporarily extended with the definitions of all predicates in its **where** part. For each definition of the form w(x) = P, the following two rules are generated:

$$\begin{array}{ccc} \mid w(x) \vdash & \quad \mid \vdash w(x) \\ \mathbf{if} & \quad \mathbf{if} \\ \mid P \vdash & \quad \mid \vdash P \\ \end{array}$$

After all exported implications in the clause have been checked, the definitions are removed from the background theory. After all export clauses have been verified, each exported implication  $P \Rightarrow (Q_1 * \ldots * Q_n)$  is added to the background theory as a set of n rules, where rule  $i \in 1...n$  has the form

 $\begin{array}{l} \mid P \vdash Q_i \\ \textbf{if} \\ Q_i \mid Q_1 * \ldots * Q_{i-1} * Q_{i+1} * \ldots * Q_n \vdash \end{array}$ 

This rule form retains information about  $Q_i$  in its premise, and removal of  $Q_i$  from the goal sequent's right-hand side brings the proof closer to completion.

The background theory augmented with export information is then used to verify axiom clauses. The predicates in axiom clauses are written as implications. After all axiom clauses have been verified, an axiom implication  $P \Rightarrow (Q_1 * \ldots * Q_n)$  written in class C is encoded as n rules, with rule  $i \in$ 1..n of the form

 $\begin{array}{l} \mid P \vdash Q_i \\ \textbf{if} \\ Q_i \mid Q_1 * \ldots * Q_{i-1} * Q_{i+1} * \ldots * Q_n \vdash x <: C \end{array}$ 

where x is the pattern variable substituted for **Current**.

The background theory augmented with export and axiom information is then used for method verification.

#### Method proof obligations

The back-end accommodates abstract classes and abstract methods in addition to shared multiple inheritance. An abstract method has no body and hence no static specification. The back-end takes this into account when expanding specification shorthands. After shorthand expansion, verification of method m in class C proceeds as follows (the formalisation contains details about the proof obligations):

- If m has a static specification and C can be instantiated (i.e. is nonabstract), then check Dynamic dispatch.
- If m has a body in C, then check Body verification.
- Always check Behavioural subtyping. This succeeds trivially if m is introduced in C: the set of dynamic specifications for m in C's parents is empty, and therefore all its elements are preserved by the new specification.
- If m has a static specification but no body in C, then check Inheritance.



Figure 3.7: The core Gobo cursor hierarchy.

The treatment subsumes interface inheritance – interfaces are treated as abstract classes with only abstract methods and no fields.

# 3.3 Case study

The Gobo data structure library [29] is an open-source Eiffel library covering data structures and algorithms. It contains classic data structures such as lists, stacks and sets, and provides several implementations of each structure. The library is stable and a popular choice among Eiffel developers.

Data structures such as lists and sets can be traversed with iterators. The iterator (or *cursor*) hierarchy is characterised by relatively simple algorithms and extensive use of multiple inheritance, which makes it an ideal candidate for evaluating the novel aspects of our proof system and its implementation. The core classes are shown in Figure 3.7: a LINEAR\_CURSOR can traverse a data structure forwards, a BILINEAR\_CURSOR can traverse both forwards and backwards, an INDEXED\_CURSOR offers random data structure access with an integer position or index, and a DYNAMIC\_CURSOR can modify the data structure being traversed.

We successfully verified the core cursor hierarchy of Figure 3.7 with Multi-Star. The overall effort for specification and verification was five person-days. Most of the time was spent on finding and revising specifications, since we did not modify the code. Table 3.1 shows the experimental results. The total time taken by MultiStar is reported, which includes translating Eiffel code, expanding specification shorthands and checking all proof obligations.

Since iterators rely on properties of the data structures (containers) they traverse, we annotated the container classes with the required specifications. Particularly interesting are the axiom clauses that iterators demand.

Class	$LOC_1$	$LOC_2$	Time(s)
BILINEAR_CURSOR	99	124	1.306
BILINEAR_SET_CURSOR	44	50	0.841
CURSOR	130	158	1.039
DYNAMIC_CURSOR	50	66	1.070
INDEXED_CURSOR	46	57	0.698
LINEAR_CURSOR	98	123	1.327
LIST_CURSOR	238	271	1.643
SET_CURSOR	38	44	0.738
8 classes	743	893	8.662

Table 3.1: Experimental results of the Gobo iterator case study.  $LOC_1$  and  $LOC_2$  denote the lines of code before and after specification respectively. MultiStar was executed on a 2.53 GHz Intel Core 2 Duo with 4 GB RAM.

Consider for example the simplified extract of DYNAMIC\_CURSOR in Figure 3.8. The [G] denotes that DYNAMIC\_CURSOR has a generic parameter G. Method *swap* takes another cursor referencing the same container, and additionally requires that there are data elements (items) at both cursor positions (the cursors are not 'off'). The Body verification proof of *swap* uses several properties of containers that can be expressed as axioms, including the following one:

 $\forall r1, r2, iter1, iter2, i, c1, c2, c3 \cdot [ItemAt(res: r1, ref: iter1, iters: i, content: c1) * ItemAt(res: r2, ref: iter2, iters: i, content: c1) * Replaced(ref: iter1, value: r2, newcontent: c2, oldcontent: c1, iters: i) * Replaced(ref: iter2, value: r1, newcontent: c3, oldcontent: c2, iters: i)]$  $<math display="block">\Rightarrow$ Swapped(ref1: iter1, ref2: iter2, iters: i, oldcontent: c1, newcontent: c3)

This invariant property relates the ItemAt, Replaced and Swapped abstractions. Informally, it states that if a data structure has items r1 and r2 at iterators iter1 and iter2, and we replace the item at iter1 with r2 and the item at iter2 with r1, then the resulting data structure has the same con-

tents as the original one except that the items at iter1 and iter2 have been swapped. The example involves neither multiple inheritance nor splitting of superclass and subclass state, and illustrates the generality and expressive power of axiom clauses.

The complete specifications and code of the case study are included in the download [28].

```
abstract class DYNAMIC_CURSOR [G] inherit CURSOR [G]
feature
    introduce abstract replace(v: G) dynamic
    \{Cursor(ds:d) * d.DS(content:c1, iters:i) *
      d.IsOff(res: False, ref: Current, iters: i, content: c1)}_
    \{Cursor(ds:d) * d.DS(content:c2, iters:i) *
     d.Replaced(ref: Current, value: v, newcontent: c2, oldcontent: c1, iters: i)}
    inherit item(): G static
    \{Cursor(ds:d) * d.DS(content:c, iters:i) *
      d.IsOff(res: False, ref: Current, iters: i, content: c)}_
    \{Cursor(ds:d) * d.DS(content:c, iters:i) *
      d.ItemAt(res: Result, ref: Current, iters: i, content: c)}
    introduce swap(other: DYNAMIC_CURSOR [G]) static
    \{Cursor(ds:d) * d.DS(content:c1, iters:i) * other.Cursor(ds:d) *
      d.IsOff(res: False, ref: Current, iters: i, content: c1) *
      d.IsOff(res: False, ref: other, iters: i, content: c1)}_
    \{Cursor(ds:d) * d.DS(content:c2, iters:i) * other.Cursor(ds:d) *
     d.Swapped(ref1: Current, ref2: other, iters: i, oldcontent: c1, newcontent: c2)}
    do
        v: G. w: G.
        v := item(); w := other.item();
        replace(w); replace(v)
    end
end
```

Figure 3.8: A simplified extract of DYNAMIC\_CURSOR

# **3.4** Formalisation

This section contains a formal presentation of the programming language and its proof system. It extends the material of chapter 2 with abstract classes, multiple inheritance, and export and axiom specifications. To keep things simple, the presentation concentrates on the new extensions.

### 3.4.1 Language syntax

The grammar of the extended kernel language appears in Figure 3.9. New and changed productions are highlighted: abstract classes, multiple parents, export and axiom clauses, and abstract methods are now supported. The letter p ranges over apf names, w over auxiliary predicate names, and a over axiom names.

We assume the absence of clashes when names are introduced. This guarantees that axioms, methods and fields with the same name in parent classes stem from common ancestors.

The shared semantics of multiple inheritance is used, which is popular in Eiffel [26] and known as inheritance with *virtual base classes* in C++ [27]. Common ancestor fields are shared, and method overriding overrides all ancestor versions. To avoid ambiguity, a class can inherit a method only if its body (if there is one) is the same along all inheritance paths. Direct method calls can encode language mechanisms which allow a particular ancestor implementation to be chosen, so no generality is lost.

#### 3.4.2 Operational semantics

The shared semantics of multiple inheritance ensures that 1) only dynamic type information is needed at runtime (in contrast to what 'select' clauses of Eiffel's replicated inheritance demand), and 2) the usual semantics of casts can be adopted (in contrast to replicated inheritance in C++, where casting can change pointer values [27]).

The operational semantics is therefore similar to Parkinson's semantics of Java [57]. Configurations contain a stack, a heap and a sequence of statements under execution. The stack maps variables to values which include object ids. The heap maps object ids to records containing a dynamic type G and field-value mappings.

$L ::= Ab$ class G inherit $\overline{H}$ define $\overline{D}$ expor	${f t} \ \overline{ m E} \ {f axiom} \ \overline{ m A} \ {f feature} \ \overline{ m M} \ \overline{ m F} \ {f end}$
Ab ::= <b>abstract</b>   $\epsilon$	
$D ::= x.p_G(\overline{t:y})$ as P	Define clause
$E ::= P \text{ where } \{\overline{W}\}$	Export clause
$W ::= w(\overline{x}) = P$	Where clause
A ::= a: P	Axiom clause
M ::= introduce m(Args) Rt Sd Ss B	Method declaration
override m(Args) Rt Sd Ss B	
<b>inherit</b> m(Args) Rt Sd Ss	
$\mid \mathbf{introduce \ abstract} \ \mathrm{m}(\mathrm{Args}) \ \mathrm{Rt} \ \mathrm{Sd}$	
<mark>inherit abstract</mark> m(Args) Rt Sd	
F ::= f: Type	Field declaration
Sd ::= dynamic Spec	$Dynamic \ specification$
Ss ::= static Spec	$Static \ specification$
Spec ::= $\{P\}_{Q} \mid \{P\}_{Q}$ also Spec	Specification
$\mathbf{B} ::= \mathbf{do} \mathbf{s} \mathbf{end}$	Method body
s ::= skip	Statement
$  \mathbf{x} := \mathbf{e}$	Assignment
s ; s	$Sequential\ composition$
$\mathbf{i} \mathbf{f} e \mathbf{then} s \mathbf{else} s \mathbf{end}$	Conditional
while $e \mathbf{do} s \mathbf{end}$	Loop
x: Type. s	Local variable block
$\mathbf{x} := \mathbf{new} \mathbf{G}$	Object allocation
$\mathbf{x} := \mathbf{y}.\mathbf{f}$	Field lookup
$\mathbf{x}.\mathbf{f} := \mathbf{e}$	Field update
$  \mathbf{x} := \mathbf{y}.\mathbf{m}(\overline{\mathbf{e}})   \mathbf{y}.\mathbf{m}(\overline{\mathbf{e}})$	Dynamically dispatched call
$ \mathbf{x} := \mathbf{y}.\mathbf{G}::\mathbf{m}(\overline{\mathbf{e}})   \mathbf{y}.\mathbf{G}::\mathbf{m}(\overline{\mathbf{e}})$	Direct method call
e ::= x   e + e   e = e   Void   0   1   2	Expression
$Type ::= INT \mid BOOL \mid G$	
$Args ::= \overline{x: Type}$	Formal arguments
$Rt ::= \epsilon \mid : Type$	Return type

Figure 3.9: The grammar of the extended kernel language.

#### 3.4. FORMALISATION

#### 3.4.3 Logic syntax and semantics

The assertion language is extended with auxiliary predicates. These predicates are defined in the **where** part of export clauses.

### 3.4.4 Specification refinement

The formalisation of specification refinement of Section 2.3 remains valid here. However, the assumptions  $\Delta$  now contain the standard apf assumptions of a class as well as export and axiom information of all other classes.

#### 3.4.5 The specification environment

The specification environment  $\Gamma$  is extended to map axiom names to their associated predicates for all classes in a program:

 $\begin{array}{ll} \Gamma ::= \begin{tabular}{ll} G.m \mapsto (\overline{x}, \{P\}_{-}\{Q\}) & Method \ dynamic \ specification \\ & | \ G::m \mapsto (\overline{x}, \{S\}_{-}\{T\}) & Method \ static \ specification \\ & | \ \overline{\Gamma} & \end{array}$ 

For well-formed programs,  $\Gamma$  is still guaranteed to be a partial function, and we write  $\Gamma(G.a) = P$  for  $G.a \mapsto P \in \Gamma$ .

#### 3.4.6 Export information verification

A class can make information about itself available to other classes in an export clause. Export clauses are frequently used to specify relationships between apfs or their entries, and to expose apf entry definitions. Information can be hidden in predicates defined after the keyword **where**: the definitions are not exported, so other classes must treat these predicates abstractly.

Export information must be verified since other classes use it for reasoning. Under the predicate definitions following **where**, the assumptions about a class must imply exported information. This is captured by the following proof rule:

$$\begin{bmatrix} \Delta \land (\forall \overline{\mathbf{x}_1} \cdot \mathbf{w}_1(\overline{\mathbf{x}_1}) \Leftrightarrow \mathbf{Q}_1) \land \dots \land (\forall \overline{\mathbf{x}_n} \cdot \mathbf{w}_n(\overline{\mathbf{x}_n}) \Leftrightarrow \mathbf{Q}_n) \end{bmatrix} \Rightarrow \mathbf{P} \\ \Delta \vdash_e \mathbf{P} \text{ where } \{ \mathbf{w}_1(\overline{\mathbf{x}_1}) = \mathbf{Q}_1; \dots; \mathbf{w}_n(\overline{\mathbf{x}_n}) = \mathbf{Q}_n \}$$

Since the assumptions about a class do not include assumptions about the exact dynamic type of **Current**, export information can be used to verify axioms and methods of other classes in a program.

#### 3.4.7 Axiom verification

Information about a class and all its subclasses can be made available in an axiom clause. This knowledge can be used later to verify methods. To simplify the treatment, we require that a class explicitly lists all axiom clauses applicable to it (in MultiStar and the examples, specification shorthands achieve this).

In the rule for axiom verification, the assumptions  $\Delta$  include information about class G and export information from all other classes. A subclass must preserve all axioms of its parents and may refine the predicate associated with an axiom name (the Parent consistency [P.c.] obligation). A non-abstract class must also show that the predicate holds for its direct instances (the Implication [Imp.] obligation).

$$\begin{array}{ll} \forall i \in I \cdot \Gamma(H_i.a) = Q_i & \land & \forall j \in (1..n \setminus I) \cdot H_j.a \notin dom(\Gamma) \\ (\Delta \wedge P) \Rightarrow \bigwedge_{i \in I} Q_i & [P.c.] \\ Ab \neq \epsilon & \lor & (\Delta \wedge \textbf{Current} : G) \Rightarrow P & [Imp.] \end{array}$$

 $\Delta; \Gamma \vdash_a a: P \text{ in Ab } G \text{ parents } H_1 \dots H_n$ 

#### 3.4.8 Statement verification

The assumptions  $\Delta$  used to verify statements contain information about the enclosing class as well as export and axiom information from all other classes. The rules remain the same as those in Section 2.5.

### 3.4.9 Method verification

The rules for method verification of Section 2.6 are extended here to deal with multiple inheritance and abstract classes and methods. As for statement verification, the assumptions  $\Delta$  used to verify method definitions contain information about the method's enclosing class as well as export and axiom information of all other classes.

A newly introduced method's static and dynamic specifications must be consistent if the class is not abstract:  $\begin{array}{ll} B = \mathbf{do \ s \ end} \\ \mathrm{Sd} = \mathbf{dynamic} \ \{P_G\}_{-}\{Q_G\} \\ \mathrm{Ss} = \mathbf{static} \ \{S_G\}_{-}\{T_G\} \\ \mathrm{Ab} \neq \epsilon \ \lor \ \Delta \vdash \{S_G\}_{-}\{T_G\} \xrightarrow{\mathbf{Current}: G} \{P_G\}_{-}\{Q_G\} \\ \Delta; \Gamma \vdash_s \ \{S_G\}_{\mathrm{s}}\{T_G\} \xrightarrow{\mathbf{Current}: G} \{P_G\}_{-}\{Q_G\} \\ & [B.v.] \end{array}$ 

 $\Delta; \Gamma \vdash_m \mathbf{introduce} \ \mathrm{m}(\mathrm{Args}) \ \mathrm{Rt} \ \mathrm{Sd} \ \mathrm{Ss} \ \mathrm{B} \ \mathrm{in} \ \mathrm{Ab} \ \mathrm{G} \ \mathrm{parents} \ \overline{\mathrm{H}}$ 

An abstract method can be introduced without any proof obligations, since there is only a dynamic specification and no method body.

# $\Delta;\Gamma\vdash_m$ introduce abstract m(Args) Rt Sd in Ab G parents $\overline{\mathrm{H}}$

The next rule is used whenever an abstract method is implemented or a method body is redefined. The  $H_1 \ldots H_n$  are the immediate superclasses of G.

$$\begin{array}{ll} \forall i \in I \cdot \Gamma(H_i.m) = (\overline{x}, \{P_{Hi}\}_{-}\{Q_{Hi}\}) \\ \forall j \in (1..n \setminus I) \cdot H_j.m \notin dom(\Gamma) \\ B = \textbf{do s end} \\ Sd = \textbf{dynamic} \{P_G\}_{-}\{Q_G\} \\ Ss = \textbf{static} \{S_G\}_{-}\{T_G\} \\ \Delta \vdash \{P_G\}_{-}\{Q_G\} \Longrightarrow (\textbf{also}_{i \in I} \{P_{Hi}\}_{-}\{Q_{Hi}\}) \\ Ab \neq \epsilon \lor \Delta \vdash \{S_G\}_{-}\{T_G\} \xrightarrow{\textbf{Current}:G} \{P_G\}_{-}\{Q_G\} \\ \Delta; \Gamma \vdash_s \{S_G\}s\{T_G\} \end{array}$$
 [B.v.]

 $\Delta; \Gamma \vdash_m$  over ride m(Args) Rt Sd Ss B in Ab G parents  $H_1 \dots H_n$ 

When a non-abstract method is inherited, its static specification must follow from those in parents. The Inheritance [Inh.] obligation enforces it.

 $\begin{array}{ll} \forall i \in I \cdot \Gamma(H_i.m) = (\overline{x}, \{P_{H_i}\}_{-}\{Q_{H_i}\}) \\ \forall k \in (1..n \setminus I) \cdot H_k.m \notin dom(\Gamma) \\ \forall j \in J \cdot \Gamma(H_j::m) = (\overline{x}, \{S_{H_j}\}_{-}\{T_{H_j}\}) \\ \forall l \in (1..n \setminus J) \cdot H_l::m \notin dom(\Gamma) \\ Sd = \textbf{dynamic} \{P_G\}_{-}\{Q_G\} \\ Ss = \textbf{static} \{S_G\}_{-}\{T_G\} \\ \Delta \vdash \{P_G\}_{-}\{Q_G\} \Longrightarrow (\textbf{also}_{i \in I} \{P_{H_i}\}_{-}\{Q_{H_i}\}) \\ \Delta \vdash (\textbf{also}_{j \in J} \{S_{H_j}\}_{-}\{T_{H_j}\}) \Longrightarrow \{S_G\}_{-}\{T_G\} \\ Ab \neq \epsilon \ \lor \ \Delta \vdash \{S_G\}_{-}\{T_G\} \\ \end{array}$ 

 $\Delta; \Gamma \vdash_m$  inherit m(Args) Rt Sd Ss in Ab G parents H<sub>1</sub>... H<sub>n</sub>

The next rule applies whenever an abstract method is inherited or a nonabstract method is inherited and made abstract. Such a method has no static specification, so only the consistency of its dynamic specification w.r.t. those in parent classes is required with the Behavioural subtyping proof obligation.

 $\forall i \in I \cdot \Gamma(H_i.m) = (\overline{x}, \{P_{Hi}\}, \{Q_{Hi}\})$  $\forall \mathbf{j} \in (1..n \setminus I) \cdot \mathbf{H}_{\mathbf{j}}.\mathbf{m} \notin dom(\Gamma)$  $Sd = dynamic \{P_G\}_{-}\{Q_G\}$  $\Delta \vdash \{P_G\}_{-}\{Q_G\} \Longrightarrow (\mathbf{also}_{i \in I} \{P_{Hi}\}_{-}\{Q_{Hi}\})$ |B.s.| $\Delta; \Gamma \vdash_m$  inherit abstract m(Args) Rt Sd in Ab G parents H<sub>1</sub>...H<sub>n</sub>

Several intuitive relationships hold between the rules. For example, introducing a non-abstract method is the same as overriding the version of parent classes  $I = \emptyset$ . Likewise, introducing an abstract method is the same as inheriting it from the parent set  $I = \emptyset$ . Section 3.2.2 gives yet another perspective the rationale behind the proof obligations of the various rules.

#### 3.4.10 Class and program verification

For class verification, different assumptions are used to verify the various class sections. The formula  $\Delta_{APF}$  contains class-specific information and is used to verify export clauses. The assumptions  $\Delta_E$  contain export information from all classes, and are used together with  $\Delta_{APF}$  to verify axioms. The formula  $\Delta_A$  contains axiom information of all classes, and is used with  $\Delta_{APF}$ and  $\Delta_E$  in method definition verification.

 $\forall \mathbf{E}_{i} \in \overline{\mathbf{E}} \cdot \Delta_{APF} \vdash_{e} \mathbf{E}_{i}$  $\forall A_i \in \overline{A} \cdot (\Delta_{APF} \wedge \Delta_E); \Gamma \vdash_a A_i \text{ in Ab G parents } \overline{H}$  $\forall \mathbf{M}_{\mathbf{i}} \in \overline{\mathbf{M}} \cdot (\Delta_{APF} \wedge \Delta_E \wedge \Delta_A); \Gamma \vdash_{m} \mathbf{M}_{\mathbf{i}} \text{ in Ab G parents } \overline{\mathbf{H}}$  $\Delta_{APF}, \Delta_E, \Delta_A; \Gamma \vdash_c$ 

Ab class G inherit  $\overline{H}$  define  $\overline{D}$  export  $\overline{E}$  axiom  $\overline{A}$  feature  $\overline{M}$   $\overline{F}$  end

Finally, here is the rule for program verification:

 $\forall i \in 1..n \cdot L_i = \ldots class G_i \ldots export \overline{E_i} axiom \overline{A_i} feature \ldots end$  $\Delta_E = \bigwedge_{i \in 1..n} \bigwedge_{E_{i_k} \in \overline{E_i}} export info(E_{i_k})$  $\Delta_A = \bigwedge_{i \in 1..n} \bigwedge_{A_{i_k} \in \overline{A_i}} axiominfo(G_i, A_{i_k})$  $\Gamma = specs(L_1 \dots L_n)$  $\forall i \in 1..n \cdot apf(L_i), \Delta_E, \Delta_A; \Gamma \vdash_c L_i$  $\Delta_E \wedge \Delta_A; \Gamma \vdash_s \{\text{true}\}$ s{true}  $\vdash_p L_1 \dots L_n s$ 

exportinfo(P where ...)  $\stackrel{\text{def}}{=}$  P axiominfo(G, a: P)  $\stackrel{\text{def}}{=} \forall x <: G \cdot P[x/\text{Current}], \text{ where } x \text{ is fresh.}$ 

Predicate definitions following the **where** keyword are hidden by *export-info*, and the definition of *axiominfo* reflects the fact that subclasses preserve axioms.

The function apf translates the abstract predicate family definitions of a class into its standard apf assumptions in the same way as before.

MultiStar and the examples assume that every class implicitly exports tag reduction information. In other words, for every entry  $(x.p_G(Y) \text{ as } P)$  in the **define** section of a class G,  $(\forall x <: G \cdot TR(p,x,Y) \text{ where } \{\})$  is implicitly exported.

**Theorem.** The program verification rule is sound. (The proof, sketched in Appendix A, depends on the layered assumption structure of export and axiom clauses that avoids circularity in reasoning<sup>6</sup>.)

## **3.5** Conclusions and related work

The presented proof system supports two complementary mechanisms that can express relationships between abstractions in the logic. Such relationships are pervasive in OO programs, and facilitate flexible client reasoning, access control, specification inference, and constraints on the implementation of abstractions. Moreover, the system offers a sound way to verify various forms and uses of shared multiple inheritance. By virtue of extending Parkinson and Bierman's system, the examples in [58] illustrate that it can also deal with behaviour extension, restriction and modification, as well as representation replacement in subclasses. It is modular and every method body is verified only once. MultiStar implements these features in an automatic tool that, as the Gobo case study shows, holds good promise for verifying real-world software.

We are not aware of any other proof system or tool that can verify our examples and case study. Nevertheless, there are many relationships with other work:

<sup>&</sup>lt;sup>6</sup>The layered assumption structure does not rule out axioms that depend on other axioms. If we want an axiom Q in class  $C_2$  that depends on axiom P in class  $C_1$ , we can write the axiom ( $\forall x <: C_1 \cdot P[x/Current]$ )  $\Rightarrow$  Q in class  $C_2$  instead, where x is a fresh variable. After axiom verification, method and statement verification can use ( $\forall y <: C_2 \cdot Q[y/Current]$ ) where y is fresh. The proof system enables the specification and verification of invariants for aggregate structures involving multiple objects of different types.

Class invariants	Axiom clauses
Related to object consistency	No notion of object consistency
Hold at particular program points	Hold everywhere
Expressed i.t.o. fields and pure method calls	Expressed as logical predicates
Constrain operations	Constrain logical abstractions of data
Verification involves methods	Verification cannot involve methods

Table 3.2: The main differences between class invariants and axiom clauses.

**Axiom clauses** We do not know of any existing specification mechanisms that are closely related to axiom clauses, in the sense that they describe and enforce relationships between logical abstractions of data in a similar way for inheritance hierarchies.

Class invariants form the basis of several OO specification and verification approaches, including Spec# [3] and JML [40]. Two main flavours of class invariants exist: private invariants, as exemplified by the object invariants of Spec#, and public invariants [41], which include JML's derived invariants and the invariants of Jacobs and Piessens for describing relationships between inspector methods [33]. Class invariants, like axiom clauses, constrain subclasses. However, there are several important differences between them. Class invariants either define exactly when an object is consistent (private invariants), or describe abstract properties of consistent objects (public invariants). Axiom clauses have no notion of object consistency. Class invariants are expected to hold at particular points in a program and may be broken at others, according to the employed invariant protocol [25]. Axiom clauses are true invariants in the sense that they hold everywhere. The relationships they describe cannot be violated by assignment statements, and hence there are no problems with e.g. method callbacks [44, 39]. Class invariants are expressed in terms of fields (including model fields) and pure method calls. Public invariants constrain operations that must establish or preserve them. Private invariants constrain the pure methods they use and indirectly also other operations that depend on the private invariant. Axioms are expressed as logical predicates, and they constrain logical abstractions of data. The verification of private invariants involves the inspection of method bodies (for the proof obligations see e.g. [45]), and the verification of public invariants involves private invariants and the specifications and/or bodies of pure methods. Axiom clauses are verified prior to methods and do not depend on methods in any way. The main differences between class invariants and axiom clauses are summarised in Table 3.2.

**Export clauses** There is some correspondence between the class axioms of Kassios [37, 36] and apfs/export clauses. Class axioms can be used to axiomatise the specification and program attributes of a class. In a class implementation, class axioms typically describe abstract state (represented by specification attributes) as a function of the concrete implementation state (represented by program attributes). This corresponds loosely to the way apf entry definitions relate the concrete state to apf arguments. Furthermore, class axioms can describe consequences of a specification attribute such as a class invariant, which typically include framing properties and relationships between other specification attributes. This corresponds somewhat to export clauses that describe properties of apf arguments or relationships between them. The framework of class axioms does not include inheritance, so despite the similar names, it is appropriate to compare them with export and not axiom clauses. Class axioms can also define method specification-s/implementations, which apfs and export clauses are incapable of.

In jStar [23], the modifier 'export' can be added to an apf entry definition to expose it to all other classes. Even though our export clauses are more general and flexible than this mechanism, MultiStar supports it as a useful shorthand.

The rules for lossless casting by Chin et al. [14] describe relationships between predicates that provide full and partial views of objects. A view predicate describes the contents of the fields of an object directly: a full view of object o provides full knowledge of all o's fields, while a partial view with respect to class C describes only values of fields introduced by C and its ancestors. View predicates and relationships between them are generated automatically. The relationships do not have to be verified and do not constrain subclasses.

Krishnaswami et al. use so-called 'static specifications' in [38] to specify relationships between abstract predicates. Although not presented in an OO context, these relationships must be satisfied by implementations and are thus related to our export clauses.

The lemma functions of VeriFast [34] record proofs of relationships between predicates. The relationships are then used in reasoning; the proof of the Composite pattern in [35] provides a good example. Lemma functions, like export clauses, do not constrain subclasses.

**Multiple inheritance** Surprisingly few systems exist for reasoning about multiple inheritance. The system in [46] also uses separation logic, but without abstraction mechanisms such as apfs. Most of the paper is devoted to elementary separation logic proof rules that also apply in a single-inheritance

context. Diamond inheritance is never treated, and the bodies of inherited methods are re-verified in subclasses.

The focus of [24] is on behavioural subtyping. It proposes to verify behavioural subtyping of methods lazily, i.e. only to the extent demanded by client code. Supplier code is then continually re-verified as a client's use of it grows.

The restricted form of interface inheritance is easily handled by our proof system: an interface is simply an abstract class with only abstract methods and no fields. Many verification tools for OO programs, including Spec# [3] and the JML toolset [9], provide support for specifying and verifying interface inheritance. Both Spec# and JML use pure expressions of the programming language for specification, and follow a class invariant-based approach to verification.

# CHAPTER 4

# VERIFYING EXECUTABLE CONTRACTS

Many conventional OO program specification approaches, including Eiffel [26], Spec# [3] and JML [40], use Boolean expressions of the programming language to specify routine preconditions and postconditions, class invariants, and other assertions which hold at particular points such as loop invariants. Explaining the operational meaning of such executable assertions to programmers is easy, as they already know the meaning of programming language expressions. Even if such specifications are not formally verified, their runtime checking and value for testing provide significant benefits. Their popularity in software development is therefore not surprising.

Verifying that executable specifications will always hold at runtime is not easy. Expressions used for assertions typically include calls to methods which depend on the heap and which may also cause side-effects such as heap mutation. Consider for example a class SLIST which implements a sorted list of integers. The postcondition of method insert(i) can be specified as has(i), meaning that the list has or contains an element i. Yet has(i) might allocate a new iterator, traverse the linked structure, and temporarily affect bookkeeping of active iterators. Translating expressions such as has(i) into logical formulae for static verification is hard. Moreover, programming language expressions might have preconditions and are generally not guaranteed to terminate. This further complicates matters for verification.

In contrast to executable specifications, separation logic predicates are not computations. They specify the effects of computations in terms of program states, i.e. the contents of the stack and heap, and not in terms of outcomes of further computations. Proof systems based on separation logic (such as the ones we have seen) are promising for OO verification and can successfully verify common programming patterns such as the Visitor [23] and Composite [35]. Unfortunately, the semantics of separation logic is not yet widely known by OO programmers.

How separation logic specifications can be used to tame executable ones is the topic of this chapter. The guarantee is simple: if a program satisfies its separation logic specification and the executable assertions are verified with the separation logic ones, then all executable assertions are guaranteed to hold at runtime. We do not verify the program with respect to its executable assertions, we verify the program and its executable assertions with respect to a separation logic specification. For example, if a program containing class SLIST satisifies its separation logic specification, and the separation logic postcondition of insert(i) is Q, then we are guaranteed that the executable postcondition, has(i), will always hold at runtime if we can prove the connecting implication  $Q \Rightarrow has(i)$ . A connecting implication connects the world of separation logic predicates with the one of Boolean programming language expressions. It is proved in this case by deriving the Hoare-style triple  $\{Q\}v := has(i)\{Q \land v = True\}$ , where v is a fresh variable. A novel notion called *relative purity* is embedded in connecting implications and embraces side-effects in expressions such as has(i). The logical framework depends on the non-faulting semantics of separation logic triples and its \*-connective, as we shall see.

We believe that executable and separation logic specifications can be complementary in OO software development. The proposed formal framework accommodates both ordinary programmers and proof experts. Programmers can express their intentions in the form of executable assertions and use runtime checking to identify faults. While software designs are still evolving, it is probably also easier to change executable assertions than more elaborate specifications. We envisage that in a second phase, once the software has stabilised and many faults have been removed, proof experts would annotate the critical parts with separation logic for verification. At this point the executable assertions do not have to be discarded: our framework integrates the specification approaches and can verify whether the expectations recorded in executable assertions are fulfilled. Problems in the verification of executable assertions can indicate discrepancies between the two types of specifications, e.g. a misunderstanding by the separation logic specifier as to when a routine may be called. Executable and separation logic specifications can therefore complement each other even in verification.

This chapter makes several contributions in the area of OO specification and verification:

1. It describes a simple technique based on connecting implications for

verifying executable preconditions and postconditions. It gives executable assertions a semantics based on their semantics as expressions. The formalisation can even be applied to contracts for non-OO nongarbage collected languages such as C.

- 2. It presents simple techniques to verify class invariants. If an invariant is specified with a separation logic predicate, then properties of the invariant, such as the fact that it holds in all visible states [52], often follow as a consequence. The framework can help to devise flexible and sound class invariant protocols.
- 3. It illustrates the framework's applicability to model-based specifications [12], where model classes and model queries are used to strengthen contracts [63].
- 4. It shows how the novel notion of relative purity tolerates side-effects in executable assertions to a high degree.

The techniques are well-suited to separation logic proof tools. We implemented them in the MultiStar tool [65] and verified the examples automatically.

**Chapter outline** A discussion of background material follows in Section 4.1. Precondition and postcondition verification are the topics of Sections 4.2 and 4.3 respectively. Section 4.4 contains an exposition on class invariants. The verification techniques are then applied to model-based specifications in Section 4.5. Relative purity and predicate extraction are considered in Section 4.6. A discussion of the MultiStar implementation follows in Section 4.7. Finally, Section 4.8 concludes and mentions related work.

# 4.1 Background

This chapter presents the verification framework in an abstract setting, which can be instantiated with concrete languages and proof systems. The abstract setting is not committed to a particular separation logic, proof system or language type. It is even applicable to non-OO non-garbage-collected languages such as C. The presentation uses the OO language and proof system of Chapter 2 to illustrate the abstract ideas with concrete examples.

#### 4.1.1 The abstract setting: triples and footprints

An abstract triple is of the form  $\{P\}s\{Q\}$ . The partial correctness meaning of a triple is as follows: statement s does not fault when executed in a state satisfying P, and if it terminates then the resulting state satisfies Q. Faulting occurs when s accesses unallocated memory. In OO terms, 'unallocated memory' means heap storage which is not necessarily present in the initial state<sup>1</sup> and not allocated by s before being accessed. O'Hearn [54] uses the term *footprint* of s to describe the minimal state from which s can be executed safely. So s when executed in a state satisfying P will not fault if P describes at least the footprint of s. The notion of footprint has been expanded since [59], yet this is of little concern here.

#### 4.1.2 The concrete setting

The OO language and proof system of Chapter 2 are convenient for illustrating the abstract concepts. To improve readability, we drop empty argument lists in method declarations and calls, and adhere to the uniform access principle [26] where queries can be implemented by fields or result-returning methods. As usual in Hoare-style logics, predicates may use inductive data types and functions involving them. This chapter employs only sequences of integers, where  $\alpha$  and  $\beta$  are sequence-valued variables.  $\epsilon$  denotes the empty sequence, and [e] denotes the singleton sequence whose only element is e. The length of  $\alpha$  is written  $|\alpha|$ , the i'th element of  $\alpha$  is  $\alpha_i$ , and  $\alpha + \beta$  denotes the sequence obtained by appending  $\alpha$  and  $\beta$ .

The Boolean expressions of the executable contract language can be more complex than the expressions of the kernel language of Chapter 2. In particular, feature calls are allowed in contract expressions. To facilitate formal reasoning about executable assertions, the function  $(E)_v$  yields a kernel language statement which captures the meaning of the contract expression E in the variable v. Said otherwise,  $(E)_v$  translates the sugared statement v := Einto an appropriate kernel language statement. Formally, assume that v has the same static type as E and that t,  $t_1, \ldots t_n$  are fresh variables:

$$\begin{aligned} & (\mathbf{x})_{\mathbf{v}} & \stackrel{\text{def}}{=} & \mathbf{v} := \mathbf{x}, \text{ if } \mathbf{x} \text{ is a variable or a constant.} \\ & (\mathbf{e}_0.\mathbf{f}(\mathbf{e}_1, \dots, \mathbf{e}_n))_{\mathbf{v}} & \stackrel{\text{def}}{=} & (\mathbf{e}_0)_{\mathbf{t}_0}; \dots; (\mathbf{e}_n)_{\mathbf{t}_n}; \mathbf{v} := \mathbf{t}_0.\mathbf{f}(\mathbf{t}_1, \dots, \mathbf{t}_n) \\ & (\mathbf{not} \ \mathbf{e})_{\mathbf{v}} & \stackrel{\text{def}}{=} & (\mathbf{e})_{\mathbf{t}}; \mathbf{v} := \mathbf{not} \ \mathbf{t} \\ & (\mathbf{e}_1 \text{op } \mathbf{e}_2)_{\mathbf{v}} & \stackrel{\text{def}}{=} & (\mathbf{e}_1)_{\mathbf{t}_1}; (\mathbf{e}_2)_{\mathbf{t}_2}; \mathbf{v} := \mathbf{t}_1 \ \text{op } \mathbf{t}_2, \text{ if } \mathbf{op} \in \{\mathbf{and}, +, -, =, >=\}. \end{aligned}$$

<sup>1</sup>Separation logic systems do not rely on well-formedness of the heap.

The translation function fixes the semantics of contract language expressions. For example, if we want the contract language to use short-circuit evaluation of  $e_1$  and  $e_2$  instead, then its translation would involve a conditional statement. The definition above is sufficient for our purposes, and we use it in the examples that follow.

## 4.2 Precondition verification

Given a separation logic predicate P and an executable assertion B which should hold at the same program point, the question is whether B somehow follows from P. To this end we define the connecting implication  $P \Rightarrow B$ , which informally means that P is sufficient to evaluate B to True:

 $P \Rightarrow B \stackrel{\text{def}}{=} \{P\}v := B\{P \land v = \text{True}\}, \text{ where } v \text{ is a fresh variable.}$ 

The intuition behind the definition is that in every state satisfying P, we can evaluate B into v without faulting, and if this computation terminates, then P will hold in the resulting state and v will have value True. The routine body relies on P, so it must be re-established by the evaluation of B. This enforces a notion of purity on B, i.e. it prevents B from performing certain kinds of side-effects while allowing others. Section 4.6.1 contains more about this.

In the concrete setting of the OO proof system, there is a sufficient condition<sup>2</sup> for  $P \Rightarrow B$ :

If  $\Delta; \Gamma \vdash_s \{P\}(B)_v \{P * v = True\}$ , then  $P \Longrightarrow B$  under  $\Delta$  and  $\Gamma$ .

In intuitionistic separation logic, which is used for garbage-collected OO languages such as our concrete one,  $(P * e = e') \Leftrightarrow (P \land e = e')$ . The  $\Delta; \Gamma$  used to verify a B appearing in class C is the same  $\Delta; \Gamma$  under which statements and methods of C are verified. In the examples of this chapter we omit explicit reference to  $\Delta$  and  $\Gamma$ , since none of them uses the additional logical assumptions in  $\Delta$ , and method specifications in  $\Gamma$  can be read directly from code listings.

*Examples.* Consider the method preconditions of class SLIST in Figure 4.1.

1. The executable precondition of the constructor *SLIST* and features *insert*, *has*, *count* and *is\_empty* is True. For any separation logic predicate P it holds that  $P \Rightarrow$  True:

 $<sup>^2 \</sup>rm Whether or not this condition is also necessary depends on the completeness of the proof system.$ 

```
class SLIST
define x.L_{SLIST}(l:\alpha) as ...
axiom
     L_sorted: \forall \alpha \cdot \mathbf{L}(\mathbf{l}; \alpha) \Rightarrow [\forall \mathbf{i}, \mathbf{j} \in (1, |\alpha|) \cdot \mathbf{i} < \mathbf{j} \Rightarrow \alpha_{\mathbf{i}} \le \alpha_{\mathbf{i}}]
feature
     SLIST
     dynamic {True}_{{L(l: \epsilon)}}
     executable {True}_{is_empty}
     insert(i: INT)
     dynamic \{L(l;\alpha)\}_{-}\{\exists \alpha_{\rm F}, \alpha_{\rm S} \cdot L(l;\alpha_{\rm F} + [i] + \alpha_{\rm S}) * \alpha_{\rm F} + \alpha_{\rm S} = \alpha\}
     executable {True}_{has(i) and count = old(count) + 1}
     remove_first
     dynamic {L(l: [e] ++ \alpha)}_{{L(l: \alpha)}}
     executable {not is_empty}_{count} = old(count) - 1}
     first: INT
     dynamic {L(l: [e] ++ \alpha)}-{L(l: [e] ++ \alpha) * Result = e}
     executable {not is_empty}_{not is_empty}
     has(i: INT): BOOL
     dynamic {L(l:\alpha)}_{{L(l:\alpha)} * Result = (\exists j \in 1.. |\alpha| \cdot \alpha_j = i)}
     executable {True}_{True}
     count: INT
     dynamic {L(l:\alpha)}-{L(l:\alpha) * Result = |\alpha|}
     executable {True}_{True}
     is_empty: BOOL
     dynamic {L(l:\alpha)}-{L(l:\alpha) * Result = (\alpha = \epsilon)}
     executable {True}_{True}
invariant
     count_non_negative: {L(l:\alpha)} count >= 0
     empty_definition: {L(l:\alpha)} is_empty = (count = 0)
end
```

Figure 4.1: The interface of a sorted list class.

	Assignment axiom
$\Lambda \cdot \Gamma \vdash_{\circ} \{ True = True \} v := True \{ v = True \}$	Assignment axiom
	Consequence
$\Delta; \Gamma \vdash_s \{ \text{True} \} \mathbf{v} := \text{True} \{ \mathbf{v} = \text{True} \}$	Framo rulo
$\Delta$ : $\Gamma \vdash_{\mathfrak{s}} \{ \text{True} * P \} v := \text{True} \{ v = \text{True} * P \}$	
	Consequence
$\Delta; \Gamma \vdash_s \{P\}v := \operatorname{True}\{P * v = \operatorname{True}\}$	

The side-condition of the Frame rule is satisfied:  $modifies(v := True) = \{v\}$ and  $\{v\} \cap FV(P) = \emptyset$  (remember that v is fresh). The second application of Consequence used the commutativity of \* and the fact that  $P \Rightarrow (P * True)$  in intuitionistic separation logic.

Instead of such detailed proofs, the rest of the chapter uses proof outlines where statements are interspersed between assertions. Explicitly mentioning the freshness of certain variables is omitted if it is clear from the context. The above proof looks as follows in outline form:

 $\begin{aligned} \{ \mathbf{P} \} \\ \mathbf{v} &:= \mathrm{True} \\ \{ \mathbf{P} * \mathbf{v} &= \mathrm{True} \\ \end{aligned}$ 

2. The preconditions of *remove\_first* and *first* are identical. Here is a proof of  $L(l:[e] ++ \alpha) \Rightarrow not$  is\_empty:

$$\{ L(l: [e] ++ \alpha) \}$$
  

$$t := is\_empty$$
  

$$\{ L(l: [e] ++ \alpha) * t = ([e] ++ \alpha = \epsilon) \}$$
  

$$\{ L(l: [e] ++ \alpha) * t = False \}$$
  

$$v := not t$$
  

$$\{ L(l: [e] ++ \alpha) * t = False * v = \neg t \}$$
  

$$\{ L(l: [e] ++ \alpha) * v = True \}$$

3. The executable precondition does not always follow from the separation logic one. Suppose that the executable precondition of *has* is not True but instead **not** is\_empty. A proof attempt of  $L(l:\alpha) \Rightarrow (not is_empty)$  proceeds as follows:

$$\begin{aligned} \{ \mathbf{L}(\mathbf{l}; \alpha) \} \\ \mathbf{t} &:= \mathbf{i}_{\mathbf{s}} \text{empty} \\ \{ \mathbf{L}(\mathbf{l}; \alpha) * \mathbf{t} = (\alpha = \epsilon) \} \\ \mathbf{v} &:= \mathbf{not} \mathbf{t} \\ \{ \mathbf{L}(\mathbf{l}; \alpha) * \mathbf{t} = (\alpha = \epsilon) * \mathbf{v} = \neg \mathbf{t} \} \\ \{ \mathbf{L}(\mathbf{l}; \alpha) * \mathbf{v} = (\alpha \neq \epsilon) \} \end{aligned}$$

Since  $\alpha \neq \epsilon$  is not a consequence, the proof cannot conclude with  $L(l;\alpha) * v =$  True. The separation logic specification is too weak to demonstrate the executable one.<sup>3</sup>

Such a clash of specifications has several potential causes. The executable assertion is maybe overly restrictive, or it captures important semantic properties of the domain that the separation logic one ignores. If the issue is not resolved, then there is of course no guarantee that the executable assertion will hold at runtime.  $\Box$ 

The rest of the chapter contains more examples of the form  $P \Rightarrow B$ .

## 4.3 Postcondition verification

Executable postconditions for routines may contain so-called *old-expressions*. The old-expression old(E) denotes the value of expression E in the state right before the routine is executed, i.e. the state which satisfies the precondition.

Given two separation logic predicates P and Q and an executable assertion B which may contain old-expressions, the connecting implication P,Q  $\Rightarrow$  B informally means that any pre-state satisfying P and any post-state satisfying Q are sufficient to evaluate B to True:

Let  $old(E_1), \ldots, old(E_n)$  be the list of old-expressions in B and let  $v, v_1, \ldots, v_n$  be fresh variables.

$$\begin{array}{rcl} P,Q \Rrightarrow B & \stackrel{def}{=} & \exists R \cdot & \{P\}(v_1 := E_1, \dots, v_n := E_n)\{P \ast R\} \text{ and} \\ & \{Q \ast R\}v := B[v_1; \dots; v_n]\{Q \land v = True\} \\ B[v_1; \dots; v_n] & \stackrel{def}{=} & B[v_1/\text{old}(E_1), \dots, v_n/\text{old}(E_n)] \end{array}$$

Intuitively, R contains the result of evaluating  $(v_1:=E_1,\ldots,v_n:=E_n)$  in the pre-state where P holds. Old-expression evaluation must re-establish P because the routine body relies on it. The existence of old-expression results depends on runtime checking, so R is not contained in Q and the body is not allowed to access state described by R. The body is verified only with respect to P and Q, which guarantees that it will establish Q upon termination and never touch R. So Q \* R holds right after the body's execution. Evaluating  $B[v_1;\ldots;v_n]$  in this state should yield True and re-establish Q, since clients

<sup>&</sup>lt;sup>3</sup>In fact we have shown that (**not** is\_empty) evaluates to False if  $\alpha = \epsilon$ , so the only way it can also evaluate to True is when it loops forever. A verified implementation of SLIST (and classes transitively used by it) for which (**not** is\_empty) terminates when executed in some initial state satisfying L(l: $\alpha$ ) comprises a counterexample for L(l: $\alpha$ )  $\Rightarrow$  (**not** is\_empty).



Figure 4.2: (a) Routine r and its specification. (b) Resulting proof obligations. (c) Proof obligations as triples in a proof outline.

depend on the fact that Q holds. Figure 4.2 summarises the proof obligations of a routine.

The predicate R will almost certainly have  $v_1, \ldots, v_n$  as free variables, but should not have free variables which are modified by the routine body. This allows the transfer of R over the routine body with the Frame rule.

An assertion language's semantics will stipulate how to prove a triple of the form  $\{P\}(v_1 := E_1, \ldots, v_n := E_n)\{Q\}$ . For example, if old-expressions are evaluated in an unspecified order, then  $\{P\}s\{Q\}$  must be proved for every permutation s of  $(v_1:=E_1,\ldots,v_n:=E_n)$ .

In our concrete OO setting, the assertion language specification states that old-expressions will be evaluated in an arbitrary order. The following condition is sufficient (but not necessary<sup>4</sup>) for concluding  $P,Q \Rightarrow B$ :

If  $\forall i \in (1..n)$ .  $\Delta; \Gamma \vdash_s \{P\}(\mathbb{E}_i)_{v_i}\{P * R_i\}$  with **Result**  $\notin FV(R_i)$ and  $\Delta; \Gamma \vdash_s \{Q * R_1 * \ldots * R_n\}_{v_i} := B[v_1; \ldots; v_n]\{Q * v = True\}$ then  $P, Q \Longrightarrow B$  under  $\Delta$  and  $\Gamma$ .

Once again, an assertion B appearing in class C is verified with the same  $\Delta; \Gamma$  as the statements and methods of C.

<sup>&</sup>lt;sup>4</sup>Suppose we take  $(\exists e \cdot o.f \hookrightarrow e * even(e))$  for P and o.f++ for both  $E_1$  and  $E_2$ . Since there exists no  $R_i$  such that  $\Delta; \Gamma \vdash_s \{P\}(\!\!\{E_i\}\!\!)_{v_i}\{P * R_i\}$  for  $i \in 1..2$ , the rule cannot prove P,True  $\Rightarrow \text{odd}(\text{old}(E_1)+\text{old}(E_2))$ .

Relationships between  $P,Q \Rightarrow B$  and  $Q \Rightarrow B$  exist, which are convenient in proofs because many executable postconditions contain no old-expressions.

1. If  $P,Q \Rightarrow B$  then  $Q \Rightarrow B$  whenever B contains no old-expression.

2. If  $Q \Rightarrow B$  then  $P,Q \Rightarrow B$  for any P.

So  $P,Q \Rightarrow B$  iff  $Q \Rightarrow B$  whenever B contains no old-expression.

Examples. Consider class SLIST in Figure 4.1.

1. For *remove\_first*, it is the case that  $L(l:[e] + \alpha), L(l:\alpha) \Rightarrow (count = old(count) - 1)$ . The executable postcondition contains one old-expression and  $E_1 = count$ . Here is the first part of the proof:

$$\begin{aligned} & \{ \mathbf{L}(\mathbf{l}; [\mathbf{e}] ++ \alpha) \} \\ & \mathbf{t}1 := \text{count} \\ & \{ \mathbf{L}(\mathbf{l}; [\mathbf{e}] ++ \alpha) * \mathbf{t}1 = |[e] ++ \alpha | \} \end{aligned}$$

Notice that  $FV(t1 = |[e] + \alpha|) = \{t1, e, \alpha\}$ . Using  $(t1 = |[e] + \alpha|)$  for  $R_1$  completes the proof:

$$\begin{aligned} \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t}\mathbf{1} &= |[e] +\!\!\!+ \alpha | \} \\ \mathbf{t}\mathbf{2} := \operatorname{count} \\ \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t}\mathbf{2} &= |\alpha| * \mathbf{t}\mathbf{1} = |[e] +\!\!\!+ \alpha | \} \\ \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t}\mathbf{2} &= |\alpha| * \mathbf{t}\mathbf{1} = |\alpha| + 1 \} \\ \mathbf{t}\mathbf{3} := \mathbf{t}\mathbf{1} - \mathbf{1} \\ \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t}\mathbf{2} &= |\alpha| * \mathbf{t}\mathbf{1} = |\alpha| + \mathbf{1} * \mathbf{t}\mathbf{3} = |\alpha| \} \\ \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t}\mathbf{2} &= |\alpha| * \mathbf{t}\mathbf{3} = |\alpha| \} \\ \mathbf{v} := \mathbf{t}\mathbf{2} = \mathbf{t}\mathbf{3} \\ \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t}\mathbf{2} &= |\alpha| * \mathbf{t}\mathbf{3} = |\alpha| * \mathbf{v} = (\mathbf{t}\mathbf{2} = \mathbf{t}\mathbf{3}) \} \\ \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{v} = \operatorname{True} \} \end{aligned}$$

2. The executable postcondition of the constructor *SLIST* contains no old-expression, so it suffices to prove  $L(l:\epsilon) \Rightarrow$  is\_empty:

 $\{ L(l:\epsilon) \}$ v := is\_empty  $\{ L(l:\epsilon) * v = (\epsilon = \epsilon) \}$  $\{ L(l:\epsilon) * v = True \}$ 

3. The executable postcondition of *first* contains no old-expression, so we only need (L(l: [e]  $++ \alpha$ ) \* **Result** = e)  $\Rightarrow$  **not** is\_empty. Example 2 in the previous section established the inner triple of the proof:
$\{ L(l: [e] + \alpha) * \mathbf{Result} = e \}$   $\{ L(l: [e] + \alpha) \}$   $(\mathbf{not} is\_empty) \rangle_v$   $\{ L(l: [e] + \alpha) * v = True \}$   $\{ L(l: [e] + \alpha) * v = True * \mathbf{Result} = e \}$   $\{ L(l: [e] + \alpha) * \mathbf{Result} = e * v = True \}$ 

The Frame rule is key to the proof.

4. Consider the postcondition of *insert*. It contains one old-expression, namely **old**(count):

 $\begin{aligned} & \{ \mathbf{L}(\mathbf{l}; \alpha) \} \\ & \mathbf{t}1 := \mathbf{count} \\ & \{ \mathbf{L}(\mathbf{l}; \alpha) * \mathbf{t}1 = |\alpha| \} \end{aligned}$ 

The second part of the proof consists of small pieces which are put together.

$$\{ L(l: \alpha_{\rm F} ++ [i] ++ \alpha_{\rm S}) \}$$
  

$$t2 := has(i)$$
  

$$\{ L(l: \alpha_{\rm F} ++ [i] ++ \alpha_{\rm S}) * t2 = (\exists j \in 1.. | \alpha_{\rm F} ++ [i] ++ \alpha_{\rm S} | \cdot (\alpha_{\rm F} ++ [i] ++ \alpha_{\rm S})_{j} =$$
  

$$i) \}$$
  

$$\{ L(l: \alpha_{\rm F} ++ [i] ++ \alpha_{\rm S}) * t2 = True \}$$

Applying Frame and Consequence to this triple yields Piece 1:

$$\{ L(l: \alpha_{\rm F} ++ [i] ++ \alpha_{\rm S}) * \alpha_{\rm F} ++ \alpha_{\rm S} = \alpha * t1 = |\alpha| \}$$
  
t2 := has(i)  
$$\{ L(l: \alpha_{\rm F} ++ [i] ++ \alpha_{\rm S}) * \alpha_{\rm F} ++ \alpha_{\rm S} = \alpha * t1 = |\alpha| * t2 = \text{True} \}$$

Next, we prove

$$\begin{split} & \{ L(l; \alpha_F ++ [i] ++ \alpha_S) * \alpha_F +\!\!+ \alpha_S = \alpha * t1 = |\alpha| \} \\ & t3 := \text{count} \\ & \{ L(l; \alpha_F +\!\!+ [i] +\!\!+ \alpha_S) * t3 = |\alpha_F +\!\!+ [i] +\!\!+ \alpha_S| * \alpha_F +\!\!+ \alpha_S = \alpha * t1 = |\alpha| \} \\ & \{ L(l; \alpha_F +\!\!+ [i] +\!\!+ \alpha_S) * t3 = |\alpha| +\!\!1 * \alpha_F +\!\!+ \alpha_S = \alpha * t1 = |\alpha| \} \\ & t4 := t1 + 1 \\ & \{ L(l; \alpha_F +\!\!+ [i] +\!\!+ \alpha_S) * t3 = |\alpha| +\!\!1 * \alpha_F +\!\!+ \alpha_S = \alpha * t1 = |\alpha| * t4 = t1 + 1 \} \\ & \{ L(l; \alpha_F +\!\!+ [i] +\!\!+ \alpha_S) * t3 = |\alpha| +\!\!1 * \alpha_F +\!\!+ \alpha_S = \alpha * t4 = |\alpha| +\!\!1 \} \\ & t5 := t3 = t4 \\ & \{ L(l; \alpha_F +\!\!+ [i] +\!\!+ \alpha_S) * t3 = |\alpha| +\!\!1 * \alpha_F +\!\!+ \alpha_S = \alpha * t4 = |\alpha| +\!\!1 * t5 = (t3 =\!\!t4) \} \\ & \{ L(l; \alpha_F +\!\!+ [i] +\!\!+ \alpha_S) * \alpha_F +\!\!+ \alpha_S = \alpha * t5 = \text{True} \} \end{split}$$

Applying Frame establishes Piece 2:

```
\{ L(l: \alpha_{\rm F} ++ [i] ++ \alpha_{\rm S}) * \alpha_{\rm F} ++ \alpha_{\rm S} = \alpha * t1 = |\alpha| * t2 = {\rm True} \} \\ t3 := {\rm count} \\ t4 := t1 + 1 \\ t5 := t3 = t4 \\ \{ L(l: \alpha_{\rm F} ++ [i] ++ \alpha_{\rm S}) * \alpha_{\rm F} ++ \alpha_{\rm S} = \alpha * t5 = {\rm True} * t2 = {\rm True} \}
```

Here is Piece 3:

 $\{ \mathbf{L}(\mathbf{l}: \alpha_{\mathrm{F}} ++ [\mathbf{i}] ++ \alpha_{\mathrm{S}}) * \alpha_{\mathrm{F}} ++ \alpha_{\mathrm{S}} = \alpha * \mathbf{t5} = \mathrm{True} * \mathbf{t2} = \mathrm{True} \}$   $\mathbf{v} := \mathbf{t2} \text{ and } \mathbf{t5}$   $\{ \mathbf{L}(\mathbf{l}: \alpha_{\mathrm{F}} ++ [\mathbf{i}] ++ \alpha_{\mathrm{S}}) * \alpha_{\mathrm{F}} ++ \alpha_{\mathrm{S}} = \alpha * \mathbf{t5} = \mathrm{True} * \mathbf{t2} = \mathrm{True} * \mathbf{v} = (\mathbf{t2} \text{ and } \mathbf{t5}) \}$   $\{ \mathbf{L}(\mathbf{l}: \alpha_{\mathrm{F}} ++ [\mathbf{i}] ++ \alpha_{\mathrm{S}}) * \alpha_{\mathrm{F}} ++ \alpha_{\mathrm{S}} = \alpha * \mathbf{v} = \mathrm{True} \}$ 

The proof is completed by putting the three pieces together, eliminating  $\alpha_{\rm F}$  and  $\alpha_{\rm S}$  with the Auxiliary variable elimination rule, and applying Consequence to move (t1 =  $|\alpha|$ ) and (v = True) out from under the quantifiers to the top level in the precondition and postcondition respectively.

# 4.4 Class invariant verification

Consider an executable assertion B which should hold at program point pp. If the separation logic predicate P holds at pp and P  $\Rightarrow$  B, then B is verified for pp. This technique can be used to verify executable assert statements and loop invariants, for example.

Class invariants<sup>5</sup> (henceforth simply called invariants) are verified in a similar way. Since an invariant protocol [64] always specifies the points in a program where an invariant, say B, should hold, B is verified if it is verified for all such points.

Another technique is to annotate an invariant B with a separation logic predicate P which characterises the states in which the invariant should hold. If  $P \Rightarrow B$ , then B is verified. This offers a flexible scheme if annotations are given on the level of individual invariant clauses.

*Examples.* Consider the following invariant clauses, reproduced from the bottom of class SLIST in Figure 4.1:<sup>6</sup>

<sup>&</sup>lt;sup>5</sup>Also called *object invariants*.

<sup>&</sup>lt;sup>6</sup>These are public invariants, as opposed to private representation invariants (which is how the term 'invariant' is used in the Spec# literature, e.g. [45]). Private invariants can be verified in a similar way.

## invariant

count\_non\_negative: {L(l: $\alpha$ )} count >= 0 empty\_definition: {L(l: $\alpha$ )} is\_empty = (count = 0)

1. For the clause named *count\_non\_negative*,  $L(l:\alpha) \Rightarrow (count \ge 0)$  holds:

$$\begin{aligned} \left\{ \begin{split} \mathbf{L}(\mathbf{l};\alpha) \right\} \\ \mathbf{t} &:= \mathrm{count} \\ \left\{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t} = |\alpha| \right\} \\ \mathbf{v} &:= \mathbf{t} >= 0 \\ \left\{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t} = |\alpha| * \mathbf{v} = (\mathbf{t} \geq 0) \right\} \\ \left\{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{v} = \mathrm{True} \right\} \end{aligned}$$

2. The invariant clause *empty\_definition* is verified similarly:

$$\begin{aligned} \{ L(l;\alpha) \} \\ t1 &:= is\_empty \\ \{ L(l;\alpha) * t1 = (\alpha = \epsilon) \} \\ t2 &:= count \\ \{ L(l;\alpha) * t2 = |\alpha| * t1 = (\alpha = \epsilon) \} \\ t3 &:= t2 = 0 \\ \{ L(l;\alpha) * t2 = |\alpha| * t1 = (\alpha = \epsilon) * t3 = (t2 = 0) \} \\ \{ L(l;\alpha) * t1 = (\alpha = \epsilon) * t3 = (|\alpha| = 0) \} \\ v &:= t1 = t3 \\ \{ L(l;\alpha) * t1 = (\alpha = \epsilon) * t3 = (|\alpha| = 0) * v = (t1 = t3) \} \\ \{ L(l;\alpha) * v = ((\alpha = \epsilon) = (|\alpha| = 0)) \} \\ \{ L(l;\alpha) * v = True \} \end{aligned}$$

The fact that the structural rules of separation logic are sound provides a simple way to show that a verified invariant clause holds at a particular program point. The following rules can also be used when verifying other executable assertions:<sup>78</sup>

<sup>&</sup>lt;sup>7</sup>Similar rules can be given for  $P,Q \Rightarrow B$ .

<sup>&</sup>lt;sup>8</sup>These rules are independent of the structure of B. Rules also exist which do depend on its structure. For example, if  $P_1 \Rightarrow B_1$  and  $P_2 \Rightarrow B_2$ , then  $(P_1 * P_2) \Rightarrow (B_1 \text{ par\_and} B_2)$ . This rule allows concurrency in executable assertions.

$$\frac{P \Rightarrow B}{(P * Q) \Rightarrow B} \text{ Frame'}$$

$$\frac{P \Leftrightarrow Q \quad P \Rightarrow B}{Q \Rightarrow B} \text{ Conseq'}$$

$$\frac{P \Rightarrow B}{(\exists x \cdot P) \Rightarrow B} \text{ AuxVarElim'}$$

$$\frac{P \Rightarrow B \quad Q \Rightarrow B}{(P \lor Q) \Rightarrow B} \text{ Disj'}$$
and others.

The Frame' rule does not need any side-condition, since the translation of B will modify only fresh variables, i.e. variables not free in Q. Note the way Conseq' is written: if  $P \Rightarrow B$  and  $Q \Rightarrow P$ , then  $Q \Rightarrow B$  does *not* necessarily hold (example 3 in Section 4.6.1 provides a counterexample). In the AuxVarElim' rule, the variable x may not be free in B.

*Example.* If a program containing class SLIST from Figure 4.1 is verified, then it follows as a consequence of the separation logic specification that both invariant clauses hold in the visible states [52] of SLIST. In other words, both clauses are established by the constructor and hold on entry and exit of all other public features. Since each clause B is verified, we know  $L(l:\alpha) \Rightarrow B$ . In the following proofs the ' $\Rightarrow$  B' part is omitted.

1. Upon exit from SLIST:

$$\begin{array}{r} \underline{L(l:\alpha)} & \text{Frame'} \\ \hline \underline{L(l:\alpha) * \alpha = \epsilon} & \text{Conseq'} \\ \hline \underline{L(l:\epsilon) * \alpha = \epsilon} & \text{AuxVarElim'} \\ \hline \underline{\exists \alpha \cdot L(l:\epsilon) * \alpha = \epsilon} & \text{Conseq'} \\ \hline \underline{L(l:\epsilon) * \text{True}} & \text{Conseq'} \\ \hline \underline{L(l:\epsilon)} & \text{Conseq'} \\ \hline \end{array}$$

2. Upon entry to *remove\_first*:

$L(l:\alpha)$	Frame'
$L(l:\alpha)  \ast  \alpha = \beta$	Consoc'
$L(l:\beta) * \alpha = \beta$	Conseq
$\exists \alpha \cdot \mathbf{L}(\mathbf{l}; \beta) * \alpha = \beta$	AuxVarElim
$\frac{1}{1} \frac{1}{1} \frac{1}{\beta}$	Conseq'
$- \frac{L(1,p)}{L(1,p)}$	Frame'
$L(I:\beta) * \beta = [e] ++ \alpha$	Conseq'
$\mathbf{L}(\mathbf{l}: [\mathbf{e}] + \alpha) * \beta = [\mathbf{e}] + \alpha$	AuxVarElim'
$\exists \beta \cdot \mathbf{L}(\mathbf{l}: [\mathbf{e}] ++ \alpha) * \beta = [e] ++ \alpha$	Conseq'
$L(l:[e] ++ \alpha)$	Conseq

3. For the exit of *insert*, we start with  $L(l: \beta)$  which was derived in the previous proof.

$$\begin{array}{c} L(l;\beta) & \text{Frame'} \\ \hline L(l;\beta) * \beta = (\alpha_{\text{F}} + + [i] + \alpha_{\text{S}}) * \alpha_{\text{F}} + + \alpha_{\text{S}} = \alpha \\ \hline \exists \beta \cdot L(l;\beta) * \beta = (\alpha_{\text{F}} + + [i] + \alpha_{\text{S}}) * \alpha_{\text{F}} + + \alpha_{\text{S}} = \alpha \\ \hline L(l;\alpha_{\text{F}} + + [i] + \alpha_{\text{S}}) * \alpha_{\text{F}} + + \alpha_{\text{S}} = \alpha \\ \hline \exists \alpha_{\text{S}} \cdot L(l;\alpha_{\text{F}} + + [i] + \alpha_{\text{S}}) * \alpha_{\text{F}} + \alpha_{\text{S}} = \alpha \\ \hline \exists \alpha_{\text{F}}, \alpha_{\text{S}} \cdot L(l;\alpha_{\text{F}} + + [i] + \alpha_{\text{S}}) * \alpha_{\text{F}} + \alpha_{\text{S}} = \alpha \\ \hline \exists \alpha_{\text{F}}, \alpha_{\text{S}} \cdot L(l;\alpha_{\text{F}} + + [i] + \alpha_{\text{S}}) * \alpha_{\text{F}} + \alpha_{\text{S}} = \alpha \\ \hline \end{array}$$

In effect we create fine-grained invariant protocols by annotating invariant clauses: the predicate abstractly specifies the program points where they should hold, and invariants can be verified, i.e. guaranteed, to hold there. One can refine this idea to create more sophisticated invariant protocols.

#### Examples.

1. Here is an invariant protocol for single-inheritance programs where subclasses can refine individual invariant clauses.  $\Gamma$  maps a class name and invariant clause name to the invariant specification predicate and Boolean expression. Two rules distinguish the case where class C introduces an invariant clause named inv from the case where C inherits and refines inv from its parent C':

$$\forall i \in (1..n) \cdot (P * Current : C) \Rightarrow B_i$$
  
$$\Gamma \vdash_i introduce inv: \{P\} \langle B_1 \rangle and \dots and \langle B_n \rangle in C$$

 $C' \prec_{1} C$   $\Gamma(C.inv) = \{P\} B$   $B = \langle B_{1} \rangle \text{ and } \dots \text{ and } \langle B_{n} \rangle$   $P \Leftrightarrow (P' * R)$   $\forall i \in (1..n) \cdot (P' * Current : C') \Longrightarrow B_{i}$   $\forall j \in (1..m) \cdot (P' * Current : C') \Longrightarrow B'_{j}$ E = intervit i = (P') P and (P') and a source of P'

 $\Gamma \vdash_i \mathbf{inherit} \text{ inv: } \{P'\} \text{ B and } \langle B'_1 \rangle \text{ and } \dots \text{ and } \langle B'_m \rangle \text{ in } C'$ 

The above invariant protocol guarantees that whenever 'inv: {P} B' appears in class C, then  $\forall x <: C \cdot P[x/Current] \Rightarrow B[x/Current]$  where x is fresh. Enclosing an expression in angle-brackets provides it with a side-effect scope. The next example explains why it works.

2. The previous invariant protocol is an instance of a more general scheme. Here, invariant clauses are equipped with both a dynamic and a static specification predicate. When a class C introduces an invariant clause:

introduce inv: {P} {S}  $\langle B_1 \rangle$  and ... and  $\langle B_n \rangle$ 

then it is considered a shorthand for introducing the methods C\_inv\_i, where  $i \in 1..n$ :

introduce C\_inv\_i: BOOL dynamic  $\{P\}_{P} * \text{Result} = \text{True}\}$ static  $\{S\}_{S} * \text{Result} = \text{True}\}$ do  $(B_i)_{\text{Result}}$  end

When the direct subclass C' refines the invariant clause by listing:

inherit inv:  $\{P'\}$   $\{S'\}$  B and  $\langle B'_1 \rangle$  and ... and  $\langle B'_m \rangle$ 

where  $B = \langle B_1 \rangle$  and ... and  $\langle B_n \rangle$ , then it is considered a shorthand for overriding all the methods C\_inv\_i, where  $i \in 1..n$ :

override C\_inv\_i: BOOL dynamic  $\{P'\}_{} \{P' * \text{Result} = \text{True}\}$ static  $\{S'\}_{} \{S' * \text{Result} = \text{True}\}$ do  $(B_i)_{\text{Result}}$  end

and introducing the methods C'\_inv\_j, where  $j \in 1..m$ :

introduce C'\_inv\_j: BOOL dynamic  $\{P'\}_{} \{P' * \text{Result} = \text{True}\}$ static  $\{S'\}_{} \{S' * \text{Result} = \text{True}\}$ do  $(B'_i)_{\text{Result}}$  end Looking at the method verification rules of 2.6, we see that class C must satisfy:

 $\begin{array}{l} \Delta \vdash \{S\}_{-}\{S \ast \mathbf{Result} = \mathrm{True}\} \xrightarrow{\mathbf{Current} : C} \{P\}_{-}\{P \ast \mathbf{Result} = \mathrm{True}\} \\ \forall i \in (1..n) \cdot S \Rrightarrow B_{i} \end{array}$ 

and class C' must satisfy:

 $\begin{array}{l} \Delta \vdash \{P'\}_{\text{-}}\{P'*\text{Result} = \operatorname{True}\} \Longrightarrow \{P\}_{\text{-}}\{P*\text{Result} = \operatorname{True}\}\\ \Delta \vdash \{S'\}_{\text{-}}\{S'*\text{Result} = \operatorname{True}\} \xrightarrow{\operatorname{Current}:C'} \{P'\}_{\text{-}}\{P'*\text{Result} = \operatorname{True}\}\\ \forall i \in (1..n) \cdot S' \Rrightarrow B_i\\ \forall j \in (1..m) \cdot S' \Rrightarrow B'_j \end{array}$ 

By taking S = P \* Current : C, and S' = P' \* Current : C', it is simple to prove that the two rules of the previous example are theorems. It is also easy to verify the protocol's guarantee.

3. Instead of overriding all the methods C\_inv\_i in class C', as in the previous example, a protocol might choose to inherit them:

inherit C\_inv\_i: BOOL dynamic  $\{P'\}_{} \{P' * \text{Result} = \text{True}\}$ static  $\{S'\}_{} \{S' * \text{Result} = \text{True}\}$ 

Doing so changes the proof obligations of class C' – instead of:

 $\forall i \in (1..n) \cdot S' \Rrightarrow B_i$ 

we must establish that:

 $\Delta \vdash \{S\}_{}\{S * \mathbf{Result} = \mathrm{True}\} \Longrightarrow \{S'\}_{}\{S' * \mathbf{Result} = \mathrm{True}\} \qquad \Box$ 

# 4.5 Model-based specifications

Since executable specifications are frequently not very expressive, model classes and model-based contracts are sometimes used to strengthen them [12, 63].

*Example.* Consider the interface of model class SEQUENCE in Figure 4.3. It provides an abstraction of immutable sequences for specification purposes. Class SLIST can be specified in terms of SEQUENCE, as the interface extract in Figure 4.4 shows. Note that SLIST now has a model query, namely *model*, which returns the immutable sequence abstraction of an SLIST instance at the point when it is called. A comparison of *remove\_first*'s specification in Figures 4.1 and 4.4 shows that the model-based specification involves the

```
class SEQUENCE
define x.SEQ_{SEQUENCE}(s; \alpha) as ...
feature
    SEQUENCE
    dynamic {True}_{{SEQ(s: \epsilon)}}
    cons(i: INT): SEQUENCE
    dynamic {SEQ(s: \alpha)}-{SEQ(s: \alpha) * Result.SEQ(s: [i] + \alpha)}
    head: INT
    dynamic {SEQ(s: [e] + \alpha)}-{SEQ(s: [e] + \alpha) * Result = e}
    tail: SEQUENCE
    dynamic {SEQ(s: [e] + \alpha)}-{SEQ(s: [e] + \alpha) * Result.SEQ(s: \alpha)}
    is_nil: BOOL
    dynamic {SEQ(s: \alpha)}-{SEQ(s: \alpha) * Result = (\alpha = \epsilon)}
    eq(o: SEQUENCE): BOOL
    dynamic {SEQ(s: \alpha) * o.SEQ(s: \beta)}-
                {SEQ(s: \alpha) * o.SEQ(s: \beta) * Result = (\alpha = \beta)}
\mathbf{end}
```

```
Figure 4.3: The interface of model class SEQUENCE.
```

class SLIST

feature

```
 \begin{array}{l} \dots \\ \text{model: SEQUENCE} \\ \textbf{dynamic } \{ \mathbf{L}(\mathbf{l}:\alpha) \}_{-} \{ \mathbf{L}(\mathbf{l}:\alpha) * \textbf{Result.SEQ}(\mathbf{s}:\alpha) \} \\ \textbf{executable } \{ \text{True} \}_{-} \{ \text{True} \} \end{array}
```

```
remove_first

dynamic \{L(l:[e] ++ \alpha)\}_{-}\{L(l:\alpha)\}

executable {not model.is_nil}_{model.eq(old(model).tail)}
```

invariant

```
empty_inv: {L(l:\alpha)} is_empty = model.is_nil end
```

Figure 4.4: An extract from class SLIST which uses model-based contracts.

element values stored in an SLIST instance and not just their number.  $\Box$ 

The following three problems are typically not easy to solve with conventional techniques [39, 19]:

- 1. Devising semantics for model classes and proving their implementations correct.
- 2. Giving a semantics to model queries, such as *model* in Figure 4.4, and proving their implementations correct.
- 3. Verifying model-based specifications, such as the model-based contract of *remove\_first* and the model-based invariant clause *empty\_inv* in Figure 4.4.

The first two problems can be solved with separation logic. Figures 4.3 and 4.4 show separation logic specifications for a model class and model query. A conventional separation logic proof system, such as the one of Chapter 2, can be used to verify their implementations. The third problem can then be addressed with the framework of this chapter.

*Examples.* Consider the model-based specifications of class SLIST in Figure 4.4. Suppose that omitted features have the same separation logic specifications as in Figure 4.1.

1. For the invariant clause *empty\_inv*:

 $\{ L(l:\alpha) \}$   $t1 := is\_empty$   $\{ L(l:\alpha) * t1 = (\alpha = \epsilon) \}$  t2 := model  $\{ L(l:\alpha) * t2.SEQ(s:\alpha) * t1 = (\alpha = \epsilon) \}$   $t3 := t2.is\_nil$   $\{ L(l:\alpha) * t2.SEQ(s:\alpha) * t3 = (\alpha = \epsilon) * t1 = (\alpha = \epsilon) \}$   $\{ L(l:\alpha) * t3 = (\alpha = \epsilon) * t1 = (\alpha = \epsilon) \}$  v := t1 = t3  $\{ L(l:\alpha) * t3 = (\alpha = \epsilon) * t1 = (\alpha = \epsilon) * v = (t1 = t3) \}$   $\{ L(l:\alpha) * v = True \}$ 

2. We next verify the postcondition of *remove\_first* which contains one old-expression:

$$\begin{aligned} & \{ \mathbf{L}(\mathbf{l}; [\mathbf{e}] ++ \alpha) \} \\ & \mathbf{v1} := \mathbf{model} \\ & \{ \mathbf{L}(\mathbf{l}; [\mathbf{e}] ++ \alpha) * \mathbf{v1}. \mathbf{SEQ}(\mathbf{s}; [\mathbf{e}] ++ \alpha) \} \end{aligned}$$

The proof is completed by using v1.SEQ(s:  $[e] + \alpha)$  for R<sub>1</sub>:

$$\begin{aligned} & \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{v1.SEQ}(\mathbf{s};[\mathbf{e}] +\!\!\!+\alpha) \} \\ & \mathbf{t1} := \mathbf{model} \\ & \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t1.SEQ}(\mathbf{s};\alpha) * \mathbf{v1.SEQ}(\mathbf{s};[\mathbf{e}] +\!\!\!+\alpha) \} \\ & \mathbf{t2} := \mathbf{v1.tail} \\ & \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t1.SEQ}(\mathbf{s};\alpha) * \mathbf{v1.SEQ}(\mathbf{s};[\mathbf{e}] +\!\!\!+\alpha) * \mathbf{t2.SEQ}(\mathbf{s};\alpha) \} \\ & \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t1.SEQ}(\mathbf{s};\alpha) * \mathbf{t2.SEQ}(\mathbf{s};\alpha) \} \\ & \mathbf{v} := \mathbf{t1.eq}(\mathbf{t2}) \\ & \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{t1.SEQ}(\mathbf{s};\alpha) * \mathbf{t2.SEQ}(\mathbf{s};\alpha) * \mathbf{v} = (\alpha = \alpha) \} \\ & \{ \mathbf{L}(\mathbf{l};\alpha) * \mathbf{v} = \mathrm{True} \} \end{aligned}$$

# 4.6 Relative purity and predicate extraction

# 4.6.1 Relative purity

Side-effects in executable specifications conventionally complicate verification, since the logical predicates extracted from them cannot be imperative. Most techniques therefore impose some form of purity, i.e. side-effect freeness, on specification expressions. Purity comes in many flavours, such as strong purity, weak purity and observational purity [18, 53].

The verification techniques of this chapter tolerate side-effects in executable specifications to a high degree. In fact, they use a new notion of purity, namely *relative* purity. An executable assertion is pure or impure with respect to a given separation logic specification:

B is pure relative to  $P \stackrel{\text{def}}{=} \{P\}v := B\{P\}$ , where v is a fresh variable.

This means informally that B can be evaluated in a state satisfying P, and that the evaluation will preserve P. Similarly, for an executable assertion B containing the list of **old**-expressions  $old(E_1), \ldots, old(E_n)$ :

B is pure relative to P,Q  $\stackrel{\text{def}}{=} \exists R \cdot \{P\}(v_1 := E_1, \dots, v_n := E_n)\{P * R\}$  and  $\{Q * R\}v := B[v_1; \dots; v_n]\{Q\}$ 

where  $v, v_1, \ldots, v_n$  are fresh variables and  $B[v_1; \ldots; v_n]$  is defined as before.

The following lemmas follow from the definitions by the rule of Consequence:

If 
$$P \Rightarrow B$$
, then B is pure relative to P.  
If  $P,Q \Rightarrow B$ , then B is pure relative to P,Q.

*Examples.* Consider the specification of class SLIST in Figure 4.1.

1. Suppose we replace the executable postcondition of *insert* with a hypothetical assertion B for which  $(B)_v$  is given by:

$$\begin{split} t &:= remove\_first\\ insert(t)\\ v &:= has(i) \end{split}$$

B is pure relative to the separation logic postcondition of *insert* because one can prove<sup>9</sup>  $(\exists \alpha_{\rm F}, \alpha_{\rm S} \cdot L(l: \alpha_{\rm F} ++ [i] ++ \alpha_{\rm S}) * \alpha_{\rm F} ++ \alpha_{\rm S} = \alpha) \Rightarrow B.$ 

<sup>&</sup>lt;sup>9</sup>The proof uses the axiom information expressed in L\_sorted. The rule of Disjunction can combine subproofs arising from a case split on  $\alpha_{\rm F} = \epsilon$ .

2. Suppose B is a hypothetical assertion for which  $(B)_v$  is:

insert(4) $v := not is_empty$ 

B is not pure relative to  $L(l:\alpha)$ . We cannot complete the following proof attempt, since the needed implication does not necessarily hold:

 $\{ \mathbf{L}(\mathbf{l};\alpha) \}$ insert(4)  $\{ \exists \alpha_{\mathrm{F}}, \alpha_{\mathrm{S}} \cdot \mathbf{L}(\mathbf{l};\alpha_{\mathrm{F}} ++ [4] ++ \alpha_{\mathrm{S}}) * \alpha_{\mathrm{F}} ++ \alpha_{\mathrm{S}} = \alpha \}$ v := **not** is\_empty  $\{ \exists \alpha_{\mathrm{F}}, \alpha_{\mathrm{S}} \cdot \mathbf{L}(\mathbf{l};\alpha_{\mathrm{F}} ++ [4] ++ \alpha_{\mathrm{S}}) * \alpha_{\mathrm{F}} ++ \alpha_{\mathrm{S}} = \alpha \}$ // An implication is needed to establish:  $\{ \mathbf{L}(\mathbf{l};\alpha) \}$ 

A counterexample showing that B is not pure relative to P in the context of class C comprises:

- (a) a verified implementation of C and all classes transitively used by it;
- (b) an execution trace where B is evaluated in an initial state satisfying P that either faults or terminates in a state not satisfying P.
- 3. We use B from the previous example and show  $(\exists \alpha \cdot \mathbf{L}(\mathbf{l}; \alpha)) \Rightarrow \mathbf{B}$ :

 $\{\exists \alpha \cdot \mathbf{L}(\mathbf{l}; \alpha)\}$ insert(4)  $\{\exists \alpha, \alpha_{\mathrm{F}}, \alpha_{\mathrm{S}} \cdot \mathbf{L}(\mathbf{l}; \alpha_{\mathrm{F}} ++ [4] ++ \alpha_{\mathrm{S}}) * \alpha_{\mathrm{F}} ++ \alpha_{\mathrm{S}} = \alpha\}$ v := **not** is\_empty  $\{\exists \alpha, \alpha_{\mathrm{F}}, \alpha_{\mathrm{S}} \cdot \mathbf{L}(\mathbf{l}; \alpha_{\mathrm{F}} ++ [4] ++ \alpha_{\mathrm{S}}) * v = \mathrm{True} * \alpha_{\mathrm{F}} ++ \alpha_{\mathrm{S}} = \alpha\}$   $\{\exists \alpha \cdot \mathbf{L}(\mathbf{l}; \alpha) * v = \mathrm{True}\}$ 

So B is pure with respect to  $\exists \alpha \cdot \mathbf{L}(\mathbf{l}; \alpha)$ .

Since  $L(l:\alpha) \Rightarrow (\exists \alpha \cdot L(l:\alpha))$ , this example and the previous one show that if  $P \Rightarrow Q$  and  $Q \Rightarrow B$ , then  $P \Rightarrow B$  does not hold in general.

4. Let B be any executable assertion without old-expressions. B is always pure relative to False. If B never faults, then it is also pure relative to True. □

Any notion of purity which classifies side-effects as intrinsically harmful or not will rule out executable assertions that programmers might want to write. Relative purity judges whether a side-effect is harmful or not only with respect to the properties one cares about. For example, the side-effects in B of examples 2 and 3 are harmless with respect to  $\exists \alpha \cdot \mathbf{L}(\mathbf{l}; \alpha)$ , but harmful with respect to  $\mathbf{L}(\mathbf{l}; \alpha)$ . This is so because the side-effect of inserting 4 in a sorted list will maintain a list structure (what the first purity specification states), but not a list structure with the same elements (what the second specification demands). The concept of relativity pervades verification already, since code is only correct or incorrect relative to a specification. We adopt it also for purity.

Relative purity guarantees soundness of reasoning and does not impose unnecessary constraints on executable assertions. An executable assertion is free to perform any side-effect as long as nothing happened from the perspective of the separation logic specification. Runtime assertion checking does nothing relative to the specification of a verified program. An executable assertion can even print 'Hello World!' if the specification permits it. Purity is in the eye of the asserter.

# 4.6.2 Predicate extraction

Programmers are conventionally encouraged to specify sets of states with computations. Hoare-style verification cannot do much with computations – it needs predicates. So conventional verification approaches, including the Spec# system [3] and JML toolset [9], have to extract predicates from computations. This can be achieved in various ways: as long as there is an agreed-upon mapping from computations to predicates, the computations can be seen as syntactic sugar for predicates.

When given an executable assertion B without old-expressions<sup>10</sup>, one possibility is to extract a predicate P which follows from B. This is described by the connecting implication  $B \Rightarrow P$ , which informally means that B evaluating to True is sufficient to conclude P. Here is a formal definition:

 $B \Rightarrow P \stackrel{\text{def}}{=} \forall Q \cdot \text{ if } Q \Rightarrow B \text{ then } Q \Rightarrow P.$ 

If  $B \Rightarrow P$ , then  $P \Rightarrow B$  does not necessarily hold. However, the best predicate that can be extracted in this way, i.e. the strongest P such that B  $\Rightarrow$  P, is precisely the weakest P such that  $P \Rightarrow B$ . So if we define

$$Best(B) \stackrel{\text{def}}{=} \bigvee \{ P \mid P \Rightarrow B \}$$

then the following lemmas hold:

<sup>&</sup>lt;sup>10</sup>The treatment can be generalised to executable assertions with old-expressions.

```
1. Best(B) \Rightarrow B and B \Rightarrow Best(B), i.e. Best(B) \Leftrightarrow B
2. B \Rightarrow P iff Best(B) \Rightarrow P
3. If P \Rightarrow B then P \Rightarrow Best(B)
```

Since  $Best(B) \Leftrightarrow B$ , the predicate Best(B) can be seen as the exact logical counterpart of B.

*Examples.* Let us ignore inheritance and suppose an implementation of class SLIST in Figure 4.1 maintains its stored elements in a list of linked nodes.

- 1. If  $is\_empty$  is implemented as a field in class SLIST, then  $Best(is\_empty) = (is\_empty \hookrightarrow True).$
- 2. If *is\_empty* tests whether a field named 'head', which points to the first node, is **Void**, then  $Best(not is_empty) = (\exists x \cdot head \hookrightarrow x * x \neq Void)$ .
- 3. If has is implemented by a simple linear traversal of the nodes, then the predicate Best(has(i)) denotes that i is encountered inside a node before the next node pointer becomes **Void**. Note that Best(has(i))is too weak to rule out a cyclic or frying-pan [8] list. Such a list does not even have to contain i in order to satisfy Best(has(i)), because the definition does not demand the termination of has(i).

As these examples show, the logical counterparts of executable assertions are often very weak. Predicate extraction techniques will have to map computations into stronger predicates in order to verify method bodies. Such predicates are similar in flavour to the ones used in this chapter, and do not follow from the executable ones. Section 4.8 discusses this in more depth.

Our approach uses predicates rather than computations to characterise states. It views the separation logic specification as the primary one for verification. There is no extraction of predicates, and verification treats executable assertions as computations which should evaluate to True. We do not verify the program *using* its executable assertions, we verify the program *and* its executable assertions.

# 4.7 Implementation

We extended MultiStar to provide fully automatic verification of executable Eiffel assertions with respect to separation logic specifications. The implementation leverages mechanisms which are present in MultiStar and most separation logic proof tools: symbolic execution, implication checking and frame inference. Symbolic execution and implication checking are the basic ingredients for proving connecting implications. For example, when proving  $P \Rightarrow B$ , MultiStar executes v := B in the symbolic state P to obtain a symbolic state P'. It then checks whether P'  $\Rightarrow$  (P \* v = True). For executable postconditions, the results of old-expressions are inferred automatically with frame inference and not specified manually. Suppose MultiStar must prove the connecting implication P,Q  $\Rightarrow$  C, where C contains a single old-expression **old**(E). Executing v<sub>1</sub> := E in the symbolic state P yields a resulting symbolic state P'. Frame inference next determines the symbolic result R of E in P'  $\vdash$ P \* R. Then w := C[v<sub>1</sub>] is executed in the symbolic state Q \* R and yields a symbolic state Q'. Finally, MultiStar checks whether Q'  $\Rightarrow$  (Q \* w = True).

MultiStar can easily handle the examples presented in this chapter, which are also available in the EVE download [28].

# 4.8 Conclusions and related work

The presented framework offers a sound and simple way to verify various executable specifications with separation logic. The notion of relative purity is central to the framework and embraces side-effects in executable assertions, thereby allowing programmers more freedom of expression compared to conventional verification approaches. The framework is well-suited to separation logic proof tools and is implemented in MultiStar.

Regarding related work, separation logic [54, 60] offers local reasoning for heap-manipulating programs and is central to the framework. Its adaptation to object-orientation [57, 56, 58, 65] is especially relevant for the presentation.

Executable specifications are embodied in programming languages such as Eiffel [26] and Spec# [3]. Dedicated specification languages such as JML [40] also use them. JML includes model classes and allows model-based contracts [12]. Another library with model classes is MML [63]. Executable assertions offer several benefits in software development, including runtime checking [26, 13] and automated testing [47, 15].

The problem of obtaining the strongest P such that  $B \Rightarrow P$  amounts to finding the weakest footprint of B preserved by B which ensures B evaluates to True. This problem appears to be similar in flavour but more general than abduction [10, 30].

Conventional approaches to verification, including the Spec# system [3] and JML toolset [9], extract predicates from assertions in a different way [45, 18]:

1. They do not use footprints. Well-formedness of the heap is exploited and any chain of references can be followed. Since a method can potentially modify any reachable object, the absence of footprints makes reasoning more global. Specifications must describe which objects are modified, because there might be external references to reachable objects. The *aliasing problem* arises because objects maintaining such references, or aliases, can depend on properties of the aliased objects. Approaches to the problem typically restrict or prevent aliases and/or operations on references. They include confinement, sharing and access control, ownership, immutability, uniqueness, information flow and escape analyses [16].

- 2. They impose purity constraints on the methods used in assertion expressions. Several notions of purity exist which prevent methods from changing the state [18, 53]. A strongly pure method has no side-effect at all. A weakly pure method does not change existing objects, but might allocate and modify new ones. An observationally pure method may modify existing objects, provided that the change is sufficiently encapsulated such that no other class can observe a change. Proving method purity becomes harder as the notion becomes more permissive. Weak purity can be proved with a combination of pointer and escape analysis [62], while a method's observational purity can be shown by proving that it simulates a weakly pure one from the perspective of other classes [53].
- 3. They encode pure methods and their contracts in first-order logic as uninterpreted functions and axioms respectively [18, 45, 17]. Checking well-formedness of pure method specifications is vital in this step, because inconsistent axiomatisations can result from unsatisfiable specifications or recursive specifications which are ill-founded [61]. A pure method call in an assertion is encoded as an application of the corresponding uninterpreted function. A loop, written in the stylised form of quantification over a finite domain (as in JML and Spec#), is directly encoded as a quantified formula. The reader is referred to [45] for assertion encoding details. Model class and model-based contract encoding is discussed in [19].

# CHAPTER 5

# CORRECTNESS BY CONSTRUCTION

The previous two chapters proposed extensions to the separation logic proof system: features for reasoning about related abstractions, multiple inheritance and executable contracts. Instead of adding new features, this chapter investigates correctness by construction – an alternative way to develop verified programs. Dijkstra, who advocated this approach, described it well in his Turing Award lecture [22]:

The only effective way to raise the confidence level of a program significantly is to give a convincing proof of its correctness. But one should not first make the program and then prove its correctness, because then the requirement of providing the proof would only increase the poor programmer's burden. On the contrary: the programmer should let correctness proof and program grow hand in hand.

Stepwise refinement [50, 2, 31] is an important technique in correctness by construction. (It is often all that is needed, but developing correct OO code will require more machinery.) The bulk of this chapter is consequently devoted to freefinement – a new algorithm that constructs a sound refinement calculus from a verification system under certain conditions.

In fact many theories in computer science are presented, or approximated, by compositional *verification systems*. A verification system in this chapter is any formal system for establishing whether an inductively defined term, typically a program, satisfies a specification. For example, Hoare logics and type systems can be viewed as verification systems. In the case of Hoare logics, the system proves that a statement satisfies certain specifications given as preconditions and postconditions. In the case of type systems, the system proves that a term has a certain type in a type context. Refinement systems play a similar role to verification systems, the main difference being that they relate terms to other terms, instead of terms and specifications. Another difference is that they typically include so-called specification terms. Intuitively, a term refines another if it is 'better', i.e. if it satisfies more specifications. *Refinement calculi* are formal systems for establishing refinements. For example, the calculus of Morgan [50] derives refinements between statements based on total correctness specifications. Starting from an appropriate specification statement, one can derive a correct algorithm for computing the factorial of a number by applying Morgan's refinement rules.

The work on freefinement started from the observation that a Hoare logic and a refinement calculus for a command language do not have to be independent entities: once the Hoare logic is extended with specification statements, the two systems can be accommodated in a single theory. Moreover, there is a strong relation between the two systems. This chapter explains that this relation is not a coincidence: it is possible to analyse the structure of the inference rules defining a verification system, and automatically generate a related refinement calculus. Freefinement is an algorithm that implements this transformation. Surprisingly, freefinement is not limited to Hoare logics, but can be applied to any verification system whose inference rules satisfy certain conditions. Several refinement rules proposed in the literature in different contexts arise in this way.

The freefinement algorithm works as follows. Given a term language and an accompanying verification system  $V_1$  that satisfies certain conditions, freefinement extends the term language with specification terms and builds a verification system  $V_2$  for extended terms. The conditions on  $V_1$  ensure that it is possible to extend the terms without breaking the inference rules;  $V_2$  is consequently a sound and conservative extension of  $V_1$ . Moreover, freefinement proposes a sound refinement system R that is in harmony with  $V_2$ . Harmony means that the two formal systems can interoperate smoothly. It entails, for example, that a term satisfies a specification according to  $V_2$  if and only if it is possible to refine the specification into the term with R. In fact, proof translation between  $V_2$  and R becomes possible because harmony is demonstrated constructively. Freefinement internally constructs the refinement calculus by 'linearising'  $V_2$  in a series of steps. The conditions on  $V_1$  ensure that successful linearisation is possible. According to the presentation below, at most six steps are needed for this 'refinement of refinement systems'. The situation is summarised as follows:



Freefinement requires no human intervention. The conditions it imposes are fulfilled by many program logics and type systems: examples include Hoare logic, separation logic, the simply-typed lambda calculus and System F. Freefinement defines the semantics of refinement at an abstract level: it associates each term of the extended language with a set of terms from the original language, and refinement simply reduces this set.

With freefinement, tools that are based on verification systems can readily include refinement as a complementary or alternative development style. In cases where the verification system is part of a larger system for establishing program correctness, it may well be possible to use refinement in a more elaborate calculus for constructing correct programs. The second part of this chapter shows how to use the output of freefinement in a calculus for developing correct OO code. This calculus is based on the separation logic proof system of Chapter 2, and maintains close relationships with it.

**Chapter outline** Section 5.1 describes the freefinement algorithm, which is applied in Section 5.2 to a simple type system for the lambda calculus and also to Hoare logic. Section 5.3 shows how the output of freefinement can be used in a calculus for constructing correct OO programs. Section 5.4 concludes with related work.

# 5.1 Freefinement

# 5.1.1 The Inputs

Freefinement requires four things as input:

1. A set of constructors  $\mathbb{K}$ . The constructors give rise to a term language T, where an arbitrary term t of T is defined by the grammar:

$$\mathbf{t} ::= \mathbf{C}(\mathbf{t}_1, \ldots, \mathbf{t}_n)$$

where  $C \in \mathbb{K}$ .

2. A set of specifications  $\mathbb{S}$ .

- 3. A binary relation  $\models_{V_1} Sat$  between terms and specifications. Intuitively,  $\models_{V_1} t Sat S$  denotes that term  $t \in T$  satisfies specification  $S \in S$ .
- A formal system V<sub>1</sub>(K, S, ⊨<sub>V1-</sub> Sat \_), which consists of a set of inference rules for proving sentences of the form t Sat S. Each rule of V<sub>1</sub> must have the form A<sub>1</sub> or B<sub>1</sub>:

$$A_{1} \frac{t_{1} Sat S_{1} \dots t_{n} Sat S_{n}}{C(t_{1}, \dots, t_{n}) Sat S}$$
provided Pred(C, S<sub>1</sub>, ..., S<sub>n</sub>, S).
$$B_{1} \frac{t Sat S_{1} \dots t Sat S_{m}}{t Sat S}$$
provided Pred(S<sub>1</sub>, ..., S<sub>m</sub>, S).

The t's, S's and C in the rule forms indicate where the rules of  $V_1$  must use metavariables. Thus a rule of form  $A_1$  has only the freedom to choose a concrete n and a definition for its proviso predicate Pred; the proviso predicate implements the side condition of the rule based on the arguments C,  $S_1, \ldots, S_n$  and S. A rule of form  $B_1$  is also a pair: a concrete m and a definition of a predicate with arguments  $S_1, \ldots, S_m$ and S. Freefinement requires that the rules must be sound with respect to the following semantics:

**Definition 5.1** (Semantics of the Inference Rules).

- 5.1.1. For rules of the form A<sub>1</sub>:  $Pred(C, S_1, ..., S_n, S) \Rightarrow [\forall t_1, ..., t_n \in T \cdot \models_{V_1} t_1 \text{ Sat } S_1 \land \dots \land \models_{V_1} t_n \text{ Sat } S_n \Rightarrow \models_{V_1} C(t_1, ..., t_n) \text{ Sat } S]$
- 5.1.2. For rules of the form  $B_1$ :  $Pred(S_1, ..., S_m, S) \Rightarrow$  $[\forall t \in T \cdot \models_{V_1} t \text{ Sat } S_1 \land ... \land \models_{V_1} t \text{ Sat } S_m \Rightarrow \models_{V_1} t \text{ Sat } S]$

The rule forms stipulate that the rules of  $V_1$  must be highly compositional – a requirement that freefinement will exploit. For example, rules cannot inspect or constrain the t's that appear in premises. This will allow freefinement to reuse the rules after specification terms are added to the term language.

Consider the following three rules over  $\mathbb{K} = \{0, \text{ succ, pred}\}\$  and  $\mathbb{S} = \{\mathbb{N}\}\$ , where n is a metavariable:

$$1 \frac{\mathbf{n} : \mathbb{N}}{\operatorname{succ}(\mathbf{n}) : \mathbb{N}} \qquad 2 \frac{\operatorname{succ}(\mathbf{n}) : \mathbb{N}}{\operatorname{pred}(\operatorname{succ}(\mathbf{n})) : \mathbb{N}} \qquad \begin{array}{c} 3 \frac{\mathbf{n} : \mathbb{N}}{\operatorname{pred}(\mathbf{n}) : \mathbb{N}} \\ \text{provided positive}(\mathbf{n}). \end{array}$$

Rule 1 can be written in form  $A_1$  with n = 1 by defining the proviso  $Pred(C, S_1, S)$  as  $C = succ \land S_1 = S = \mathbb{N}$ . Rule 2 is unacceptable, because its premise inspects the term and requires it to match succ(n). Rule 3 is also unacceptable, because it constrains the term in its proviso.

It will become clear later that the 'structural' rules of Hoare logic, such as the rule of consequence, are examples of rules of form  $B_1$ . Other rules of Hoare logic, such as the assignment axiom and rule for sequential composition, have the form  $A_1$ .

Let  $\vdash_{V_1} t$  Sat S denote that t Sat S is derivable with  $V_1$ . The soundness of the rules with respect to the semantics of Definition 5.1 implies the soundness of  $V_1$ :

**Theorem 5.1** (Soundness of  $V_1$ ).  $\vdash_{V_1} t$  Sat  $S \Rightarrow \models_{V_1} t$  Sat S

*Proof.* By induction on the derivation of t Sat S:

- A rule of the form  $A_1$  was last applied. Assume  $Pred(C, S_1, \ldots, S_n, S)$ and the induction hypothesis  $\models_{V_1} t_1 Sat S_1 \land \ldots \land \models_{V_1} t_n Sat S_n$ . Then  $\models_{V_1} C(t_1, \ldots, t_n) Sat S$  by Definition 5.1.1.
- A rule of the form  $B_1$  was last applied. Assume  $Pred(S_1, \ldots, S_m, S)$  and also the induction hypothesis  $\models_{V_1} t Sat S_1 \land \ldots \land \models_{V_1} t Sat S_m$ . From Definition 5.1.2 follows  $\models_{V_1} t Sat S$ .

Freefinement does not assume the completeness of  $V_1$ , i.e. it never assumes  $\models_{V_1} t Sat S \Rightarrow \vdash_{V_1} t Sat S$ .

## 5.1.2 The Extended Language and Formal System

This section extends the language T with specification terms that are useful for refinement. It gives a semantics to the resulting language U, and extends  $V_1$  in a sound and conservative way to prove sentences of the form u *Sat* S where  $u \in U$ .

#### The Extended Language U

Suppose  $\mathbb{K}$  and  $\mathbb{S}$  are disjoint (if they are not, then they can always be decorated to become disjoint) and do not contain a symbol  $\square$ . The extended set of constructors

$$\mathbb{K}' = \mathbb{K} \cup \mathbb{S} \cup \{ \bigsqcup \text{ with arity } n \mid n \in \mathbb{N} \}$$

gives rise to an extended language U, which can also be written as:

$$\mathbf{u} ::= \mathbf{C}(\mathbf{u}_1, \ldots, \mathbf{u}_n) \mid \mathbf{S} \mid \bigsqcup(\mathbf{u}_1, \ldots, \mathbf{u}_n)$$

A term of the form S is called a *spec* term, and a term of the form  $\bigsqcup(u_1, \ldots, u_n)$  is called the *join* of  $u_1, \ldots, u_n$ . Intuitively, S is a generic term that satisfies S, and  $\bigsqcup(u_1, \ldots, u_n)$  is a generic term that satisfies any S that any of the  $u_1, \ldots, u_n$  satisfy. Although the details will become clear later, the reasons for adding these terms are simple: the refinement system should be able to refine spec terms into other terms for top-down development, and join terms will be important for simplifying rules of the form  $B_1$  where m > 1. If there are no rules of the form  $B_1$  where m > 1, then join terms and their consequent treatment can be omitted.

A couple of constructs are used for giving a semantics to U. Let X denote a subset of T, and let Y denote a subset of S. Specs(X) is the set of all specifications that all the terms in X satisfy, and Terms(Y) is the set of terms of T that satisfy all the specifications in Y:

**Definition 5.2** (Specs and Terms).

- Specs(X)  $\stackrel{\text{def}}{=} \{ S \mid \forall t \in X \cdot \models_{V_1} t \text{ Sat } S \}$
- Terms(Y)  $\stackrel{\text{def}}{=} \{ t \mid \forall S \in Y \cdot \models_{V_1} t \text{ Sat } S \}$

An antitone Galois connection<sup>1</sup> exists between Specs and Terms:

Lemma 5.2.  $X \subseteq Terms(Y) \Leftrightarrow Y \subseteq Specs(X)$ 

Proof.	$X \subseteq Terms(Y)$	
<	$\Rightarrow \{ \text{definition of Terms and } \subseteq \}$	
	$\forall t \in X \cdot \forall S \in Y \cdot \models_{V_1} t \text{ Sat } S$	
<	$\Rightarrow {\text{predicate calculus}}$	
	$\forall S \in Y \cdot \forall t \in X \cdot \models_{V_1} t \text{ Sat } S$	
<	$\Rightarrow \{ \text{definition of Specs and } \subseteq \} $	
	$\mathbf{Y} \subseteq \operatorname{Specs}(\mathbf{X})$	

Antitone Galois connections have several well-known properties. For instance, (Terms  $\circ$  Specs) and (Specs  $\circ$  Terms) are extensive, increasing and idempotent and therefore closure operators. Freefinement relies on the following properties (their proofs appear in Appendix B):

#### Corollary 5.3.

5.3.1.  $X \subseteq Terms(Specs(X))$ 

<sup>&</sup>lt;sup>1</sup>Also known as an *order-reversing* or *contravariant* Galois connection.

5.3.2. Terms(Specs(Terms(Y))) = Terms(Y)

5.3.3.  $\operatorname{Specs}(X) \subseteq \operatorname{Specs}(X') \Leftrightarrow \operatorname{Terms}(\operatorname{Specs}(X)) \supseteq \operatorname{Terms}(\operatorname{Specs}(X'))$ 

5.3.4. Terms $(Y \cup Y') = Terms(Y) \cap Terms(Y')$ 

The following auxiliary definition provides a shorthand for the set of all terms of the form  $C(t_1, \ldots, t_n)$  where  $t_1 \in X_1, \ldots, t_n \in X_n$ :

 $C(X_1, \ldots, X_n) \stackrel{\text{def}}{=} \{C(t_1, \ldots, t_n) \mid \bigwedge_{i \in 1..n} t_i \in X_i\}$ 

For example, it yields a singleton set for nullary constructors:

$$\{ C() \mid \bigwedge_{i \in 1..0} t_i \in X_i \} = \{ C() \mid True \} = \{ C() \}$$

The semantics of U is given by the function  $\llbracket\_\rrbracket$  of type U  $\rightarrow \mathcal{P}(T)$ , i.e. every term in U denotes a set of terms from T:

**Definition 5.3** (Semantics of U).

$$\begin{bmatrix} \mathbf{C}(\mathbf{u}_1, \dots, \mathbf{u}_n) \end{bmatrix} \stackrel{\text{def}}{=} \operatorname{Terms}(\operatorname{Specs}(\mathbf{C}(\llbracket \mathbf{u}_1 \rrbracket, \dots, \llbracket \mathbf{u}_n \rrbracket))) \\ \begin{bmatrix} \mathbf{S} \rrbracket \stackrel{\text{def}}{=} \operatorname{Terms}(\{\mathbf{S}\}) \\ \begin{bmatrix} \bigsqcup(\mathbf{u}_1, \dots, \mathbf{u}_n) \rrbracket \stackrel{\text{def}}{=} \bigcap_{i \in 1..n} \llbracket \mathbf{u}_i \rrbracket$$

If the relation  $\models_{V_1}$  Sat \_ is well-behaved in a sense that will be made precise later, then  $\llbracket u \rrbracket$  has a simple intuitive explanation: it denotes the set of all primitive terms, i.e. terms from T, that refine u. For a term  $C(u_1, \ldots, u_n)$ , first consider  $C(\llbracket u_1 \rrbracket, \ldots, \llbracket u_n \rrbracket)$  – the set of terms of the form  $C(t_1, \ldots, t_n)$  where  $t_1 \in \llbracket u_1 \rrbracket$  (i.e.  $t_1$  refines  $u_1$ ) and  $\ldots$  and  $t_n \in \llbracket u_n \rrbracket$ . All the specifications that all these terms implement are then collected, and any primitive terms that refine S are exactly those that satisfy S. Finally,  $\bigsqcup(u_1, \ldots, u_n)$  is refined by any primitive term that refines all  $u_1, \ldots, u_n$ .

For all u, the set  $\llbracket u \rrbracket$  is a fixpoint of Terms  $\circ$  Specs and hence a closed element:

Lemma 5.4.  $\operatorname{Terms}(\operatorname{Specs}(\llbracket u \rrbracket)) = \llbracket u \rrbracket$ 

*Proof.* By induction on the structure of u:

• If u has the form  $C(u_1, ..., u_n)$  or S, then  $\llbracket u \rrbracket = Terms(Y)$  for some Y and the result follows by Corollary 5.3.2.

• If u has the form  $\bigsqcup(u_1, \ldots, u_n)$ , assume  $\llbracket u_i \rrbracket = \text{Terms}(\text{Specs}(\llbracket u_i \rrbracket))$ for all  $i \in 1..n$ . So  $\llbracket \bigsqcup(u_1, \ldots, u_n) \rrbracket = \bigcap_{i \in 1..n} \text{Terms}(\text{Specs}(\llbracket u_i \rrbracket)) = \text{Terms}(\bigcup_{i \in 1..n} \text{Specs}(\llbracket u_i \rrbracket))$  by Corollary 5.3.4, and Corollary 5.3.2 concludes the proof.

The rest of the chapter introduces further properties of the semantics as needed.

#### Extending $V_1$ : Preliminaries

The next section will extend  $V_1$  to obtain a formal system  $V_2$  for proving sentences of the form u *Sat* S. The aim is to construct a sound and conservative extension of  $V_1$ . Informally, a sound extension of  $V_1$  must have equal or more power:

**Definition 5.4** (Sound Extension).  $V_2(\mathbb{K}', \mathbb{S}', \models_{V_2} Sat_{-})$  is a sound extension of  $V_1(\mathbb{K}, \mathbb{S}, \models_{V_1} Sat_{-})$  if and only if

- 1.  $V_2$  uses richer terms and specifications:  $\mathbb{K} \subseteq \mathbb{K}'$  and  $\mathbb{S} \subseteq \mathbb{S}'$
- 2.  $V_2$  can prove everything that  $V_1$  can prove:  $\forall t \in T, S \in S \cdot \vdash_{V_1} t Sat S \Rightarrow \vdash_{V_2} t Sat S$
- 3.  $V_2$  uses a richer semantics:  $\forall t \in T, S \in S \cdot \models_{V_2} t Sat S \Rightarrow \models_{V_1} t Sat S$
- 4.  $V_2$  is sound:  $\forall u \in U, S' \in S' \cdot \vdash_{V_2} u \text{ Sat } S' \Rightarrow \models_{V_2} u \text{ Sat } S'$

As a consequence,  $\forall t \in T$ ,  $S \in S \cdot \vdash_{V_2} t Sat S \Rightarrow \models_{V_1} t Sat S$ , which intuitively means that  $V_2$  restricted to  $\mathbb{K}$  and S is sound with respect to the semantics of  $V_1$ .

In a sound and conservative extension, the converse of requirement 2 also holds:

**Definition 5.5** (Sound and Conservative Extension). A formal system  $V_2(\mathbb{K}', \mathbb{S}', \models_{V_2-} Sat_{-})$  is a sound and conservative extension of  $V_1(\mathbb{K}, \mathbb{S}, \models_{V_1-} Sat_{-})$  if and only if

- 1.  $V_2$  is a sound extension of  $V_1$ .
- 2.  $V_1$  and  $V_2$  restricted to  $\mathbb{K}$  and  $\mathbb{S}$  have equal derivability:  $\forall t \in T, S \in \mathbb{S} \cdot \vdash_{V_1} t Sat S \Leftrightarrow \vdash_{V_2} t Sat S$

Although a sound and conservative extension cannot prove more sentences of the form t *Sat* S, it is still useful for extending the term language and installing a richer semantics. It can also extend the specifications, but the  $V_2$  of the next section will simply use S.

## The Extended Formal System $V_2$

The construction of  $V_2$  starts with the empty set of rules and proceeds in two steps:

1. For each rule of  $V_1$ , replace t's by u's and add the resulting rule. This change of metavariables yields the rule forms  $A_2$  and  $B_2$  in  $V_2$ :

$$A_{2} \frac{u_{1} Sat S_{1} \dots u_{n} Sat S_{n}}{C(u_{1}, \dots, u_{n}) Sat S}$$
provided Pred(C, S<sub>1</sub>, ..., S<sub>n</sub>, S).
$$B_{2} \frac{u Sat S_{1} \dots u Sat S_{m}}{u Sat S}$$
provided Pred(S<sub>1</sub>, ..., S<sub>m</sub>, S).

2. Add the following rules for spec terms and joins:

$$\frac{\text{SPEC} \ \overline{\text{S Sat S}}}{\text{JOIN} \ \underline{\text{u Sat S}}}$$

By induction on the derivation,  $V_1$  and  $V_2$  are equivalent with respect to derivability on T, i.e.  $\vdash_{V_1} t$  Sat S  $\Leftrightarrow \vdash_{V_2} t$  Sat S. So for  $V_2$  to be a sound and conservative extension of  $V_1$ , it will suffice to equip  $V_2$  with a richer semantics and to prove it sound.

The *Sat* relation between U and S is defined as follows:

**Definition 5.6** (Extended Satisfaction).  $\models_{V_2} u \text{ Sat } S \stackrel{\text{def}}{=} \forall t \in \llbracket u \rrbracket \cdot \models_{V_1} t \text{ Sat } S$ 

Furthermore, the U-semantics of t contains t as an element:

Lemma 5.5 (Term Embedding).  $\forall t \in T \cdot t \in [t]$ 

*Proof.* By induction on the structure of t. Suppose  $t = C(t_1, \ldots, t_n)$  and assume  $t_1 \in [t_1], \ldots, t_n \in [t_n]$ . So  $t \in C([t_1], \ldots, [t_n])$ , which is a subset of Terms(Specs( $C([t_1], \ldots, [t_n])$ )) by Corollary 5.3.1.

Therefore  $\models_{V_2} t \text{ Sat } S \Rightarrow \models_{V_1} t \text{ Sat } S$  holds, and the soundness proof of  $V_2$  establishes that  $V_2$  is a sound and conservative extension of  $V_1$ :

**Theorem 5.6** (Soundness of  $V_2$ ).  $\vdash_{V_2} u Sat S \Rightarrow \models_{V_2} u Sat S$ 

*Proof.* By induction on the structure of the derivation:

• For each rule of the form  $A_2$ , assume  $Pred(C, S_1, \ldots, S_n, S)$  and assume

$$\forall \mathbf{t}_1 \in \llbracket \mathbf{u}_1 \rrbracket \cdot \models_{\mathsf{V}_1} \mathbf{t}_1 \ Sat \ \mathbf{S}_1$$
$$\vdots$$
$$\forall \mathbf{t}_n \in \llbracket \mathbf{u}_n \rrbracket \cdot \models_{\mathsf{V}_1} \mathbf{t}_n \ Sat \ \mathbf{S}_n$$

So  $\forall t_1 \in \llbracket u_1 \rrbracket$ , ...,  $t_n \in \llbracket u_n \rrbracket \cdot \models_{V_1} C(t_1, \ldots, t_n)$  Sat S because the corresponding rule of the form  $A_1$  in  $V_1$  is sound with respect to Definition 5.1.1. So  $S \in \text{Specs}(C(\llbracket u_1 \rrbracket, \ldots, \llbracket u_n \rrbracket))$  and hence  $\forall t \in \text{Terms}(\text{Specs}(C(\llbracket u_1 \rrbracket, \ldots, \llbracket u_n \rrbracket))) \cdot \models_{V_1} t \text{ Sat } S.$ 

• For each rule of the form  $B_2$ , assume  $Pred(S_1, \ldots, S_m, S)$  and assume  $\forall t \in \llbracket u \rrbracket \cdot \models_{V_1} t Sat S_1 \land \ldots \land \models_{V_1} t Sat S_m$ .

Now  $\forall t \in \llbracket u \rrbracket \cdot \models_{V_1} t \text{ Sat S}$  because the corresponding rule of the form  $B_1$  in  $V_1$  is sound with respect to Definition 5.1.2.

- SPEC:  $\forall t \in Terms(\{S\}) \cdot \models_{V_1} t Sat S$  by definition.
- JOIN: Assume  $\forall t \in \llbracket u \rrbracket \cdot \models_{V_1} t \text{ Sat S. If } t \in \llbracket \bigsqcup(\ldots, u, \ldots) \rrbracket$  then  $t \in \llbracket u \rrbracket$  and hence  $\models_{V_1} t \text{ Sat S.}$

Extended satisfaction has an alternative characterisation that freefinement will also use:

Lemma 5.7.  $\models_{V_2} u \text{ Sat } S \Leftrightarrow S \in \text{Specs}(\llbracket u \rrbracket)$ 

# 5.1.3 System $V_2$ and Refinement

The next section will construct several refinement systems, or calculi, that are based on  $V_2$ . These refinement systems are formal systems for proving sentences of the form  $u \sqsubseteq u'$ . The definition of the refinement relation makes the semantics of refinement precise:

**Definition 5.7** (Refinement).  $\models \mathbf{u} \sqsubseteq \mathbf{u}' \stackrel{\text{def}}{=} \llbracket \mathbf{u} \rrbracket \supseteq \llbracket \mathbf{u}' \rrbracket$ 

This definition leads to simple proofs, and is equivalent to several other formulations. The following theorem states one such alternative, and its proof mentions others:

**Lemma 5.8** (Equivalent Characterisation of Refinement).  $\models u \sqsubseteq u' \Leftrightarrow \forall S \cdot \models_{V_2} u \text{ Sat } S \Rightarrow \models_{V_2} u' \text{ Sat } S$ 

If  $\models_{V_1-} Sat_-$  is well-behaved, then there is also another explanation for defining  $\models_{U} \sqsubseteq u'$  as  $\llbracket u \rrbracket \supseteq \llbracket u' \rrbracket$ : u' refines u iff every primitive term that refines u' also refines u. Put differently, u' refines u iff u' constrains the set of eventual primitive terms that refinement can produce to the same or higher degree compared to u. So u can be seen as a placeholder for any of the primitive terms in  $\llbracket u \rrbracket$ , and the role of refinement is to reduce the uncertainty.

Many examples of refinements will follow later, so here is a small one: a join term implements the least upper bound (join) of its immediate subterms with respect to  $\sqsubseteq$ , hence the name. In particular:

- 1.  $\forall i \in 1..n \cdot \models \mathbf{u}_i \sqsubseteq \bigsqcup(\mathbf{u}_1, \ldots, \mathbf{u}_n)$
- 2. If  $(\forall i \in 1..n \cdot \models u_i \sqsubseteq u)$ , then  $\models \bigsqcup (u_1, \ldots, u_n) \sqsubseteq u$ .

The notation  $u \equiv u'$  is a shorthand for  $\llbracket u \rrbracket = \llbracket u' \rrbracket$ , which is equivalent to  $\models u \sqsubseteq u' \land \models u' \sqsubseteq u$ .

A refinement system R will be sound if and only if  $\vdash_{\mathsf{R}} u \sqsubseteq u'$  implies  $\models u \sqsubseteq u'$ . In the next section, freefinement will construct several sound refinement systems where each system R is related to  $\mathsf{V}_2$  by the properties Harmony 1 and 2 below.

**Harmony 1.** If  $\vdash_{V_2} u$  Sat S and  $\vdash_{\mathsf{R}} u \sqsubseteq u'$ , then  $\vdash_{V_2} u'$  Sat S.

Intuitively, Harmony 1 says that  $V_2$  contains sufficient machinery to prove the same properties about u' that it could prove about u. In other words, R is not too powerful for  $V_2$ .

**Harmony 2.** If  $\vdash_{V_2} u$  Sat S, then  $\vdash_{\mathsf{R}} S \sqsubseteq u$ .

Intuitively, Harmony 2 means that the refinement system R contains sufficient machinery to refine a specification into any term that satisfies it according to  $V_2$ . In other words,  $V_2$  is embedded in R and hence R is not too weak.

Harmony 1 is stronger than the converse of Harmony 2:

**Theorem 5.9.** If  $V_2$  and a refinement system R are related by Harmony 1, then  $\vdash_{\mathsf{R}} S \sqsubseteq u \implies \vdash_{V_2} u \text{ Sat } S$ .

*Proof.* Assume  $\vdash_{\mathsf{R}} S \sqsubseteq u$ . Since  $\vdash_{\mathsf{V}_2} S$  Sat S by SPEC, it follows from Harmony 1 that  $\vdash_{\mathsf{V}_2} u$  Sat S.

A refinement system R is called *harmonic* iff it satisfies Harmony 1 and 2. Harmonic refinement systems interoperate nicely with  $V_2$ . In fact, the proofs of Harmony 1 and 2 in the next section are constructive in the sense that they enable proof translation. Given a  $V_2$ -proof of u Sat S and an R-proof of  $u \sqsubseteq u'$ , they describe a  $V_2$ -proof of u' Sat S. Based on a  $V_2$ -proof of u Sat S, they show how to build an R-proof for S  $\sqsubseteq$  u. Since Harmony 1 is established constructively, given an R-proof of S  $\sqsubseteq$  u, the proof of Theorem 5.9 shows how to build a  $V_2$ -proof for u Sat S.

The final refinement system that freefinement produces will also have a specific desired form. This form guarantees that refinement proofs are 'linear' developments where terms can be refined in-place. Formally, a refinement system has the desired form if the rules with premises describe either the transitivity or the monotonicity of refinement. All the other rules must be axioms, i.e. without any premise.

# 5.1.4 The Refinement of Refinement Systems

 $V_2$  can be linearised in a series of steps to obtain a sound and harmonic refinement system of the desired form. At most six steps are necessary according to this presentation – the exact number depends on  $V_1$ . The steps make it easy to prove and maintain soundness and harmony, which would otherwise be more complex to establish for the final refinement calculus. Many of the steps take a previously constructed refinement system and add or remove rules to obtain a new system. If a sound and harmonic refinement system is extended with a rule that is sound and respects Harmony 1, then the resulting system will be sound and harmonic. There is no need to prove Harmony 2 again, because the new refinement system can still derive all sentences that the old one could derive. If a rule is removed from a sound and harmonic refinement system, then the resulting system remains sound and will also be harmonic if it satisfies Harmony 2. A simple way of showing that Harmony 2 still holds is to show that any application of the old rule can be achieved by a combination of rules that remain in the system.

#### Getting Started: $R_1$

The first refinement system  $R_1$  is obtained from  $V_2$  by a simple syntactic transformation: each sentence u *Sat* S becomes  $S \sqsubseteq u$ .  $R_1$  has rules of the form  $A_3$  and  $B_3$ , a SPEC rule and also a JOIN rule if join terms were needed:

$$A_{3} \frac{S_{1} \sqsubseteq u_{1} \dots S_{n} \sqsubseteq u_{n}}{S \sqsubseteq C(u_{1}, \dots, u_{n})}$$
provided Pred(C, S<sub>1</sub>, ..., S<sub>n</sub>, S).  

$$B_{3} \frac{S_{1} \sqsubseteq u \dots S_{m} \sqsubseteq u}{S \sqsubseteq u}$$
provided Pred(S<sub>1</sub>, ..., S<sub>m</sub>, S).  
SPEC  $\overline{S \sqsubseteq S}$   
JOIN  $\frac{S \sqsubseteq u}{S \sqsubseteq (\dots, u, \dots)}$ 

 $V_2$  and  $R_1$  are isomorphic: a proof of u Sat S in  $V_2$  corresponds to a proof of  $S \sqsubseteq u$  in  $R_1$  and vice versa, so  $\vdash_{V_2} u$  Sat S  $\Leftrightarrow \vdash_{R_1} S \sqsubseteq u$ . The soundness proof of  $R_1$  relies on the following equivalence:

# Lemma 5.10. $\models_{V_2} u Sat S \Leftrightarrow \models S \sqsubseteq u$

**Theorem 5.11** (Soundness of  $R_1$ ).  $\vdash_{R_1} u \sqsubseteq u' \Rightarrow \models u \sqsubseteq u'$ 

*Proof.* If  $\vdash_{\mathsf{R}_1} \mathbf{u} \sqsubseteq \mathbf{u}'$ , then  $\mathbf{u}$  has the form S and  $\vdash_{\mathsf{V}_2} \mathbf{u}'$  Sat S. The soundness of  $\mathsf{V}_2$  implies  $\models_{\mathsf{V}_2} \mathbf{u}'$  Sat S, and Lemma 5.10 in turn implies  $\models_{\mathsf{S}} \sqsubseteq \mathbf{u}'$ .  $\Box$ 

**Theorem 5.12.**  $R_1$  is harmonic.

*Proof.* Harmony 2 holds by construction. For Harmony 1, assume  $\vdash_{\mathsf{R}_1} \mathbf{u} \sqsubseteq \mathbf{u}'$ . Then  $\mathbf{u}$  has the form  $\mathbf{S}''$  and  $\vdash_{\mathsf{V}_2} \mathbf{u}'$  Sat  $\mathbf{S}''$  by construction. That  $\vdash_{\mathsf{V}_2} \mathbf{u}$  Sat  $\mathbf{S}'$  (i.e.  $\vdash_{\mathsf{V}_2} \mathbf{S}''$  Sat  $\mathbf{S}'$ ) implies  $\vdash_{\mathsf{V}_2} \mathbf{u}'$  Sat  $\mathbf{S}'$  for all  $\mathbf{S}'$  follows by induction on the derivation of  $\mathbf{S}''$  Sat  $\mathbf{S}'$ :

- SPEC: S' and S'' are the same. Since  $\vdash_{V_2} u'$  Sat S'', it holds that  $\vdash_{V_2} u'$  Sat S'.
- For each rule of the form  $B_2$ : S and S' are the same. Assume  $Pred(S_1, \ldots, S_m, S)$ ,  $\vdash_{V_2} u Sat S_1, \ldots, \vdash_{V_2} u Sat S_m$ , and by the induction hypothesis also  $\vdash_{V_2} u' Sat S_1, \ldots, \vdash_{V_2} u' Sat S_m$ . So the rule being considered is applicable and  $\vdash_{V_2} u' Sat S$ . Hence  $\vdash_{V_2} u' Sat S'$ .

Note: if  $V_2$  has only rules of the form  $A_2$  where n = 0 and/or rules of the form  $B_2$  where m = 0, then  $R_1$  is a refinement system of the desired form and freefinement stops.

#### Adding Transitivity: R<sub>2</sub>

The refinement system  $R_2$  extends  $R_1$  with the rule TRANS which states that refinement is transitive:

$$TRANS \frac{u_1 \sqsubseteq u_2 \qquad u_2 \sqsubseteq u_3}{u_1 \sqsubseteq u_3}$$

TRANS is sound because  $\supseteq$  is transitive, and it maintains Harmony 1 since implication is transitive. So  $R_2$  is sound and harmonic.

#### Simplification: R<sub>3</sub>

The presence of SPEC and TRANS in  $R_2$  allows the simplification of rules of the form  $B_3$  with m = 1:

$$B_3 \frac{S_1 \sqsubseteq u}{S \sqsubseteq u}$$
  
provided Pred(S<sub>1</sub>, S).

For an arbitrary rule of this form, consider the derivation

$$\frac{\operatorname{SPEC}}{\operatorname{B}_3} \frac{\overline{\operatorname{S}_1 \sqsubseteq \operatorname{S}_1}}{\operatorname{S} \sqsubseteq \operatorname{S}_1}$$
provided  $\operatorname{Pred}(\operatorname{S}_1, \operatorname{S})$ .

By virtue of having been derived, the new rule

$$\begin{array}{c} B_3 \\ \hline S \sqsubseteq S_1 \\ provided \ Pred(S_1, \ S). \end{array}$$

is sound and maintains Harmony 1, and can therefore be added to  $R_2$  to obtain a sound and harmonic refinement system. In fact, it can replace the old version without breaking Harmony 2, since removing the old version will not decrease the derivable set of sentences: every application of the old  $B_3$  can be changed into:

$$\frac{B_3}{T_{RANS}} \frac{\overline{S \sqsubseteq S_1}}{S \sqsubseteq u} \frac{S_1 \sqsubseteq u}{S \sqsubseteq u}$$

since  $Pred(S_1, S)$  is guaranteed.

The refinement system  $R_3$  is the same as  $R_2$ , except that the rules of the form  $B_3$  with m = 1 are replaced by their simplified versions.  $R_3$  is sound and harmonic.

Note: if  $V_2$  has only rules of the form  $A_2$  where n = 0 and rules of the form  $B_2$  where  $m \leq 1$ , then  $R_3$  is a refinement system of the desired form and freefinement stops.

## Adding Monotonicity: R<sub>4</sub>

All the constructors of U are monotone with respect to  $\sqsubseteq$ , i.e. the following rules are sound:

$$C-i \frac{u_i \sqsubseteq u'_i}{C(u_1, \dots, u_i, \dots, u_n) \sqsubseteq C(u_1, \dots, u'_i, \dots, u_n)}$$

$$J_{OIN}-i \frac{u_i \sqsubseteq u'_i}{\bigsqcup(u_1, \dots, u_i, \dots, u_n) \sqsubseteq \bigsqcup(u_1, \dots, u'_i, \dots, u_n)}$$

Moreover, these rules maintain harmony:

Lemma 5.13. C-*i* maintains Harmony 1.

*Proof.* Assume  $\forall S' \cdot \vdash_{V_2} u_i Sat S' \Rightarrow \vdash_{V_2} u'_i Sat S'$ . That  $\forall S \cdot \vdash_{V_2} C(u_1, \ldots, u_i, \ldots, u_n)$  Sat  $S \Rightarrow \vdash_{V_2} C(u_1, \ldots, u'_i, \ldots, u_n)$  Sat S follows by induction on the derivation of  $C(u_1, \ldots, u_i, \ldots, u_n)$  Sat S:

- A<sub>2</sub>: Suppose  $\vdash_{V_2} u_j$  Sat S<sub>j</sub> for  $j \in 1..n$ , and also suppose Pred(C, S<sub>1</sub>, ..., S<sub>n</sub>, S) holds. Since  $\vdash_{V_2} u'_i$  Sat S<sub>i</sub>, the same rule A<sub>2</sub> can be applied to derive C(u<sub>1</sub>, ..., u'<sub>i</sub>, ..., u<sub>n</sub>) Sat S.
- B<sub>2</sub>: Suppose ⊢<sub>V2</sub>C(u<sub>1</sub>, ..., u<sub>i</sub>, ..., u<sub>n</sub>) Sat S<sub>j</sub> for j ∈ 1..m, and suppose Pred(S<sub>1</sub>, ..., S<sub>m</sub>, S). The induction hypothesis is the assumption ⊢<sub>V2</sub>C(u<sub>1</sub>, ..., u<sub>i</sub>', ..., u<sub>n</sub>) Sat S<sub>j</sub> for j ∈ 1..m. Since Pred(S<sub>1</sub>, ..., S<sub>m</sub>, S), the same rule B<sub>2</sub> is applicable and hence ⊢<sub>V2</sub>C(u<sub>1</sub>, ..., u<sub>i</sub>', ..., u<sub>n</sub>) Sat S.

Lemma 5.14. JOIN-*i* maintains Harmony 1.

*Proof.* Assume  $\forall S' \cdot \vdash_{V_2} u_i Sat S' \Rightarrow \vdash_{V_2} u'_i Sat S'$ . That  $\forall S \cdot \vdash_{V_2} \bigsqcup (u_1, \ldots, u_i, \ldots, u_n)$  Sat  $S \Rightarrow \vdash_{V_2} \bigsqcup (u_1, \ldots, u'_i, \ldots, u_n)$  Sat S follows by induction on the derivation of  $\bigsqcup (u_1, \ldots, u_i, \ldots, u_n)$  Sat S:

- JOIN: Suppose  $u_j$  Sat S was the premise for some  $j \in 1..n$ . If  $j \neq i$ , then apply JOIN to the premise  $u_j$  Sat S to derive the required  $\bigsqcup (u_1, \ldots, u'_i, \ldots, u_n)$  Sat S. If j = i, then by assumption  $\vdash_{V_2} u'_i$  Sat S holds, and the result follows by JOIN.
- B<sub>2</sub>: Suppose ⊢<sub>V2</sub>□(u<sub>1</sub>, ..., u<sub>i</sub>, ..., u<sub>n</sub>) Sat S<sub>j</sub> for j ∈ 1..m, and suppose Pred(S<sub>1</sub>, ..., S<sub>m</sub>, S). The induction hypothesis is the assumption ⊢<sub>V2</sub>□(u<sub>1</sub>, ..., u<sub>i</sub>', ..., u<sub>n</sub>) Sat S<sub>j</sub> for j ∈ 1..m. Since Pred(S<sub>1</sub>, ..., S<sub>m</sub>, S), the same rule B<sub>2</sub> is applicable and hence ⊢<sub>V2</sub>□(u<sub>1</sub>, ..., u<sub>i</sub>', ..., u<sub>n</sub>) Sat S.

Let the notation v[u] denote a term in U whose parse tree is factored into two parts: a core tree v with a 'hole' where the sub-tree for u fits. The rule MONO packages C-*i* and JOIN-*i* in a single convenient form:

$$Mono \frac{\mathbf{u} \sqsubseteq \mathbf{u}'}{\mathbf{v}[\mathbf{u}] \sqsubseteq \mathbf{v}[\mathbf{u}']}$$

Informally, the rule MONO allows in-place refinement: if  $u_0$  can be factored as v[u], and u' refines u, then v[u'] refines  $u_0$ .

MONO is sound and maintains harmony because C-i and JOIN-i are sound and maintain harmony. The refinement system  $R_4$  extends  $R_3$  with MONO. It is sound and harmonic.

## Simplification: R<sub>5</sub>

The rule MONO makes it possible to simplify:

• The JOIN rule:

$$\operatorname{Join} \frac{S \sqsubseteq u}{S \sqsubseteq \bigsqcup(\dots, u, \dots)}$$

• Rules of the form  $A_3$  with  $n \ge 1$ :

$$A_3 \frac{S_1 \sqsubseteq u_1 \dots S_n \sqsubseteq u_n}{S \sqsubseteq C(u_1, \dots, u_n)}$$
  
provided Pred(C, S<sub>1</sub>, ..., S<sub>n</sub>, S).

Consider the derivation:

$$\operatorname{Join} \frac{\operatorname{Spec} \overline{S \sqsubseteq S}}{S \sqsubseteq \bigsqcup(\dots, S, \dots)}$$

By virtue of having been derived, the simplified rule

$$Join \overline{S \sqsubseteq \bigsqcup(\dots, S, \dots)}$$

is sound and respects Harmony 1. It can replace the old version of JOIN without decreasing derivability, because any application of the old version can be achieved by:

$$\frac{\text{Join}}{\text{Trans}} \frac{S \sqsubseteq u}{\underbrace{\square(\dots, S, \dots)}} \quad \frac{\text{Mono} \frac{S \sqsubseteq u}{\bigsqcup(\dots, S, \dots) \sqsubseteq \bigsqcup(\dots, u, \dots)}}{S \sqsubseteq \bigsqcup(\dots, u, \dots)}$$

Likewise, for each rule of the form  $A_3$ , the derived rule

$$A_{3} \overline{\mathbf{S} \sqsubseteq \mathbf{C}(\mathbf{S}_{1}, \dots, \mathbf{S}_{n})}$$
  
provided Pred(C, S<sub>1</sub>, ..., S<sub>n</sub>, S).

is sound and respects harmony. It makes the old version redundant, since any application of the old rule can be replaced by:



where  $E_i$  is given by:

$$\boxed{\frac{\mathbf{S} \sqsubseteq \mathbf{C}(\mathbf{u}_1, \dots, \mathbf{u}_{i-1}, \mathbf{S}_i, \dots, \mathbf{S}_n)}{\mathbf{S} \sqsubseteq \mathbf{C}(\mathbf{u}_1, \dots, \mathbf{u}_i, \mathbf{S}_{i+1}, \dots, \mathbf{S}_n)}} \mathbf{P}_i}$$

and  $\mathbf{P}_i$  is the proof tree:

Mono 
$$\frac{S_i \sqsubseteq u_i}{C(u_1, \ldots, u_{i-1}, S_i, \ldots, S_n) \sqsubseteq C(u_1, \ldots, u_i, S_{i+1}, \ldots, S_n)}$$

Apart from these simplifications, the refinement system  $R_5$  is the same as  $R_4$ . It is sound and harmonic.

Note: if  $V_2$  does not include rules of the form  $B_2$  where m > 1, then  $R_5$  has the desired form and freefinement stops.

## Wrapping Up: R<sub>6</sub>

It remains to simplify rules of the form  $B_3$  with m > 1:

$$B_3 \frac{S_1 \sqsubseteq u \dots S_m \sqsubseteq u}{S \sqsubseteq u}$$
  
provided  $Pred(S_1, \dots, S_m, S)$ .

If  $Pred(S_1, \ldots, S_m, S)$ , then  $R_5$  can derive:

$$\frac{\text{JOIN}}{\text{B}_3} \frac{\overline{\text{S}_1 \sqsubseteq \bigsqcup(\text{S}_1, \ldots, \text{S}_m)}}{\text{S} \sqsubseteq \bigsqcup(\text{S}_1, \ldots, \text{S}_m)} \cdots \frac{\text{JOIN}}{\text{S}_m \sqsubseteq \bigsqcup(\text{S}_1, \ldots, \text{S}_m)}$$

The derived rule

$$\begin{array}{c}
B_3 \\
\hline S \sqsubseteq \bigsqcup(S_1, \ldots, S_m) \\
\text{provided } \operatorname{Pred}(S_1, \ldots, S_m, S).
\end{array}$$

is therefore sound and respects Harmony 1. Together with the rule:

Unjoin 
$$\boxed{ \bigsqcup(u, \ldots, u) \sqsubseteq u }$$

which is trivially sound and respects Harmony 1, it can replace the old  $B_3$  because any application of the old rule can be rewritten as:



where G is UNJOIN,  $F_i$  is given by:

$$\boxed{\frac{\mathbf{S} \sqsubseteq \bigsqcup(\mathbf{\widetilde{u}, \ldots, \widetilde{u}}, \mathbf{S}_i, \ldots, \mathbf{S}_m)}{\mathbf{S} \sqsubseteq \bigsqcup(\mathbf{u}, \ldots, \mathbf{u}, \mathbf{S}_{i+1}, \ldots, \mathbf{S}_m)}} \mathbf{Q}_i}$$

and  $Q_i$  is the proof tree:

MONO 
$$\frac{S_i \sqsubseteq u}{\bigsqcup(u, \ldots, u, S_i, \ldots, S_m) \sqsubseteq \bigsqcup(\underbrace{u, \ldots, u}_{i}, S_{i+1}, \ldots, S_m)}$$

 $R_6$  is the same as  $R_5$ , except that it includes UNJOIN and replaces rules of the form  $B_3$  where m > 1 with their simplified versions.  $R_6$  is sound, harmonic and of the desired form.

## 5.1.5 Discussion

 $R_6$  can be made more powerful in several ways. For example, the following generalisation of JOIN is sound and preserves Harmony 1:

$$JOIN' u \sqsubseteq \bigsqcup(\dots, u, \dots)$$

The same holds for the reflexivity of refinement, which generalises SPEC, and other rules such as UNNEST:

UNNEST  $\overline{\bigsqcup(u_1, \ldots, u_n)} \sqsubseteq \bigsqcup(u_1, \ldots, u_{i-1}, u'_1, \ldots, u'_m, u_{i+1}, \ldots, u_n)$ provided  $1 \le i \le n$  and  $u_i = \bigsqcup(u'_1, \ldots, u'_m)$ .

In specific applications of freefinement, it might also be useful to add derived rules to  $R_6$ . Examples of this will follow later.

Freefinement assumes as little as possible about  $\models_{V_1}$ . Sat \_ and is consequently very generic. As one might expect, additional assumptions can help to construct more powerful refinement systems. For example, suppose 'plus' is a constructor that is commutative in the sense that

 $\forall t_1, t_2 \in T, S \in \mathbb{S} \cdot \models_{V_1} plus(t_1, t_2) Sat S \iff \models_{V_1} plus(t_2, t_1) Sat S$ 

Then  $\operatorname{Specs}(\operatorname{plus}(\llbracket u_1 \rrbracket, \llbracket u_2 \rrbracket)) = \operatorname{Specs}(\operatorname{plus}(\llbracket u_2 \rrbracket, \llbracket u_1 \rrbracket))$  because

$$\begin{array}{ll} S \in \operatorname{Specs}(\operatorname{plus}(\llbracket u_1 \rrbracket, \llbracket u_2 \rrbracket)) \\ \Leftrightarrow & \forall t_1 \in \llbracket u_1 \rrbracket, t_2 \in \llbracket u_2 \rrbracket \cdot \models_{\mathsf{V}_1} \operatorname{plus}(t_1, t_2) \; \textit{Sat} \; S \\ \Leftrightarrow & \forall t_1 \in \llbracket u_1 \rrbracket, t_2 \in \llbracket u_2 \rrbracket \cdot \models_{\mathsf{V}_1} \operatorname{plus}(t_2, t_1) \; \textit{Sat} \; S \\ \Leftrightarrow & S \in \operatorname{Specs}(\operatorname{plus}(\llbracket u_2 \rrbracket, \llbracket u_1 \rrbracket)) \end{array}$$

So  $[[plus(u_1, u_2)]] = [[plus(u_2, u_1)]]$  and therefore the refinement rule  $plus(u_1, u_2) \equiv plus(u_2, u_1)$  is sound. Depending on the rules of  $V_1$ , it might also preserve harmony.

As mentioned before, the semantic function  $\llbracket_{-}\rrbracket$  and the refinement order  $\sqsubseteq$  have nice interpretations when  $\models_{V_1-} Sat_{-}$  is well-behaved. Here is the definition:

**Definition 5.8** (Well-behavedness).  $\models_{V_1}$ - Sat \_ is well-behaved iff  $\forall C \in \mathbb{K}, t_1, \ldots, t_n \in T, S \in \mathbb{S} \cdot \models_{V_1} C(t_1, \ldots, t_n) Sat S \Rightarrow$  $\forall t \in C(Terms(Specs(\{t_1\})), \ldots, Terms(Specs(\{t_n\}))) \cdot \models_{V_1} t Sat S$ 

There is also an alternative characterisation of well-behavedness:

**Lemma 5.15.**  $\models_{V_1}$  Sat \_ is well-behaved iff  $\forall C \in \mathbb{K}, t_1, \ldots, t_n \in T \cdot Terms(Specs(\{C(t_1, \ldots, t_n)\})) =$  $Terms(Specs(C(Terms(Specs(\{t_1\})), \ldots, Terms(Specs(\{t_n\})))))$ *Proof.*  $t_i \in \text{Terms}(\text{Specs}(\{t_i\}))$  for  $i \in 1..n$  by Corollary 5.3.1, so  $\{C(t_1, \ldots, t_n)\} \subseteq C(Terms(Specs(\{t_1\})), \ldots, Terms(Specs(\{t_n\})))$ . Hence by Corollary B.1.3 in Appendix B,  $Specs({C(t_1, \ldots, t_n)}) \supseteq$  $Specs(C(Terms(Specs(\{t_1\})), \ldots, Terms(Specs(\{t_n\})))).$ Therefore:  $\models_{V_1} Sat_{-}$  is well-behaved  $\Leftrightarrow$  $\forall C \in \mathbb{K}, t_1, \ldots, t_n \in T, S \in \mathbb{S} \cdot \models_{V_1} C(t_1, \ldots, t_n) Sat S \Rightarrow$  $\forall t \in C(Terms(Specs(\{t_1\})), \ldots, Terms(Specs(\{t_n\}))) \cdot \models_{V_1} t Sat S$  $\Leftrightarrow$  $\forall C \in \mathbb{K}, t_1, \ldots, t_n \in T, S \in \mathbb{S} \cdot S \in Specs(\{C(t_1, \ldots, t_n)\}) \Rightarrow$  $S \in Specs(C(Terms(Specs(\{t_1\})), \ldots, Terms(Specs(\{t_n\}))))$  $\Leftrightarrow$  $\forall C \in \mathbb{K}, t_1, \ldots, t_n \in T \cdot Specs(\{C(t_1, \ldots, t_n)\}) \subseteq$  $Specs(C(Terms(Specs(\{t_1\})), \ldots, Terms(Specs(\{t_n\}))))$ {by the reasoning above}  $\Leftrightarrow$  $\forall C \in \mathbb{K}, t_1, \ldots, t_n \in T \cdot Specs(\{C(t_1, \ldots, t_n)\}) =$  $Specs(C(Terms(Specs(\{t_1\})), \ldots, Terms(Specs(\{t_n\}))))$ The result then follows by Corollary 5.3.3.
Freefinement does not require well-behavedness of  $\models_{V_1-} Sat_-$ , but the next theorem shows that the intuitions behind the definitions are simple when  $\models_{V_1-} Sat_-$  is well-behaved. For example, Theorem 5.16.3 says that  $\llbracket u \rrbracket$  is the set of all primitive terms that refine u.

**Theorem 5.16.** If  $\models_{V_1}$ - Sat \_ is well-behaved, then

5.16.1.  $\forall t \in T \cdot \llbracket t \rrbracket = \text{Terms}(\text{Specs}(\{t\}))$ 5.16.2.  $\forall t \in T, S \in S \cdot \models_{V_1} t \text{ Sat } S \Leftrightarrow \models_{V_2} t \text{ Sat } S$ 5.16.3.  $\forall t \in T, u \in U \cdot t \in \llbracket u \rrbracket \Leftrightarrow \models u \sqsubseteq t$ 

#### Proof.

5.16.1. By induction on the structure of t. Suppose 
$$t = C(t_1, ..., t_n)$$
  
and assume  $\llbracket t_i \rrbracket = \text{Terms}(\text{Specs}(\{t_i\}))$  for  $i \in 1..n$ . Then:  
 $\llbracket t \rrbracket = \text{Terms}(\text{Specs}(C(\llbracket t_1 \rrbracket, ..., \llbracket t_n \rrbracket)))$   
 $= \{\text{induction hypothesis}\}$   
 $\text{Terms}(\text{Specs}(C(\text{Terms}(\text{Specs}(\{t_1\})), ..., \text{Terms}(\text{Specs}(\{t_n\}))))))$   
 $= \{\text{Lemma 5.15}\}$   
 $\text{Terms}(\text{Specs}(\{C(t_1, ..., t_n)\})) = \text{Terms}(\text{Specs}(\{t\})).$ 

$$5.16.2. \qquad \models_{V_1} t \ Sat S \\ \Leftrightarrow \ \{\text{definition of Specs}\} \\ S \in \text{Specs}(\{t\}) \\ \Leftrightarrow \ \{\text{Corollary B.1.7 in Appendix B}\} \\ S \in \text{Specs}(\text{Terms}(\text{Specs}(\{t\}))) \\ \Leftrightarrow \ \{\text{Theorem 5.16.1}\} \\ S \in \text{Specs}(\llbracket t \rrbracket) \\ \Leftrightarrow \ \{\text{Lemma 5.7}\} \\ \models_{V_2} t \ Sat S \end{cases}$$

5.16.3. The  $\Leftarrow$  proof is trivial since  $t \in \llbracket t \rrbracket$ . For  $\Rightarrow$ , assume  $\{t\} \subseteq \llbracket u \rrbracket$ . Then Terms(Specs( $\{t\}$ ))  $\subseteq$  Terms(Specs( $\llbracket u \rrbracket$ )) by Corollary B.1.5 in Appendix B, and  $\llbracket t \rrbracket \subseteq \llbracket u \rrbracket$  by Theorem 5.16.1 and Lemma 5.4.

Whether  $\models_{V_1}$  Sat \_ is well-behaved depends partly on the expressivity of specifications. For example, suppose

$$\mathbb{K} = \{ \mathbf{x} := \mathbf{e} \mid \mathbf{e} \text{ is an arithmetic expression} \} \cup \{ \_ \$ \_ \}$$

i.e. there is a nullary constructor  $\mathbf{x} := \mathbf{e}$  for all arithmetic expressions  $\mathbf{e}$ , and a binary constructor for sequential composition. Suppose  $\mathbb{S} = \{\text{Even}_x\}$ , and  $\models_{V_1} \mathbf{t} \ Sat \text{Even}_x \text{ holds iff, if t is executed in any state where x is even, then$  $x is even in every resulting state. So <math>\models_{V_1} \mathbf{x} := \mathbf{x} + 1$   $\mathbf{s} \mathbf{x} := \mathbf{x} + 1 \ Sat \text{Even}_x$ , but it is not the case that  $\models_{V_1} \mathbf{x} := \mathbf{x} + 1 \ Sat \text{Even}_x$ . In fact,  $\mathbf{x} := \mathbf{x} + 1 \ does$ not satisfy any specification. This implies that  $\text{Terms}(\text{Specs}(\{\mathbf{x} := \mathbf{x} + 1\}))$ = T, so  $\mathbf{x} := 1$   $\mathbf{s} \mathbf{x} := 1 \in \text{Terms}(\text{Specs}(\{\mathbf{x} := \mathbf{x} + 1\}))$   $\mathbf{s} \ \text{Terms}(\text{Specs}(\{\mathbf{x} := \mathbf{x} + 1\}))$  $= \mathbf{x} + 1$ . But  $\models_{V_1} \mathbf{x} := 1$   $\mathbf{s} \mathbf{x} := 1 \ Sat \ \text{Even}_x$  does not hold, hence  $\models_{V_1} \ Sat \$ 

Even though  $\models_{V_1-} Sat_-$  is not well-behaved, it is still possible to have inference rules that are amenable to freefinement, for example:

$1 \overline{\mathbf{x} := \mathbf{e} Sat Even_{\mathbf{x}}}$	<u>1</u> t Sat Even_x	t' Sat Even_x
provided $e \in \{\dots, -2, 0, 2, \dots\}.$	$^{2}$ t $;$ t' Sat Even_x	

If S is instead a set of specifications of the form [P,Q], where P is a precondition and Q a postcondition, and

$$\models_{V_1} t \ \sharp t' \ Sat \ [P,Q] \quad \Leftrightarrow \quad \exists R \cdot \models_{V_1} t \ Sat \ [P,R] \land \models_{V_1} t' \ Sat \ [R,Q]$$

then it is easy to show that this  $\models_{V_1-} Sat_-$  is well-behaved.

The completeness of  $V_1$  is a sufficient condition for the well-behavedness of  $\models_{V_1}$ - Sat \_:

**Theorem 5.17.** If  $V_1$  is complete, then  $\models_{V_1-} Sat_-$  is well-behaved.

*Proof.* If  $V_1$  is complete, then  $\models_{V_1}C(t_1, \ldots, t_n)$  Sat  $S \Leftrightarrow \vdash_{V_1}C(t_1, \ldots, t_n)$  Sat S. The well-behavedness of  $\models_{V_1-} Sat_-$  follows by induction on the derivation of  $C(t_1, \ldots, t_n)$  Sat S:

- For each rule of the form  $A_1$ , assume  $\operatorname{Pred}(C, S_1, \ldots, S_n, S)$  and  $S_i \in \operatorname{Specs}(\{t_i\})$  for all  $i \in 1..n$ . So  $\forall t'_i \in \operatorname{Terms}(\operatorname{Specs}(\{t_i\})) \cdot \models_{V_1} t'_i Sat S_i$  for all  $i \in 1..n$ . The rule is sound with respect to Definition 5.1.1, hence  $\forall t \in C(\operatorname{Terms}(\operatorname{Specs}(\{t_1\})), \ldots, \operatorname{Terms}(\operatorname{Specs}(\{t_n\}))) \cdot \models_{V_1} t Sat S.$
- For each rule of the form  $B_1$ , assume  $\operatorname{Pred}(S_1, \ldots, S_m, S)$  and  $\forall t \in C(\operatorname{Terms}(\operatorname{Specs}(\{t_1\})), \ldots, \operatorname{Terms}(\operatorname{Specs}(\{t_n\}))) \mapsto_{V_1} t Sat S_i$  for all  $i \in 1..m$ . The rule is sound w.r.t. Definition 5.1.2, so  $\forall t \in C(\operatorname{Terms}(\operatorname{Specs}(\{t_1\})), \ldots, \operatorname{Terms}(\operatorname{Specs}(\{t_n\}))) \mapsto_{V_1} t Sat S. \square$

There are several ways to establish harmony between  $V_2$  and the refinement calculi. If a refinement calculus R includes the rule TRANS and enjoys the derivability property  $\vdash_{V_2} u$  Sat  $S \Leftrightarrow \vdash_R S \sqsubseteq u$ , then it is trivially harmonic. Including TRANS in  $R_1$  and maintaining the derivability property at each step also yields a nice presentation: it requires more work to show that  $R_1$  is harmonic, but the treatment of  $R_4$  becomes simpler. This simplification also applies to the existing presentation:  $R_3$  includes TRANS; it is harmonic and satisfies the derivability property by Theorem 5.9. Adding C-*i* and JOIN-*i* does not break the derivability property, and hence preserves harmony.

## 5.2 Applications

### 5.2.1 Lambda Calculus

The top left corner of Figure 5.1 contains a type system  $\lambda_1$  for the lambda calculus. By considering pairs of the form (typing context, type) as specifications, it is possible to apply freefinement and obtain a refinement calculus for (extended) lambda terms in the spirit of Denney [20]. The inputs to freefinement are as follows:

1.  $\mathbb{K} = \text{Var} \cup \{\lambda x_{-} \mid x \in \text{Var}\} \cup \{-, -\}$ Note that  $\mathbb{K}$  defines the language T of lambda terms:

 $\mathbf{e} ::= \mathbf{x} \mid \lambda \mathbf{x} \cdot \mathbf{e} \mid \mathbf{e} \mid \mathbf{e}'$ 

Here and in the following, x ranges over the set of variables Var, and e ranges over T.

- 2.  $\mathbb{S} = \{[\Gamma; \tau] \mid \Gamma \in \text{Context} \land \tau \in \text{Type}\}\)$ , where Context is the set of typing contexts and Type is the set of types that contains the type constructor  $\_ \rightarrow \_$ . The intended representation of a typing context  $\Gamma$  is a list of variable names paired with types. Variables may appear more than once in  $\Gamma$ , and variable lookup uses the rightmost occurrence. In the following,  $\sigma$  and  $\tau$  range over Type, and  $\Gamma$  ranges over Context.
- 3.  $\models_{V_1}$  Sat \_ is defined by:
  - $\models_{V_1} x Sat [\Gamma; \tau] \Leftrightarrow x : \tau \in \Gamma$
  - $\models_{\mathsf{V}_1} \lambda x. e \ Sat \ [\Gamma; \tau] \iff \exists \sigma, \tau' \cdot \tau = \sigma \rightarrow \tau' \land \models_{\mathsf{V}_1} e \ Sat \ [\Gamma, x : \sigma; \tau']$
  - $\models_{V_1} e e' Sat[\Gamma; \tau] \Leftrightarrow \exists \sigma \cdot \models_{V_1} e Sat[\Gamma; \sigma \to \tau] \land \models_{V_1} e' Sat[\Gamma; \sigma]$
- 4.  $V_1$ , shown in the top right corner of Figure 5.1, is obtained from  $\lambda_1$  by replacing  $\Gamma \vdash e : \tau$  with  $e Sat [\Gamma; \tau]$ . The rules VAR, ABS and APP are all of the form  $A_1$  with n = 0, 1 and 2 respectively. For example, in the case of ABS, Pred(C, S<sub>1</sub>, S) is defined as

 $\exists \mathbf{x}, \, \Gamma, \, \sigma, \, \tau \cdot \mathbf{C} = \lambda \mathbf{x}_{\cdot -} \wedge \, \mathbf{S}_1 = [\Gamma, \mathbf{x} : \sigma; \tau] \wedge \mathbf{S} = [\Gamma; \sigma \to \tau].$ 

$$\begin{array}{|c|c|c|c|c|} \hline \lambda_{1} & & V_{1} \\ \hline \lambda_{R} & \frac{x:\tau \in \Gamma}{\Gamma \vdash x:\tau} & & V_{R} \\ \hline Var & \frac{x:\tau \in \Gamma}{\Gamma \vdash \lambda x. e: \sigma \to \tau} & & Abs & \frac{\Gamma, x:\sigma \vdash e:\tau}{\Gamma \vdash \lambda x. e: \sigma \to \tau} \\ \hline Abs & \frac{\Gamma, x:\sigma \vdash e:\tau}{\Gamma \vdash e: \sigma \to \tau} & & Abs & \frac{e \ Sat \ [\Gamma, x:\sigma, \tau]}{\lambda x. e \ Sat \ [\Gamma; \sigma \to \tau]} \\ \hline App & \frac{\Gamma \vdash e':\sigma}{\Gamma \vdash e: e':\tau} & & App & \frac{e' \ Sat \ [\Gamma; \sigma]}{e' \ Sat \ [\Gamma; \sigma]} \\ \hline \hline \lambda_{2} & & R_{5} \\ \hline Var & \frac{x:\tau \in \Gamma}{\Gamma \vdash x:\tau} & & Abs & \frac{r}{\Gamma \vdash \tau;\tau} \\ \hline Abs & \frac{\Gamma, x:\sigma \vdash f:\tau}{\Gamma \vdash \lambda x. f: \sigma \to \tau} \\ \hline Abs & \frac{\Gamma, x:\sigma \vdash f:\tau}{\Gamma \vdash \lambda x. f: \sigma \to \tau} \\ \hline App & \frac{\Gamma \vdash f':\sigma}{\Gamma \vdash f \ t':\tau} \\ \hline App & \frac{\Gamma \vdash f':\sigma}{\Gamma \vdash f \ t':\tau} \\ \hline Spec & \frac{\Gamma \vdash [\Gamma;\sigma]:\sigma}{\Gamma \vdash [\Gamma;\sigma]:\sigma} \\ \hline Trans & \frac{f_{1} \sqsubseteq f_{2} \ f_{2} \sqsubseteq f_{3}}{f_{1} \sqsubseteq f_{3}} \\ \hline Mono & \frac{f \sqsubseteq f'}{g[f] \sqsubseteq g[f']} \\ \hline \end{array}$$

Figure 5.1: Freefinement and a typed lambda calculus.

Since  $V_1$  does not contain rules of the form  $B_1$  where m > 1, freefinement does not add join terms to the lambda calculus. The system  $\lambda_2$  in Figure 5.1 is  $V_2$  where f Sat  $[\Gamma; \tau]$  is written instead as  $\Gamma \vdash f : \tau$ . The system  $R_5$ , shown in the bottom right of Figure 5.1, is the final harmonic refinement calculus that freefinement produces.

Here is an example top-down typing derivation with  $R_5$ :

Since  $\mathsf{R}_5$  is harmonic and  $\mathsf{V}_2$  is a sound and conservative extension of  $\mathsf{V}_1$ , it holds that  $\vdash_{\lambda_1} \Gamma \vdash \lambda x$ .  $\lambda y$ .  $(x \ y) : (\sigma \to \tau) \to (\sigma \to \tau)$ .

One might wish to extend  $R_5$  using knowledge particular to lambda calculus typing. It is simple to show that  $V_1$  is complete, so

$$\vdash_{\lambda_1} \Gamma \vdash \mathbf{e} : \tau \iff \vdash_{\mathbf{V}_1} \mathbf{e} \; Sat \; [\Gamma; \tau] \iff \models_{\mathbf{V}_1} \mathbf{e} \; Sat \; [\Gamma; \tau]$$

Furthermore, by Theorems 5.17 and 5.16.2,

$$\models_{\mathsf{V}_1} e Sat [\Gamma; \tau] \Leftrightarrow \models_{\mathsf{V}_2} e Sat [\Gamma; \tau]$$

and because  $V_2$  is a sound and conservative extension of  $V_1$ ,

$$\vdash_{\mathsf{V}_1} \mathsf{e} \; Sat \; [\Gamma; \tau] \; \Leftrightarrow \; \vdash_{\mathsf{V}_2} \mathsf{e} \; Sat \; [\Gamma; \tau]$$

Consider the property of *preservation*:

**Definition 5.9.** A relation  $\rightsquigarrow \subseteq T \times T$  satisfies preservation  $\stackrel{\text{def}}{=} \forall \Gamma, \tau, e, e' \cdot \text{if } \vdash_{\lambda_1} \Gamma \vdash e : \tau \text{ and } e \rightsquigarrow e', \text{ then } \vdash_{\lambda_1} \Gamma \vdash e' : \tau.$ 

**Theorem 5.18.** If  $\rightsquigarrow$  satisfies preservation, then:

5.18.1. If  $e \rightsquigarrow e'$ , then  $\models e \sqsubseteq e'$ .

5.18.2. If  $\vdash_{V_2} e$  Sat  $[\Gamma; \tau]$  and  $e \rightsquigarrow e'$ , then  $\vdash_{V_2} e'$  Sat  $[\Gamma; \tau]$ .

*Proof.* The proof of 5.18.2 is trivial. For 5.18.1:

$$\begin{array}{l} \forall \Gamma, \tau, e, e' \cdot \vdash_{\lambda_1} \Gamma \vdash e : \tau \land e \rightsquigarrow e' \Rightarrow \vdash_{\lambda_1} \Gamma \vdash e' : \tau \\ \Leftrightarrow \quad \{ \text{predicate logic} \} \\ \forall e, e' \cdot e \rightsquigarrow e' \Rightarrow (\forall \Gamma, \tau \cdot \vdash_{\lambda_1} \Gamma \vdash e : \tau \Rightarrow \vdash_{\lambda_1} \Gamma \vdash e' : \tau) \\ \Leftrightarrow \quad \{ \text{equivalent terms} \} \\ \forall e, e' \cdot e \rightsquigarrow e' \Rightarrow (\forall S \in \mathbb{S} \cdot \models_{V_2} e \text{ Sat } S \Rightarrow \models_{V_2} e' \text{ Sat } S) \\ \Leftrightarrow \quad \{ \text{Lemma 5.8} \} \\ \forall e, e' \cdot e \rightsquigarrow e' \Rightarrow \models_{U_2} e \subseteq U \\ \forall e \in U \in U = U \\ \forall e \in U \in U \\ e' \in U$$

So any relation that satisfies preservation contains only sound refinements that satisfy Harmony 1, and can augment  $R_5$  to yield a sound and harmonic refinement system. Examples of relations that satisfy preservation include:

- The  $\alpha$ -conversion relation.
- The  $\beta$ -reduction relation.
- The  $\eta$ -contraction relation. So  $\lambda x$ . (e x)  $\sqsubseteq$  e, provided x does not appear free in e.
- The relation  $\leq$  on closed terms, where  $e \leq e'$  exactly when e has fewer types than e'.

Here is a small example that uses the  $\eta$ -contraction extension:

### 5.2.2 Hoare Logic

Figure 5.2 contains system H, a Hoare logic for simple imperative programs. P is a precondition, Q a postcondition, and c a command in the Hoare triple  $\{P\}c\{Q\}$ , and  $\models_{\mathsf{H}}\{P\}c\{Q\}$  is the usual partial correctness interpretation of  $\{P\}c\{Q\}$ . By interpreting a specification as a pre-post pair, the rules of H do not fit the rule forms A<sub>1</sub> and B<sub>1</sub>, since the proviso of AUXVARELIM inspects the command c to determine the variables that it writes and reads. However, if specifications also keep track of written and read variables, then it becomes possible to apply freefinement to obtain a refinement calculus in the spirit of Morgan [50]. Here are the inputs:

1. There are constructors for assignments, sequential composition, conditionals and loops:



Figure 5.2: Freefinement and Hoare logic: the system H.

$V_1$	
$\begin{array}{ccc} AUXVARELIM & \frac{c  Sat  \overline{x}; \overline{y}: \{P, Q\}}{c  Sat  \overline{x}; \overline{y}: \{\exists v \cdot P, \exists v \cdot Q\}}\\ provided \ v \notin \overline{x} \cup \overline{y}. \end{array}$	
CONSEQUENCE $\frac{c  Sat  \overline{x'}; \overline{y'}: \{P', Q'\}}{c  Sat  \overline{x}; \overline{y}: \{P, Q\}}$ provided $\overline{x} \supseteq \overline{x'}$ and $\overline{y} \supseteq \overline{y'}$ and $P \Rightarrow P'$ and $Q' \Rightarrow Q$ .	
CONSTANCY $\frac{c  Sat  \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{P}, \mathbf{Q}\}}{c  Sat  \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{P} \land \mathbf{R}, \mathbf{Q} \land \mathbf{R}\}}$ provided $FV(\mathbf{R}) \cap \overline{\mathbf{x}} = \emptyset.$	
DISJ $\frac{c  Sat  \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{P}, \mathbf{Q}\}  c  Sat  \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{P}', \mathbf{Q}'\}}{c  Sat  \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{P} \lor \mathbf{P}', \mathbf{Q} \lor \mathbf{Q}'\}}$	
VARASSIGN $\overline{\mathbf{x} := \mathbf{e}  Sat  \mathbf{x}; FV(\mathbf{e}) : \{\mathbf{P}[\mathbf{e}/\mathbf{x}], \mathbf{P}\}}$	
$\operatorname{SEQCOMP} \frac{\operatorname{c} \operatorname{Sat} \overline{x}; \overline{y} : \{P, Q\}  \operatorname{c'} \operatorname{Sat} \overline{x'}; \overline{y'} : \{Q, R\}}{\operatorname{c} \operatorname{\mathfrak{s}} \operatorname{c'} \operatorname{Sat} \overline{x} \cup \overline{x'}; \overline{x} \cup \overline{x'}; \{P, R\}}$	
$COND \frac{c  Sat  \overline{x}; \overline{y}: \{P \land b, Q\}  c'  Sat  \overline{x'}; \overline{y'}: \{P \land \neg b, Q\}}{\mathbf{if \ b \ then \ c \ else \ c'  Sat  \overline{x} \cup \overline{x'}; \overline{y} \cup \overline{y'} \cup FV(b): \{P, Q\}}$	
$LOOP \xrightarrow{c  Sat  \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{I} \land \mathbf{b}, \mathbf{I}\}}{\mathbf{while } \mathbf{b} \mathbf{ do } \mathbf{c}  Sat  \overline{\mathbf{x}}; \overline{\mathbf{y}} \cup FV(\mathbf{b}): \{\mathbf{I}, \mathbf{I} \land \neg \mathbf{b}\}}$	

Figure 5.3: Freefinement and Hoare logic: the system  $\mathsf{V}_1.$ 

$$V_{2}$$
AuxVarELIM  $\frac{s \ Sat \ \overline{x}; \overline{y}; \{P, Q\}}{s \ Sat \ \overline{x}; \overline{y}; \{\exists v \cdot P, \exists v \cdot Q\}}$ 
provided  $v \notin \overline{x} \cup \overline{y}$ .  
Consequence  $\frac{s \ Sat \ \overline{x}; \overline{y}; \{P, Q\}}{s \ Sat \ \overline{x}; \overline{y}; \{P, Q\}}$ 
provided  $\overline{x} \supseteq \overline{x'}$  and  $\overline{y} \supseteq \overline{y'}$  and  $P \Rightarrow P'$  and  $Q' \Rightarrow Q$ .  
Constancy  $\frac{s \ Sat \ \overline{x}; \overline{y}; \{P, Q\}}{s \ Sat \ \overline{x}; \overline{y}; \{P, Q, AR\}}$ 
provided  $FV(R) \cap \overline{x} = \emptyset$ .  
DISJ  $\frac{s \ Sat \ \overline{x}; \overline{y}; \{P, Q\} \ s \ Sat \ \overline{x}; \overline{y}; \{P', Q'\}}{s \ Sat \ \overline{x}; \overline{y}; \{P \lor P, Q \land R\}}$ 
VARASSIGN  $\overline{x:=e \ Sat \ x; FV(e); \{P[e/x], P\}}$   
SEQCOMP  $\frac{s \ Sat \ \overline{x}; \overline{y}; \{P, Q\} \ s' \ Sat \ \overline{x'}; \overline{y'}; \{Q, R\}}{s^{2}s' \ Sat \ \overline{x} \cup \overline{x'}; \overline{x} \cup \overline{x'}; \{P, R\}}$   
Cond  $\frac{s \ Sat \ \overline{x}; \overline{y}; \{P \land b, Q\} \ s' \ Sat \ \overline{x'}; \overline{y'}; \{P \land \neg b, Q\}}{if \ b \ then \ s \ else \ s' \ Sat \ \overline{x}; \overline{y}; \{I \land b, I\}}$   
Loop  $\frac{s \ Sat \ \overline{x}; \overline{y}; \{P, Q\} \ Sat \ \overline{x}; \overline{y}; \{P, Q\}}{while \ b \ do \ s \ Sat \ \overline{x}; \overline{y}; \{P, Q\}}$ 

Figure 5.4: Freefinement and Hoare logic: the system  $\mathsf{V}_2.$ 

$R_6$
AUXVARELIM $\overline{\overline{x}}; \overline{v}; \{\exists v \cdot P, \exists v \cdot Q\} \sqsubset \overline{x}; \overline{v}; \{P, Q\}$
provided $v \notin \overline{x} \cup \overline{y}$ .
Consequence $\overline{\overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{P}, \mathbf{Q}\} \sqsubseteq \overline{\mathbf{x}'}; \overline{\mathbf{y}'}: \{\mathbf{P}', \mathbf{Q}'\}}$
provided $\overline{\mathbf{x}} \supseteq \overline{\mathbf{x}'}$ and $\overline{\mathbf{y}} \supseteq \overline{\mathbf{y}'}$ and $\mathbf{P} \Rightarrow \mathbf{P}'$ and $\mathbf{Q}' \Rightarrow \mathbf{Q}$ .
CONSTANCY $\overline{\overline{\mathbf{x}}; \overline{\mathbf{y}}: \{P \land R, Q \land R\}} \sqsubseteq \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{P, Q\}}$
provided $FV(\mathbf{R}) \cap \overline{\mathbf{x}} = \emptyset$ .
$DISJ \ \overline{\overline{x}; \overline{y}: \{P \lor P', Q \lor Q'\}} \sqsubseteq \bigsqcup(\overline{x}; \overline{y}: \{P, Q\}, \overline{x}; \overline{y}: \{P', Q'\})}$
VARASSIGN $\overline{x; FV(e): \{P[e/x], P\} \sqsubseteq x := e}$
$\underline{\operatorname{SeqComp}}_{\overline{\overline{x} \cup \overline{x'}; \overline{x} \cup \overline{x'}: \{P, R\}} \sqsubseteq \overline{x}; \overline{y}: \{P, Q\} \ ; \ \overline{x'}; \overline{y'}: \{Q, R\}}$
$\operatorname{COND} \frac{\overline{\mathbf{x} \cup \mathbf{x}'}; \overline{\mathbf{y} \cup \mathbf{y}' \cup FV(\mathbf{b})}; \{\mathbf{P}, \mathbf{Q}\}}{\mathbf{x} \cup \mathbf{x}'; \overline{\mathbf{y} \cup \mathbf{y}' \cup FV(\mathbf{b})}; \{\mathbf{P}, \mathbf{Q}\}}$
$\sqsubseteq \mathbf{if} \ \mathbf{b} \ \mathbf{then} \ \overline{\mathbf{x}}; \overline{\mathbf{y}} \colon \{\mathbf{P} \land \mathbf{b}, \mathbf{Q}\} \ \mathbf{else} \ \overline{\mathbf{x}'}; \overline{\mathbf{y}'} \colon \{\mathbf{P} \land \neg \mathbf{b}, \mathbf{Q}\}$
$Loop \ \overline{\overline{\mathbf{x}}; \overline{\mathbf{y}} \cup FV(\mathbf{b}) \colon \{\mathbf{I}, \mathbf{I} \land \neg \mathbf{b}\} \sqsubseteq \mathbf{while} \ \mathbf{b} \ \mathbf{do} \ \overline{\mathbf{x}}; \overline{\mathbf{y}} \colon \{\mathbf{I} \land \mathbf{b}, \mathbf{I}\}}$
$\operatorname{Spec} \overline{\overline{x}; \overline{y}: \{P, Q\} \sqsubseteq \overline{x}; \overline{y}: \{P, Q\}}$
$Join \ \overline{\overline{x}; \overline{y}: \{P, Q\}} \sqsubseteq \bigsqcup(\dots, \overline{x}; \overline{y}: \{P, Q\}, \dots)$
$T_{RANS} \frac{s_1 \sqsubseteq s_2}{s_1 \sqsubseteq s_3} \frac{s_2 \sqsubseteq s_3}{s_1 \sqsubseteq s_3}$
$Mono \frac{s \sqsubseteq s'}{t[s] \sqsubseteq t[s']}$
Unjoin $\overline{\bigsqcup(s, \ldots, s) \sqsubseteq s}$

Figure 5.5: Freefinement and Hoare logic: the system  $\mathsf{R}_6.$ 

 $\mathbb{K} = \{ \mathbf{x} := \mathbf{e} \mid \mathbf{x} \in \operatorname{Var} \land \mathbf{e} \in \operatorname{IntExp} \} \\ \cup \{ \_, "]_{-} \\ \cup \{ \mathbf{if} \ \mathbf{b} \ \mathbf{then} \ \_ \ \mathbf{else} \ \_ \ | \ \mathbf{b} \in \operatorname{BoolExp} \} \\ \cup \{ \mathbf{while} \ \mathbf{b} \ \mathbf{do} \ \_ \ | \ \mathbf{b} \in \operatorname{BoolExp} \}$ 

2. A specification consists of two sets of variables and two assertions, written in a notation resembling Morgan's specification statement [49]:

 $\mathbb{S} = \left\{ \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{P}, \mathbf{Q}\} \mid \overline{\mathbf{x}}, \overline{\mathbf{y}} \in \mathcal{P}(\operatorname{Var}) \land \mathbf{P}, \mathbf{Q} \in \operatorname{Assertion} \right\}$ 

3. In the specification  $\overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{P}, \mathbf{Q}\}$ , the  $\overline{\mathbf{x}}$  and  $\overline{\mathbf{y}}$  are upper bounds on the sets of variables written and read by the command respectively, the P is a precondition and the Q a postcondition:

 $\models_{\mathsf{V}_1} c \textit{ Sat } \overline{x}; \overline{y}: \{P, Q\} \stackrel{def}{=} \textit{ writes}(c) \subseteq \overline{x} \land \textit{ reads}(c) \subseteq \overline{y} \land \models_{\mathsf{H}} \{P\} c \{Q\}$ 

4.  $V_1$ , shown in Figure 5.3, has the following relationship with H:  $\vdash_{V_1c} Sat \overline{x}; \overline{y}: \{P, Q\} \iff writes(c) \subseteq \overline{x} \land reads(c) \subseteq \overline{y} \land \vdash_{\mathsf{H}} \{P\}c\{Q\}$ 

Note that:

• The non-structural rules of H have counterparts in  $V_1$  that embody the definitions of *writes* and *reads*. For example, the conclusion of COND reflects that

writes (if b then c else c')  $\stackrel{\text{def}}{=} writes(c) \cup writes(c')$  and reads(if b then c else c')  $\stackrel{\text{def}}{=} reads(c) \cup reads(c') \cup FV(b)$ .

- The structural rules of H that inspect c for its write and/or read sets have counterparts in  $V_1$  that consult the specification instead. See for example the proviso of AUXVARELIM.
- CONSEQUENCE in  $V_1$  allows the enlargement of write and read sets. This loosening of the bounds is useful in refinement developments, because then the resulting code is not forced to write and read all the variables that were originally available for writing and reading.

The V<sub>1</sub>-counterparts of the structural rules of H are all of the form B<sub>1</sub>. For example, m = 1 in the case of CONSTANCY, and m = 2 for DISJ. The other rules are of the form A<sub>1</sub>. For example, n = 2 in the case of COND, and n = 1 for LOOP.

The systems  $V_2$  and  $R_6$  that freefinement produces appear in Figures 5.4 and 5.5.  $R_6$  yields several derived rules that may be useful in practical refinement developments. For example, the rule: DERIVEDVARASSIGN  $\overline{\overline{x}; \overline{y}: \{P, Q\}} \sqsubseteq z := e$ provided  $z \in \overline{x}$  and  $FV(e) \subseteq \overline{y}$  and  $P \Rightarrow Q[e/z]$ .

can replace VARASSIGN, and is similar to the assignment law of Morgan (Law 1.3 on p. 8 of [50]). Likewise, the derived rule:

FOLLOWINGVARASSIGN  $\overline{\overline{x}; \overline{y}: \{P, Q\}} \sqsubseteq \overline{x}; \overline{y}: \{P, Q[e/z]\}; z := e$ provided  $z \in \overline{x}$  and  $FV(e) \subseteq \overline{y}$ .

is similar to the *following assignment* law of Morgan (Law 3.5 on p. 32 of [50]).

Here is an example showing that  $R_6$  can derive a correct factorial algorithm starting with its specification:

$$\begin{array}{l} y,z;x,y,z: \{ true, y = x! \} \\ & \sqsubseteq \\ y,z; \emptyset: \{ true, y = 1 \land z = 0 \} \, \mathring{}\, y,z; x,y,z: \{ y = 1 \land z = 0, y = x! \} \end{array}$$

The first spec statement is refined as follows:

 $y,z; \emptyset: \{true, y = 1 \land z = 0\}$   $\sqsubseteq \text{"SEQCOMP"}$   $y; \emptyset: \{true, y = 1\} \text{$}z; \emptyset: \{y = 1, y = 1 \land z = 0\}$   $\sqsubseteq \text{"Twice Mono with Consequence"}$   $y; \emptyset: \{1 = 1, y = 1\} \text{$}z; \emptyset: \{y = 1 \land 0 = 0, y = 1 \land z = 0\}$   $\sqsubseteq \text{"Twice Mono with VARASSIGN"}$  y := 1 \$z := 0

And for the second spec statement:

 $y,z; x,y,z: \{y = 1 \land z = 0, y = x!\}$ "CONSEQUENCE"  $y,z; x,y,z: \{y = z!, y = z! \land \neg z \neq x\}$ "LOOP" while  $z \neq x$  do  $y, z; y, z: \{y = z! \land z \neq x, y = z!\}$ "MONO with SEQCOMP" while  $z \neq x$  do  $z; z: \{y = z! \land z \neq x, y \cdot z = z!\}$   $gy; y; z: \{y \cdot z = z!, y = z!\}$ "Mono with VARASSIGN" while  $z \neq x$  do z; z: {y = z!  $\land z \neq x, y \cdot z = z!$ }  $gy := y \cdot z$ "MONO with CONSEQUENCE" while  $z \neq x$  do z; z: {y·(z+1) = (z+1)!, y·z = z!}  $gy := y \cdot z$ 

= "Mono with VARASSIGN" while  $z \neq x$  do z := z+1;  $y := y \cdot z$ 

Since  $\vdash_{\mathsf{R}_6}$  y,z; x,y,z: {true, y = x!}  $\sqsubseteq$  y := 1 °; z := 0 °; while  $z \neq x$  do z := z+1 °; y := y·z, it is the case that  $\vdash_{\mathsf{V}_1}$  y := 1 °; z := 0 °; while  $z \neq x$  do z := z+1 °; y := y·z Sat y,z; x,y,z: {true, y = x!} and hence also

 $\vdash_{\mathsf{H}} \{ \text{true} \} \mathbf{y} := 1 \, \mathbf{\hat{s}} \, \mathbf{z} := 0 \, \mathbf{\hat{s}} \, \mathbf{while} \, \mathbf{z} \neq \mathbf{x} \, \mathbf{do} \, \mathbf{z} := \mathbf{z} + 1 \, \mathbf{\hat{s}} \, \mathbf{y} := \mathbf{y} \cdot \mathbf{z} \{ \mathbf{y} = \mathbf{x}! \}.$ 

Here is another example of using  $\mathsf{R}_6$ ; it involves join statements. The statement  $\bigsqcup(\overline{x}; \overline{y} : \{\mathsf{P}_1, \mathsf{Q}_1\}, \overline{x}; \overline{y} : \{\mathsf{P}_2, \mathsf{Q}_2\})$  is the join of the specification statements  $\overline{x}; \overline{y} : \{\mathsf{P}_1, \mathsf{Q}_1\}$  and  $\overline{x}; \overline{y} : \{\mathsf{P}_2, \mathsf{Q}_2\}$ . Expressing it as a spec statement is simple because

$$\bigsqcup(\overline{\mathbf{x}}; \overline{\mathbf{y}}: \{P_1, Q_1\}, \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{P_2, Q_2\}) \equiv \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{P_1, Q_1\} \text{ also } \{P_2, Q_2\}$$

where the definition of  $\{P_1, Q_1\}$  also  $\{P_2, Q_2\}$ , taken from [58], is:  $\{(P_1 \land z=1) \lor (P_2 \land z\neq 1), (Q_1 \land z=1) \lor (Q_2 \land z\neq 1)\}$  where z is fresh.  $\mathsf{R}_6$  can derive both directions of refinement. Firstly:

Secondly:

$$\overline{\mathbf{x}}; \overline{\mathbf{y}}: \{ (P_1 \land z=1) \lor (P_2 \land z\neq 1), (Q_1 \land z=1) \lor (Q_2 \land z\neq 1) \}$$

$$\sqsubseteq \qquad \text{``DISJ''} \\ \bigsqcup(\overline{\mathbf{x}}; \overline{\mathbf{y}}: \{P_1 \land z=1, Q_1 \land z=1\}, \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{P_2 \land z\neq 1, Q_2 \land z\neq 1\})$$

$$\sqsubseteq \qquad \text{``Twice Mono with Constancy''} \\ \bigsqcup(\overline{\mathbf{x}}; \overline{\mathbf{y}}: \{P_1, Q_1\}, \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{P_2, Q_2\})$$

Leino and Manohar [43] mention several uses of the join of spec-like statements.

### 5.2.3 Discussion

The type system  $\lambda_1$  considered above is very simple. Freefinement also applies to System F and other more sophisticated type systems.

Although  $\lambda_1$  had only rules of the form  $A_1$ , typing rules of the form  $B_1$  are quite common – examples include rules for subtyping and intersection types:

$$\begin{array}{l} \text{SUB} \frac{\Gamma \vdash \text{e} : \tau}{\Gamma \vdash \text{e} : \tau'} \\ \text{provided } \tau <: \tau'. \end{array} \qquad \qquad \text{INTER} \frac{\Gamma \vdash \text{e} : \tau}{\Gamma \vdash \text{e} : \tau \wedge \tau'} \end{array}$$

There is no golden recipe for adapting proof systems to make them amenable to freefinement. However, enriching specifications and/or terms might help. The Hoare logic example used enriched specifications to keep track of write and read sets. Consider again the two problematic rules from before:

$$2 \frac{\operatorname{succ}(n) : \mathbb{N}}{\operatorname{pred}(\operatorname{succ}(n)) : \mathbb{N}} \qquad \qquad 3 \frac{n : \mathbb{N}}{\operatorname{pred}(n) : \mathbb{N}}$$
  
provided positive(n).

Rule 2 can be accommodated by choosing  $\mathbb{S} = \{ z', z', z', p' \} \times \{\mathbb{N}\}$ . Intuitively, the specification ('s',  $\mathbb{N}$ ) tracks the fact that the outermost constructor is 'succ'. The rule then becomes:

$$2 \frac{\mathrm{n} : (\mathrm{`s'}, \mathbb{N})}{\mathrm{pred}(\mathrm{n}) : (\mathrm{`p'}, \mathbb{N})}$$

Rule 3 can be accommodated by choosing  $\mathbb{S} = \mathbb{N} \times {\mathbb{N}}$ . Then the sentence  $n : (i, \mathbb{N})$  tracks the fact that term n denotes the natural number i. The adapted rule is of the form  $A_1$  with n = 1:

$$3 \frac{\mathbf{n} : (i, \mathbb{N})}{\operatorname{pred}(\mathbf{n}) : (i - 1, \mathbb{N})}$$
  
provided  $i > 0$ .

In some cases it might be useful to enrich the term language. For example, consider the rule of concurrent separation logic [7] that removes auxiliary commands (ghost assignments):

AUXILIARY 
$$\frac{\Gamma \vdash \{P\}c\{Q\}}{\Gamma \vdash \{P\}c\backslash a\{Q\}}$$
  
provided  $a \in aux(c)$  and  $a \cap (FV(P) \cup FV(Q)) = \emptyset$ 

This rule is not of the form  $A_1$  or  $B_1$ , because it contains a meta-operation in the conclusion. However, if the meta-operation is turned into an explicit constructor (and specifications track auxiliaries), then the rule is of the form  $B_1$  with m = 1 and freefinement can handle it.

### 5.3 Constructing correct OO programs

This section outlines a technique that provides correctness by construction for OO programs. It applies freefinement to the statement verification part of the separation logic proof system in Chapter 2, and combines the result with rules that deal with non-statement constructs.

The judgements for statement verification in Section 2.5 have the form  $\Delta; \Gamma \vdash_s \{P\}s\{Q\}$ . No rule modifies  $\Delta$  and  $\Gamma$ , so they are constant parameters of statement verification. Spec statements can consequently exclude  $\Delta$  and  $\Gamma$  and retain the form  $\overline{x}; \overline{y} : \{P, Q\}$  of the previous section. Freefinement also adds join statements to the language, and proceeds as in the Hoare logic example. The resulting refinement judgements have the form  $\Delta; \Gamma \vdash s \sqsubseteq s'$ . For example, the refinement counterpart of the Frame rule resembles CONSTANCY from before:

$$FV(\mathbf{R}) \cap \overline{\mathbf{x}} = \emptyset$$
  
$$\Delta; \Gamma \vdash \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{P} * \mathbf{R}, \mathbf{Q} * \mathbf{R}\} \sqsubseteq \overline{\mathbf{x}}; \overline{\mathbf{y}}: \{\mathbf{P}, \mathbf{Q}\}$$

The ability to refine statements is useful in the context of OO programming, but correctness by construction also needs to accommodate other language constructs and concepts such as data refinement [49]. To this end, the rest of the section presents a suitably extended language and a development calculus.

### 5.3.1 Language and proof rules

Figure 5.6 shows the grammar of a slightly modified language. The highlighted productions generate non-executable code, which facilitate the gradual development of methods and statements. The only other difference with the language of Chapter 2 is that method declarations of the form 'Sig Sd Ss **do** s **end**' do not explicitly indicate whether they introduce or override the method. However, the earlier convention that all non-constructor methods must be listed in all subclasses makes it simple to distinguish these cases.

The proof rules of Chapter 2 are sufficient to verify code that does not use the highlighted productions. Verifying programs that are partially developed needs additional rules for the new constructs. Familiar rules apply to spec and join statements:

$$\Delta; \Gamma \vdash_{s} \{P\}\overline{\mathbf{x}}; \overline{\mathbf{y}}: \{P, Q\}\{Q\}$$
$$\Delta; \Gamma \vdash_{s} \{P\}s\{Q\}$$
$$\Delta; \Gamma \vdash_{s} \{P\} \bigsqcup (\dots, s, \dots)\{Q\}$$

```
L ::= class G Inh Def Feat end
Inh ::= inherit H
Def ::= define \overline{D}
Feat ::= feature \overline{M} \overline{F}
D ::= x.p_G(\overline{t:y}) as P
                                                                 Define clause
M ::= Sig
                                                                 Method declaration
    Sig Sd
     Sig Sd Ss
     Sig Sd Ss do s end
    inherit Sig Sd Ss
F ::= f: Type
                                                                 Field declaration
Sig ::= m(Args) Rt
Sd ::= dynamic Spec
                                                                 Dynamic specification
Ss ::= static Spec
                                                                 Static specification
Spec ::= \{P\}_{-}\{Q\} | \{P\}_{-}\{Q\} also Spec
                                                                 Specification
s ::= \overline{x}; \overline{y}: \{P, Q\}
                                                                 Spec statement
     |(\mathbf{s}_1, \ldots, \mathbf{s}_n)|
                                                                 Join statement
     skip
                                                                 Assignment
     \mathbf{x} := \mathbf{e}
     s:s
                                                                 Sequential composition
     if e then s else s end
                                                                 Conditional
      while e do s end
                                                                 Loop
     x: Type. s
                                                                 Local variable block
     x := new G
                                                                 Object allocation
     \mathbf{x} := \mathbf{y}.\mathbf{f}
                                                                 Field lookup
     x.f := e
                                                                 Field update
     \mathbf{x} := \mathbf{y}.\mathbf{m}(\overline{\mathbf{e}}) \mid \mathbf{y}.\mathbf{m}(\overline{\mathbf{e}})
                                                                 Dynamically dispatched call
    | \mathbf{x} := \mathbf{y}.\mathbf{G}::\mathbf{m}(\overline{\mathbf{e}}) | \mathbf{y}.\mathbf{G}::\mathbf{m}(\overline{\mathbf{e}})
                                                                 Direct method call
e ::= x | e + e | e = e | Void | 0 | 1 | 2 | \dots
                                                                 Expression
Type ::= INT \mid BOOL \mid G
Args ::= x: Type
                                                                 Formal arguments
Rt ::= \epsilon | : Type
                                                                 Return type
```

Figure 5.6: The extended grammar.

A method signature is always verified:

 $\Delta; \Gamma \vdash_m \text{Sig in G parent H}$ 

In order to reduce duplication and improve readability, the presentation will frequently use holes or placeholders in the premises of rules. These holes can then be filled in different ways to obtain different rules. For example, the rule for verifying a method signature with a dynamic specification has a hole, [Hole<sub>B.s</sub>], that captures the Behavioral subtyping proof obligation:

 $\frac{[\operatorname{Hole}_{B.s.}]}{\Delta; \Gamma \vdash_m \operatorname{Sig} \operatorname{Sd} \text{ in G parent H}}$ 

 $[Hole_{B.s.}]$  can be filled in two ways:

 $\begin{aligned} &\mathrm{Sd} = \mathbf{dynamic} \ \mathrm{Spec}_{\mathrm{G}} \\ &\Gamma(\mathrm{H.m}) = (\overline{\mathrm{u}}, \mathrm{Spec}_{\mathrm{H}}) \\ &\Delta \vdash \mathrm{Spec}_{\mathrm{G}} \Longrightarrow \mathrm{Spec}_{\mathrm{H}} \end{aligned}$ 

H.m  $\notin dom(\Gamma)$ 

The rule for signatures with dynamic and static specifications also uses  $[Hole_{B.s.}]$ , and adds a proof obligation for Dynamic dispatch:

 $\begin{array}{l} [\operatorname{Hole}_{\operatorname{B.s.}}]\\ [\operatorname{Hole}_{\operatorname{D.d.}}] \end{array}$  $\Delta; \Gamma \vdash_m \operatorname{Sig} \operatorname{Sd} \operatorname{Ss} \text{ in } \operatorname{G} \text{ parent } \operatorname{H} \end{array}$ 

 $[Hole_{D.d.}]$  is a placeholder for:

 $\begin{array}{l} \mathrm{Ss} = \mathbf{static} \ \mathrm{Spec}_{\mathrm{S}} \\ \mathrm{Sd} = \mathbf{dynamic} \ \mathrm{Spec}_{\mathrm{D}} \\ \Delta \vdash \mathrm{Spec}_{\mathrm{S}} \overset{\mathbf{Current} \, : \, \mathrm{G}}{\Longrightarrow} \ \mathrm{Spec}_{\mathrm{D}} \end{array}$ 

The following definition is convenient for referring to a collection of wellformed and verified classes:

 $\vdash \overline{\mathbf{L}} \stackrel{\mathrm{def}}{=} \forall \mathbf{L} \in \overline{\mathbf{L}} \cdot apf(\mathbf{L}); specs(\overline{\mathbf{L}}) \vdash_{c} \mathbf{L}$ 

### 5.3.2 Development calculus

The calculus for developing correct OO programs uses judgements of the form  $\overline{L} \rightsquigarrow \overline{L'}$ . The symbol  $\rightsquigarrow$  can be read as 'evolves to' or 'becomes'. To be useful, the calculus must satisfy two properties:

- Soundness: If  $\vdash \overline{L}$  and  $\overline{L} \rightsquigarrow \overline{L'}$ , then  $\vdash \overline{L'}$ .
- Completeness: If  $\vdash \overline{\mathbf{L}}$  then  $\epsilon \rightsquigarrow^* \overline{\mathbf{L}}$ .

The soundness property guarantees that the rules will maintain correctness and not introduce errors. The completeness property ensures that any correct program can be developed from scratch with the calculus. In practical terms, this means that the calculus supports the top-down development of every correct program. However, several rules will break with the top-down style and enable more flexible evolution. The rest of this section summarises the calculus.

#### Adding a class

Any new class needs a fresh name. A new class that does not inherit from any user-defined class inherits no method signatures:

$$\overline{\mathbf{G}} \notin class-names(\overline{\mathbf{L}}) \cup \{ANY\}$$
$$\overline{\mathbf{L}} \rightsquigarrow \overline{\mathbf{L}} + \mathbf{class} \ \mathbf{G} \ \mathbf{define \ feature \ end}$$

Otherwise, it inherits from its parent the signatures of all methods that are not constructors:

$$\begin{array}{l} \mathbf{G} \notin class-names(\overline{\mathbf{L}}) \cup \{\mathbf{ANY}\} \\ \overline{\mathbf{L}} = \overline{\mathbf{L}_1} + \mathbf{class} \; \mathbf{H} \; \mathbf{Inh} \; \mathbf{Def} \; \mathbf{feature} \; \overline{\mathbf{M}} \; \overline{\mathbf{F}} \; \mathbf{end} \\ \overline{\mathbf{Sig}} = non-constructor-signatures(\overline{\mathbf{M}}) \end{array}$$

 $\overline{L} \rightsquigarrow \overline{L} +$ class G inherit H define feature  $\overline{Sig}$  end

#### Adding an apf entry

Apf entries support data refinement: they describe how a class represents the abstract concepts/structures expressed by apf predicates. Provided that the definition is well-formed, the rule for adding an apf entry is simple:

 $\overline{\mathbf{L}} = \overline{\mathbf{L}_1} + \mathbf{class} \ \mathbf{G} \ \mathbf{Inh} \ \mathbf{define} \ \overline{\mathbf{D}} \ \mathbf{Feat} \ \mathbf{end}$  $\frac{\mathbf{p}_G}{\mathbf{p}'} \notin \underbrace{defined}_{apf} - apf-entries(\overline{\mathbf{D}})$  $\overline{\mathbf{D}'} = \overline{\mathbf{D}} + \mathbf{x} \cdot \mathbf{p}_G(\overline{\mathbf{t}}; \mathbf{y}) \ \mathbf{as} \ \mathbf{P}$ 

 $\overline{L} \rightsquigarrow \overline{L_1} + \mathbf{class} \ \mathrm{G} \ \mathrm{Inh} \ \mathbf{define} \ \overline{D'} \ \mathrm{Feat} \ \mathbf{end}$ 

#### Adding a field

A new field must have a fresh name and a legal type:

 $\overline{L} = \overline{L_1} + \textbf{class} \ G \ Inh \ Def \ \textbf{feature} \ \overline{M} \ \overline{F} \ \textbf{end}$   $classes = ancestors(G,\overline{L}) + G + descendants(G,\overline{L})$   $f \notin field\text{-names}(classes,\overline{L})$   $Type \in legal\text{-types}(\overline{L})$   $\overline{F'} = \overline{F} + f: \ Type$   $\overline{L} \rightsquigarrow \overline{L_1} + \textbf{class} \ G \ Inh \ Def \ \textbf{feature} \ \overline{M} \ \overline{F'} \ \textbf{end}$ 

#### Adding a constructor signature

A constructor signature has the same name as its enclosing class and no return type.

 $\overline{L} = \overline{L_1} + \text{class G Inh Def feature } \overline{M} \ \overline{F} \ \text{end}$ Sig = G(Args) valid-signature(Sig,G,\overline{L})  $\overline{M'} = \overline{M} + \text{Sig}$  $\overline{L} \rightsquigarrow \overline{L_1} + \text{class G Inh Def feature } \overline{M'} \ \overline{F} \ \text{end}$ 

The predicate valid-signature  $(Sig, \overline{G}, \overline{L})$  holds iff

- 1.  $\forall \mathbf{G} \in \overline{\mathbf{G}} \cdot name(\mathrm{Sig}) \notin signature\text{-}names(\mathbf{G}, \overline{\mathbf{L}})$
- 2. If Sig has a return type, then it must be in  $legal-types(\overline{L})$
- 3. The argument names in Sig are all distinct
- 4. The argument types of Sig are all in  $legal-types(\overline{L})$

#### Adding an ordinary method signature

A new ordinary method signature gets added to a class and all its descendants, which can later decide to inherit or override the eventual implementation.

$$\begin{split} \overline{L} &= \overline{L_1} + \overline{L_2} \\ class-names(\overline{L_2}) &= G + descendants(G,\overline{L}) \\ name(\operatorname{Sig}) \notin \operatorname{CLASS-NAMESPACE} \\ valid-signature(\operatorname{Sig}, class-names(\overline{L_2}), \overline{L}) \\ \overline{L_2} &= +_{i \in 1..n} \text{ class } \operatorname{H_i} \operatorname{Inh_i} \operatorname{Def_i} \text{ feature } \overline{\operatorname{M_i}} \overline{\operatorname{F_i}} \text{ end} \\ \overline{L'_2} &= +_{i \in 1..n} \text{ class } \operatorname{H_i} \operatorname{Inh_i} \operatorname{Def_i} \text{ feature } \overline{\operatorname{M_i}} + \operatorname{Sig} \overline{\operatorname{F_i}} \text{ end} \\ \overline{L} \rightsquigarrow \overline{L_1} + \overline{L'_2} \end{split}$$

#### Method manipulation

All remaining rules manipulate methods, and are based on the following rule:

 $\overline{\mathbf{L}} = \overline{\mathbf{L}_{1}} + \mathbf{L}$   $\mathbf{L} = \mathbf{class} \ \mathbf{G} \ \mathbf{inherit} \ \mathbf{H} \ \mathbf{Def} \ \mathbf{feature} \ \overline{\mathbf{M}} \ \overline{\mathbf{F}} \ \mathbf{end}$   $\Gamma = specs(\overline{\mathbf{L}})$   $\underline{\Delta} = apf(\mathbf{L})$   $\overline{\mathbf{M}} = \overline{\mathbf{M}_{1}} + \mathbf{M}$   $[\text{Hole}_{a}]$   $\overline{\mathbf{M'}} = \overline{\mathbf{M}_{1}} + \mathbf{M'}$ 

 $\overline{L} \rightsquigarrow \overline{L_1} + {\bf class} \; G \; {\bf inherit} \; H \; Def \; {\bf feature} \; \overline{M'} \; \overline{F} \; {\bf end}$ 

#### Adding a dynamic specification

The rule for adding a dynamic specification to a method signature fills  $[Hole_a]$  as follows:

$$\begin{split} M &= m(Args) \ Rt \\ [ Hole_{B.s.} ] \\ M' &= m(Args) \ Rt \ Sd \end{split}$$

#### Adding a static specification

Static specifications are also related to data refinement, since they can state how an operation on abstract structures/concepts, as mentioned in the dynamic specification, is realised in more concrete terms. The rule for adding a static specification fills  $[Hole_a]$  with:

$$\begin{split} M &= \operatorname{Sig}\,\operatorname{Sd}\\ [\operatorname{Hole}_{D.d.}]\\ M' &= \operatorname{Sig}\,\operatorname{Sd}\,\operatorname{Ss} \end{split}$$

#### Adding a method body

The calculus supports the refinement of method bodies, so the initial body can well be a single spec statement, as this filling for  $[Hole_a]$  shows:

$$\begin{split} \mathbf{M} &= \mathrm{Sig} \; \mathrm{Sd} \; \mathrm{Ss} \\ \mathrm{Sig} &= \mathrm{m}(\mathrm{Args}) \; \mathrm{Rt} \\ \mathrm{Ss} &= \mathbf{static} \; \{\mathrm{S}\}_{-}\{\mathrm{T}\} \\ [ \operatorname{Hole}_{\mathrm{b}} ] \\ \overline{\mathrm{y}} &= \overline{\mathrm{x}} + names(\mathrm{Args}) \\ \mathrm{M}' &= \mathrm{Sig} \; \mathrm{Sd} \; \mathrm{Ss} \; \mathbf{do} \; \overline{\mathrm{x}}; \overline{\mathrm{y}}; \{\mathrm{S},\mathrm{T}\} \; \mathbf{end} \end{split}$$

The purpose of  $[Hole_b]$  is to constrain  $\overline{x}$ , the variable(s) that may be modified, appropriately. It has two fillings:

$$Rt = \epsilon$$
$$\overline{x} = \epsilon$$
$$Rt \neq \epsilon$$
$$\overline{x} = Result$$

#### Removing a method body

This gives the opportunity to implement the body in a different way, or to inherit a parent version. The filling for  $[Hole_a]$  is simple:

 $M = Sig Sd Ss \mathbf{do} s \mathbf{end}$ M' = Sig Sd Ss

This is an example of a rule that relaxes the top-down style of development.

#### Inheriting a method body

Inheriting a method body imposes the Inheritance proof obligation, as the filling for  $[Hole_a]$  shows:

$$\begin{split} \mathbf{M} &= \mathrm{Sig} \; \mathrm{Sd} \; \mathrm{Ss} \\ \mathrm{Sig} &= \mathrm{m}(\mathrm{Args}) \; \mathrm{Rt} \\ \mathrm{Ss} &= \mathbf{static} \; \mathrm{Spec}_{\mathrm{G}} \\ \Gamma(\mathrm{H}:\mathrm{m}) &= (\overline{\mathrm{u}}, \mathrm{Spec}_{\mathrm{H}}) \\ \Delta \vdash \mathrm{Spec}_{\mathrm{H}} \Longrightarrow \mathrm{Spec}_{\mathrm{G}} \\ \mathrm{M}' &= \mathbf{inherit} \; \mathrm{Sig} \; \mathrm{Sd} \; \mathrm{Ss} \end{split}$$

### Uninheriting a method body

The next filling for  $[Hole_a]$  can be used to override a method's implementation instead of inheriting it:

$$\begin{split} \mathbf{M} &= \mathbf{inherit} \ \mathrm{Sig} \ \mathrm{Sd} \ \mathrm{Ss} \\ \mathbf{M}' &= \mathrm{Sig} \ \mathrm{Sd} \ \mathrm{Ss} \end{split}$$

#### Statement refinement

This is where the result of freefinement is incorporated in the development calculus. It fills  $[Hole_a]$  with:

$$M = \text{Sig Sd Ss } \mathbf{do s end}$$
  

$$\Delta; \Gamma \vdash s \sqsubseteq s'$$
  

$$M' = \text{Sig Sd Ss } \mathbf{do s' end}$$

## 5.4 Related Work

In his work on refinement for the lambda calculus, Denney [20] treats types as rudimentary specifications and introduces a specification construct  $?_{\tau}$  for each type  $\tau$ . Conceptually,  $?_{\tau}$  corresponds to  $[\Gamma; \tau]$  where the context  $\Gamma$  is left implicit. For example, consider the term  $\lambda x : \sigma . ?_{\tau}$  in the context  $\Gamma$ . The  $?_{\tau}$  inside the term corresponds to  $[\Gamma, x : \sigma; \tau]$ . Denney also considers richer specifications for lambda terms in his PhD thesis [21]. This results in a more powerful refinement calculus in which specification constructs can contain logical assumptions.

The specification statement  $\overline{\mathbf{x}}$  : [P, Q] of Morgan [49] is analogous to  $\overline{\mathbf{x}}$ ; Var : {P, Q}, since there is no restriction on the variables that the statement may read. However, his specification statement is a total correctness specification, and the accompanying refinement calculus [50] establishes total correctness. Similar refinement calculi for total correctness were proposed by Back [1, 2], Morris [51] and Hehner [31]. The books [50, 2, 31] contain many examples of how correct programs can be constructed from specifications via refinement.

Leino and Manohar [43] consider the join of Morgan's specification statements  $\bar{\mathbf{x}}$  :  $[\mathbf{P}_1, \mathbf{Q}_1]$  and  $\bar{\mathbf{x}}$  :  $[\mathbf{P}_2, \mathbf{Q}_2]$ , and mention several of its uses. Freefinement adds explicit constructors for joins, and relies on the ability to join arbitrary terms from U in order to establish harmony.

Several systems in the literature provide refinement and correctness by construction for OO programs [4, 48, 6, 5, 55]. The calculus in Section 5.3 is rather minimal and not as elaborate as some published ones. Nevertheless, several characteristics set it apart from existing proposals:

- 1. It is faithful to the OO paradigm and popular OO languages. It accommodates references, shared mutable state, inheritance and dynamic dispatch.
- 2. It uses state-of-the-art mechanisms that facilitate modular and flexible reasoning, such as separation logic and abstract predicate families.
- 3. It is not postulated in isolation. The calculus has a clear relationship with an accepted system for verifying OO programs in a bottom-up way.

There is a relationship between observational equivalence of terms and the function Specs, because  $\models_{V_1-} Sat$  gives rise to a notion of observability from the specification point of view. In particular, two terms t and t' are observationally equivalent in this sense iff t ~ t', where

$$t \sim t' \stackrel{\text{def}}{=} \operatorname{Specs}(\{t\}) = \operatorname{Specs}(\{t'\})$$

It is trivial to check that  $\sim$  is an equivalence relation. If  $\models_{V_1-} Sat_-$  is wellbehaved, then  $t \sim t' \Leftrightarrow [t] = [t']$  (i.e.  $t \sim t' \Leftrightarrow t \equiv t'$ ) by Corollary B.1.9 in Appendix B and Theorem 5.16.1.

# CHAPTER 6

# CONCLUSION

The state of the art in reasoning about software has seen several recent advances. In particular, proof systems that are based on separation logic emerged to provide intuitive, flexible and modular reasoning for OO programs. Their success is due to the synergy of the following insights:

- Separation is a natural way to think about shared mutable data. It leads to intuitive and simple correctness proofs for code that manipulates references.
- Abstract predicate families befit reasoning about object-orientation. They are logical abstractions that encapsulate state and hide representation details. Subclasses can change the representation and still satisfy the inherited specifications of routines.
- Giving two specifications to each routine resolves the tension between saying in detail what the particular body achieves, and saying what the general idea of the routine is that all subclasses must respect. Using separation logic and apfs in these specifications yields elegant proofs for OO programs.

The existing work forms a solid foundation for further investigation and research, which can of course proceed in many directions. This thesis tackled the following shortcomings in previous work:

• The basic apf mechanism can be too restrictive. Code sometimes depends on how a particular class implements and/or relates these abstractions, or properties that a whole class hierarchy will fulfill. This is especially important in the presence of multiple inheritance, where a class can relate and combine several concepts in various ways.

- Separation logic verification excluded the executable contracts that are present in existing OO code. These contracts may be weak and/or perform side-effects. They nevertheless capture useful design information which can be exploited in reasoning.
- It is sometimes easier to write code that is correct in the first place instead of hacking away and trying to verify code afterwards. Supporting this requires calculi for correctness by construction and refinement.

All the proposed solutions fit in nicely with the existing work of Parkinson and others [57, 58]. In particular:

- Export and axiom clauses can specify properties of and relationships between apfs that hold for particular classes and whole class hierarchies. The information is first verified and then made available as ordinary assumptions to reason about the code. Together with other generalisations, export and axiom clauses allow flexible reasoning about multiple inheritance.
- Separation logic can be used to verify that executable contracts will always hold at runtime. Central to such reasoning is the new concept of relative purity, which embraces side-effects in executable contracts.
- Freefinement can automatically provide a refinement calculus that interoperates smoothly with the rules for statement verification. This refinement calculus fits into a larger calculus for correctness by construction, which complements the separation logic system for bottomup verification.

The contributions of this thesis thus advance the state of the art in the verification of existing OO programs, and in the development of new programs that are correct by design. The ultimate hope is that, in the long term, such techniques will encourage and enable engineers to produce software that is more reliable.

# Appendix A

# SEMANTICS OF THE PROOF SYSTEM

This appendix outlines the semantics and soundness proof of the verification system of chapter 3. Since our system is a conservative extension of Parkinson and Bierman's system, the presentation closely follows the one in [58]. The most interesting difference is the treatment of export and axiom information in the soundness proof of the program verification rule (Theorem A.4 below).

The semantics of the logical formula is defined in terms of a state  $\sigma$ , an interpretation of predicate symbols  $\mathcal{I}$ , and an interpretation of logical variables  $\mathcal{L}$ . The interpretation  $\mathcal{I}$  maps predicate names to their definitions, whereas a definition maps a list of arguments to a set of states:

$$\begin{array}{rcl} \mathcal{I} & : & \mathsf{Preds} \to (\mathsf{Vals}^* \to \mathcal{P}(\Sigma)) \\ \mathcal{L} & : & \mathsf{Vars} \to \mathsf{Vals} \end{array}$$

Predicates are defined in the standard way:

$$\sigma, \mathcal{I}, \mathcal{L} \models \operatorname{pred}(\overline{X}) \Leftrightarrow \sigma \in (\mathcal{I}(\operatorname{pred})(\mathcal{L}(\overline{X})))$$

**Definition A.1.**  $\mathcal{I} \models \Delta$  iff  $\sigma, \mathcal{I}, \mathcal{L} \models \Delta$  for all  $\sigma$  and  $\mathcal{L}$ .

Under mild syntactic restrictions on predicate definitions, obeyed in this thesis and detailed in [57], one can show that every set of disjoint predicate definitions is satisfiable:

**Lemma A.1.** For any set of definitions  $W_1, \ldots, W_m, D_1, \ldots, D_n$  where  $W_i$  has form  $w_i(\overline{x_i}) = Q_i$  and  $D_j$  is listed in class  $G_j$ , there exists an interpretation  $\mathcal{I}$ such that  $\mathcal{I} \models [\bigwedge_{i \in 1...m} \forall \overline{x_i} \cdot w_i(\overline{x_i}) \Leftrightarrow Q_i] \land [\bigwedge_{j \in 1...n} apf_{G_j}(D_j)]$  provided that no two distinct definitions in the set define the same predicate. The semantics of our proof system's judgements is defined next. We do not define the semantics of  $\vdash_e$  and  $\vdash_a$  explicitly, since we work with their premises (valid logical formulae whose existence is guaranteed) instead. For triples, the usual partial-correctness semantics for separation logic is used: if the precondition holds in the start state, then 1) the statement will not fault (access unallocated memory, for example), and 2) if the statement terminates, then the postcondition holds in the resulting state.

**Definition A.2.**  $\mathcal{I} \models_{n} \{P\}s\{Q\}$  iff whenever  $\sigma, \mathcal{I}, \mathcal{L} \models P$  then  $\forall m \leq n \cdot$ 

- 1.  $\sigma, s \longrightarrow^{m}$  fault does not hold, and
- 2. if  $\sigma, s \longrightarrow^{m} \sigma', skip$  then  $\sigma', \mathcal{I}, \mathcal{L} \models Q$

The index n deals with mutual recursion in method definitions.  $\mathcal{I} \models_n \Gamma$  means that all methods in  $\Gamma$  meet their specifications when executed for up to n steps.

**Definition A.3** (Method verification semantics). If m in G is non-abstract, let s denote its body.

- $$\begin{split} \mathcal{I}, \Gamma &\models_{0} G.m \mapsto (\overline{x}, \{P\}_{-}\{Q\}) \text{ always holds.} \\ \mathcal{I}, \Gamma &\models_{n+1} G.m \mapsto (\overline{x}, \{P\}_{-}\{Q\}) \text{ iff} \\ \mathcal{I} &\models_{n} \Gamma \Rightarrow \mathcal{I} \models_{n+1} \{P * \textbf{Current} : G\}s\{Q\} \\ \text{ if G is non-abstract and true otherwise.} \end{split}$$
- $\mathcal{I}, \Gamma \models_0 G::m \mapsto (\overline{x}, \{S\}, \{T\})$  always holds.
- $\mathcal{I}, \Gamma \models_{n+1} G :: m \mapsto (\overline{x}, \{S\}_{-}\{T\}) \text{ iff } \mathcal{I} \models_{n} \Gamma \Rightarrow \mathcal{I} \models_{n+1} \{S\}s\{T\}$
- $\mathcal{I} \models_{n} \Gamma$  iff  $\forall$ methodspec  $\in \Gamma \cdot \mathcal{I}, \Gamma \models_{n}$  methodspec

We next define the semantics of the statement judgement.

**Definition A.4.**  $\Delta$ ;  $\Gamma \models \{P\}s\{Q\}$  iff for all  $\mathcal{I}$  and n, if  $\mathcal{I} \models \Delta$  and  $\mathcal{I} \models_n \Gamma$ , then  $\mathcal{I} \models_{n+1} \{P\}s\{Q\}$ 

In other words, for all interpretations which satisfy the assumptions  $\Delta$ , if all methods in  $\Gamma$  meet their specifications for up to n steps, then s meets its specification for up to n + 1 steps.

The judgements are sound with respect to their semantics.

#### Lemma A.2.

- 1. If  $\Delta$ ;  $\Gamma \vdash_m \ldots m \ldots$ , then  $\forall \mathcal{I} \cdot \text{ if } \mathcal{I} \models \Delta$  then for all n and every spec of m we have  $\mathcal{I}, \Gamma \models_n$  spec
- 2. If  $\Delta$ ;  $\Gamma \vdash_s \{P\}s\{Q\}$  then  $\Delta$ ;  $\Gamma \models \{P\}s\{Q\}$

Whenever a judgement is derivable under weak assumptions, it can also be derived under stronger ones.

#### Lemma A.3.

- 1. If  $\Delta$ ;  $\Gamma \vdash_m \ldots m \ldots$  and  $\Delta' \Rightarrow \Delta$ , then  $\Delta'$ ;  $\Gamma \vdash_m \ldots m \ldots$
- 2. If  $\Delta$ ;  $\Gamma \vdash_s \{P\} s\{Q\}$  and  $\Delta' \Rightarrow \Delta$ , then  $\Delta'$ ;  $\Gamma \vdash_s \{P\} s\{Q\}$
- 3. If  $\Delta_{APF}, \Delta_E, \Delta_A; \Gamma \vdash_c L$  and  $\Delta' \Rightarrow \Delta_{APF}$ , then  $\Delta', \Delta_E, \Delta_A; \Gamma \vdash_c L$

Finally, here is the soundness statement and detailed proof sketch of the program verification rule.

**Theorem A.4.** If a program and its main body s can be proved with the program verification rule, then  $\forall \mathcal{I}, n \in \mathcal{I} \models_n \{\text{true}\} \text{s}\{\text{true}\}$ . *Proof.* 

- 1. The goal. We have to prove  $\forall \mathcal{I}, \mathbf{n} \cdot \mathcal{I} \models_{\mathbf{n}} \{\text{true}\} \mathbf{s} \{\text{true}\}, \text{ which abbre$  $viates } \forall \mathcal{I}, \mathbf{n} \cdot \text{ whenever } \sigma, \mathcal{I}, \mathcal{L} \models \text{true, then } \forall \mathbf{m} \leq \mathbf{n} \cdot \mathbf{1}) \sigma, \mathbf{s} \longrightarrow^{\mathbf{m}} \mathsf{fault}$ does not hold, and 2) if  $\sigma, \mathbf{s} \longrightarrow^{\mathbf{m}} \sigma', \mathbf{skip}$  then  $\sigma', \mathcal{I}, \mathcal{L} \models \text{true. This}$ can be simplified to  $\forall \mathbf{n} \cdot \sigma, \mathbf{s} \longrightarrow^{\mathbf{n}} \mathsf{fault}$  does not hold.
- 2. Strengthened assumptions. Let  $\Delta_T \stackrel{\text{def}}{=} \bigwedge_{i \in 1..t} apf(L_i)$ , where  $L_1 \ldots L_t$ are all classes in the program. By Lemma A.3, we can strengthen the assumptions under which all classes and the main body have been verified. For every class  $L_i$ , we have  $\Delta_T$ ,  $\Delta_E$ ,  $\Delta_A$ ;  $\Gamma \vdash_c L_i$ , and  $\Delta_T \land$  $\Delta_E \land \Delta_A$ ;  $\Gamma \vdash_s \{\text{true}\}$ s{true} also holds for the main body s.
- 3. The interpretation  $\mathcal{I}'$ . Since  $\Delta_T \wedge \Delta_E \wedge \Delta_A$ ;  $\Gamma \vdash_s \{\text{true}\} s\{\text{true}\}$ , Lemma A.2 guarantees  $\Delta_T \wedge \Delta_E \wedge \Delta_A$ ;  $\Gamma \models \{\text{true}\} s\{\text{true}\}$ . This abbreviates  $\forall \mathcal{I}, \mathbf{n} \cdot \text{ if } \mathcal{I} \models \Delta_T \wedge \Delta_E \wedge \Delta_A$  and  $\mathcal{I} \models_n \Gamma$ , then  $\mathcal{I} \models_{n+1} \{\text{true}\} s\{\text{true}\}$ , which can be simplified to  $\forall \mathcal{I}, \mathbf{n} \cdot \text{ if } \mathcal{I} \models \Delta_T \wedge \Delta_E \wedge \Delta_A$ and  $\mathcal{I} \models_n \Gamma$ , then  $\forall \mathbf{m} \leq \mathbf{n} + 1 \cdot \sigma, \mathbf{s} \longrightarrow^m$  fault does not hold. Now if we can find an  $\mathcal{I}'$  such that  $\mathcal{I}' \models \Delta_T \wedge \Delta_E \wedge \Delta_A$  and  $\forall \mathbf{n} \cdot \mathcal{I}' \models_n \Gamma$ , then we can instantiate  $\mathcal{I}$  to  $\mathcal{I}'$  in the formula and simplify to obtain  $\forall \mathbf{n} \cdot \sigma, \mathbf{s} \longrightarrow^n$  fault does not hold. Therefore  $\mathcal{I}'$  serves as a witness that  $\mathbf{s}$  will never fault, which is exactly our goal.

Let  $\mathcal{I}'$  be the interpretation whose existence is guaranteed by Lemma A.1 for all the where and define clauses in the program. Clearly  $\mathcal{I}' \models \Delta_T$ . We next prove  $\mathcal{I}' \models \Delta_E$  and then  $\mathcal{I}' \models \Delta_A$ .

- 4. Satisfiability of  $\Delta_E$ . Consider an arbitrary export clause E = P where  $\{w_1(\overline{x_1}) = Q_1; \ldots; w_n(\overline{x_n}) = Q_n\}$  in class L. Since  $apf(L) \vdash_e E$ , we know  $[apf(L) \land (\bigwedge_{i \in 1..n} \forall \overline{x_i} \cdot w_i(\overline{x_i}) \Leftrightarrow Q_i)] \Rightarrow P$ . The interpretation  $\mathcal{I}'$  satisfies the antecedent, so we also have  $\mathcal{I}' \models P$ . Therefore  $\mathcal{I}' \models \Delta_E$ , and  $\mathcal{I}' \models \Delta_T \land \Delta_E$ .
- 5. Satisfiability of  $\Delta_A$ . We prove this by induction. If class G has children  $H_1 \ldots H_k$ , let  $level(G) \stackrel{\text{def}}{=} 1 + \max(0, level(H_1), \ldots, level(H_k))$ . Furthermore,  $P(n) \stackrel{\text{def}}{=} \forall G$  in the program such that  $level(G) \leq n$  and for all axiom clauses a: P in the listing of G,  $(\Delta_T \land \Delta_E) \Rightarrow axiominfo(G, a: P)$ .
  - Base case. Consider an arbitrary class G with  $level(G) \leq 1$  and an axiom clause a: P appearing in it. G has no subclasses, and
    - (a) If G is abstract, there are no objects with dynamic type G or a subtype thereof, thus axiominfo(G, a: P) holds vacuously and  $\Delta_T \wedge \Delta_E$  implies it.
    - (b) If G is non-abstract, then the only objects whose dynamic type is a subtype of G are direct instances of G. Since  $(\Delta_T \land \Delta_E \land \mathbf{Current} : \mathbf{G}) \Rightarrow \mathbf{P}$  by the Implication premise, we therefore also know  $(\Delta_T \land \Delta_E) \Rightarrow axiominfo(\mathbf{G}, \mathbf{a}: \mathbf{P}).$

Thus P(1) holds.

• Step case. Suppose P(n) holds. Now consider a class G at level n+1 with axiom clause a: P. Every child H of G must list a, say a: Q. By the induction hypothesis we know  $(\Delta_T \wedge \Delta_E) \Rightarrow$  axiominfo(H, a: Q), and by the Parent Consistency premise of a: Q we know  $(\Delta_T \wedge \Delta_E \wedge Q) \Rightarrow$  P. Therefore  $(\Delta_T \wedge \Delta_E) \Rightarrow$  axiominfo(H, a: P). We have  $(\Delta_T \wedge \Delta_E) \Rightarrow$  axiominfo(G, a: P) if G is abstract, and the same holds if G is non-abstract since the Implication premise of a: P guarantees  $(\Delta_T \wedge \Delta_E \wedge \mathbf{Current} : G) \Rightarrow$  P. Thus P(n+1) holds.

So  $\mathcal{I}' \models \Delta_T \land \Delta_E \land \Delta_A$ .

6. Wrapping up. We still have to prove  $\forall n \cdot \mathcal{I}' \models_n \Gamma$ . Let m be an arbitrary method in the program. Since  $\Delta_T \wedge \Delta_E \wedge \Delta_A$ ;  $\Gamma \vdash_m m$ , by Lemma A.2 we know for all n and every spec of m that  $\mathcal{I}'$ ,  $\Gamma \models_n$  spec. Thus  $\forall n \cdot \forall method spec \in \Gamma \cdot \mathcal{I}', \Gamma \models_n method spec$ , in other words  $\forall n \cdot \mathcal{I}' \models_n \Gamma$ .

# Appendix B

# ANTITONE GALOIS CONNECTIONS

Lemma 5.2 established that an antitone Galois connection exists between the functions Specs and Terms:

$$X \subseteq Terms(Y) \Leftrightarrow Y \subseteq Specs(X) \tag{(*)}$$

Theorems derived from this equivalence come in pairs because of the symmetry between Specs and Terms. Here are a few well-known ones together with their proofs:

#### Corollary B.1.

B.1.1.  $X \subseteq Terms(Specs(X))$ 

B.1.2.  $Y \subseteq Specs(Terms(Y))$ 

- B.1.3.  $X \subseteq X' \Rightarrow \operatorname{Specs}(X) \supseteq \operatorname{Specs}(X')$
- B.1.4.  $Y \subseteq Y' \Rightarrow Terms(Y) \supseteq Terms(Y')$
- B.1.5.  $X \subseteq X' \Rightarrow Terms(Specs(X)) \subseteq Terms(Specs(X'))$
- B.1.6.  $Y \subseteq Y' \Rightarrow Specs(Terms(Y)) \subseteq Specs(Terms(Y'))$

B.1.7. Specs(Terms(Specs(X))) = Specs(X)

B.1.8. Terms(Specs(Terms(Y))) = Terms(Y)

B.1.9.  $\operatorname{Specs}(X) \subseteq \operatorname{Specs}(X') \Leftrightarrow \operatorname{Terms}(\operatorname{Specs}(X)) \supseteq \operatorname{Terms}(\operatorname{Specs}(X'))$ 

B.1.10.  $\operatorname{Terms}(Y) \subseteq \operatorname{Terms}(Y') \Leftrightarrow \operatorname{Specs}(\operatorname{Terms}(Y)) \supseteq \operatorname{Specs}(\operatorname{Terms}(Y'))$ 

B.1.11.  $\operatorname{Specs}(X \cup X') = \operatorname{Specs}(X) \cap \operatorname{Specs}(X')$ 

B.1.12. Terms $(Y \cup Y') = Terms(Y) \cap Terms(Y')$ 

Proof.

- B.1.1. In (\*), instantiate Y with Specs(X).
- B.1.3.  $X \subseteq$  "Assumption"  $X' \subseteq$  "B.1.1" Terms(Specs(X')). In (\*), instantiate Y with Specs(X').
- B.1.5. If  $X \subseteq X'$ , then  $\text{Specs}(X) \supseteq \text{Specs}(X')$  holds by B.1.3. The result follows from B.1.4.
- B.1.7. From B.1.1 and B.1.3 follows  $\text{Specs}(X) \supseteq \text{Specs}(\text{Terms}(\text{Specs}(X)))$ . Instantiating Y with Specs(X) in B.1.2 yields  $\text{Specs}(X) \subseteq \text{Specs}(\text{Terms}(\text{Specs}(X)))$ .
- B.1.9. ⇒ holds by B.1.4. From  $\text{Terms}(\text{Specs}(X)) \supseteq \text{Terms}(\text{Specs}(X'))$ and B.1.3,  $\text{Specs}(\text{Terms}(\text{Specs}(X))) \subseteq \text{Specs}(\text{Terms}(\text{Specs}(X')))$ .  $\text{Specs}(X) \subseteq \text{Specs}(X')$  by B.1.7.
- B.1.11. Proof by indirect equality. For arbitrary Y:

$$Y \subseteq \operatorname{Specs}(X \cup X')$$
  

$$\Leftrightarrow \{By (*)\}$$

$$X \cup X' \subseteq \operatorname{Terms}(Y)$$
  

$$\Leftrightarrow \{\operatorname{Set theory}\}$$

$$X \subseteq \operatorname{Terms}(Y) \land X' \subseteq \operatorname{Terms}(Y)$$
  

$$\Leftrightarrow \{By (*)\}$$

$$Y \subseteq \operatorname{Specs}(X) \land Y \subseteq \operatorname{Specs}(X')$$
  

$$\Leftrightarrow \{\operatorname{Set theory}\}$$

$$Y \subseteq \operatorname{Specs}(X) \cap \operatorname{Specs}(X') \square$$

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